The Mu3e ultra-low-mass tracker

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Mu3e is an experiment to search for

$$\mu^+
ightarrow e^+ e^- e^+$$

A very rare decay.



We're in an unusual regime, hence allow for some physics background.



 $\mu \rightarrow \textit{eee}$ in the standard model.





 $\mu \rightarrow eee$ in the standard model.

$$\label{eq:SM:} \begin{split} \text{SM:} &< 1 \times 10^{-54} \\ \text{The suppression comes from the} \\ \text{neutrino masses.} \end{split}$$

Current best limit: $< 1 \times 10^{-12}$ (SINDRUM 1988)

Alternative models predict BR within reach of Mu3e ($<1\times10^{-16}).$





 $\begin{array}{l} \mbox{Signal} \\ \mbox{SM:} < 1 \times 10^{-54} \end{array}$





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 $\sum p_i = 0$





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 $\sum_{m_{inv}} p_i = 0$ $m_{inv} = m_\mu$





 $\begin{array}{l} \mbox{Signal} \\ \mbox{SM:} < 1 \times 10^{-54} \end{array}$

 $\sum_{\substack{i \neq j \\ p_i = 0}} p_i = 0$ $m_{inv} = m_{\mu}$ $t_i = t_j \quad \forall i, j$





 $\begin{array}{l} \mbox{Signal} \\ \mbox{SM:} < 1 \times 10^{-54} \end{array}$

 $\sum_{\substack{ p_i = 0 \\ m_{inv} = m_{\mu} \\ t_i = t_j \quad \forall i, j \\ \text{common vertex} }$



 $\begin{array}{l} \mbox{Signal} \\ \mbox{SM:} < 1 \times 10^{-54} \end{array}$

 $\sum_{\substack{ p_i = 0 \\ m_{inv} = m_{\mu} \\ t_i = t_j \quad \forall i, j \\ \text{common vertex} }$

Radiative decay SM: 3.4×10^{-5}

e

ν

 $\sum p_i
eq 0$ $m_{ ext{inv}} < m_\mu$ $t_i = t_j$ common vertex













What governs the detector design?

Hence we need:

- Precise **tracking** (vertexing and momentum) \Rightarrow pixels
- Good timing (coincidence, event separation) \Rightarrow scintillators
- Minimal material budget design (background suppression, multiple scattering)
 ⇒ solutions...

Note: Muons are stopped on a target. No bunch structure.

Rad-hard electronics is not that important.





Phase-I configuration:







Phase-I configuration:



- ▶ High rate: 10⁸ muon stops on target per second
- ► Time resolution (pixels): 20 ns
- Vertex resolution: about 200 μm
- Momentum resolution: about 0.5 MeV
- All inside a cryogenic 1 T magnet, warm bore I.D. 1 m



Let's focus on the pixels. Monte-Carlo studies led to the following geometry:



Identical copies of layers 3/4 will extend the detector in z to extend coverage for recoiling tracks.





 $\mathsf{Ok},$ we got the geometry. But what about the material budget of the pixel layers?

Let's put this into perspective:

Experiment	Ref.	x/X_0 per layer [%]
ATLAS IBL	[1]	1.9
CMS Phase I	[2]	1.1
ALICE upgrade	[3]	0.3
STAR	[4]	0.4
Belle-II IBL	[5]	0.2
Mu3e		0.1



Coming back to the solutions I will talk about

- Low-material mechanics based on polyimide film
- ► Aluminium flexes to reduce Z
- Interposers to save space
- **Stuffing all together** to make it work



Mu3e mechanics – Mounting structure





Top view of experiment frame, diameter $pprox 1\,{
m m}.$

Mu3e mechanics – Mounting structure

It needs to take the load:



Deformation 100 \times exagerated. Matches measurements on a mock-up well.



Pixel modules





We use a monolithic, depleted pixel sensor, thinned to 50 μm . Active chip size 20 \times 20 mm², power dissipation \leq 250 mW/cm² (confirmed by measurements):

Ivan Perić, Nucl.Instrum.Meth. A582 (2007) 876-885

Layer	Unit	units/layer	ladders/unit	chips/ladder	# chips	#links
1	half shell					
2	half shell					
3	module					
4	module					
Total						



Layer	Unit	units/layer	ladders/unit	chips/ladder	# chips	#links
1	half shell	2				
2	half shell	2				
3	module	6				
4	module	7				
Total						



Layer	Unit	units/layer	ladders/unit	chips/ladder	# chips	#links
1	half shell	2	4			
2	half shell	2	5			
3	module	6	4			
4	module	7	4			
Total						



Layer	Unit	units/layer	ladders/unit	chips/ladder	# chips	#links
1	half shell	2	4	6		
2	half shell	2	5	6		
3	module	6	4	17		
4	module	7	4	18		
Total						



Layer	Unit	units/layer	ladders/unit	chips/ladder	#chips	#links
1	half shell	2	4	6	48	
2	half shell	2	5	6	60	
3	module	6	4	17	408	
4	module	7	4	18	504	
Total					1020	



Layer	Unit	units/layer	ladders/unit	chips/ladder	#chips	# links
1	half shell	2	4	6	48	144
2	half shell	2	5	6	60	180
3	module	6	4	17	408	408
4	module	7	4	18	504	504
Total					1020	1236



Central station plus two recurl stations:

Layer	Unit	units/layer	ladders/unit	chips/ladder	# chips	# links
1	half shell	2	4	6	48	144
2	half shell	2	5	6	60	180
3	module	6	4	17	1224	1224
4	module	7	4	18	1512	1512
Total					2844	3060

One chip dissipates about $1\,\text{W}$ \Rightarrow about $3\,\text{kW}$ of heat.



Mu3e mechanics – Layers 1/2

Modules layer 2 design (1 is similar, one facet less)



Inner modules have ladders of 6 chips each. Observe: No V-folds here.

Mu3e mechanics – Layers 1/2

Modules layer 2 design (1 is similar, one facet less)





Exploded view of same part.

Mu3e mechanics – Layers 1/2

More on this: see poster by Simon Muley.





Shown: One one module per layer inserted.








Radiation length: $pprox 0.1\% \, x/X_0$



Let's have a look at gluing V-folds on ladders. This is the outcome:





1.

Fill reservoir with 50 µm layer of glue





2. Dip stamp into reservoir





3. Place bare ladder on jig





4. Apply glue





5. Place V-folds on jig





5. Place V-folds on jig





6. Pick V-folds





7. Lower V-folds down





8.

Place curing weight Let glue cure





Setup for gluing





Closeup view. Here, dispensing robot was used as an alternative approach.





Mock-up ladders made of tape heaters

Made of aluminium-polyimide laminate ($25 \mu m$ thickness each), laser structured meander for heating and temperature measurement.





Mock-up ladders made of tape heaters









Interposer Samtec Z-Ray

Pitch: 0.8 mm

Model	Compressed height
ZA8H	0.3 mm
ZA8	1 mm

Industry standard component, cost 5–10 \in a piece.

Allows use of flexes instead of cables.



The nice feature: many connections on a small area.



Mag

One stave has 9 data links. Current limit per pin: 0.8 A. This will work.

For the HDI, we go for aluminium/polyimide:



Mag

Made by LTU (Kharkiv, Ukraine), used in e.g. ALICE pixels for power strips.

Process steps:

- 1. Starts with aluminium foil, thickness of $12.5\,\mu\text{m}$ or $25\,\mu\text{m}$
- 2. Create polyimide layer: spinning of primer, drying and polimerisation
- 3. Photolithographic etching of aluminium traces
- 4. Etching of polyimide
- 5. Glueing of layers to form a stack
- 6. Additional polyimide foil for added dielectric, if needed
- 7. Tab bonding. Bonds aluminium traces directly, no wire.



Flex when cut out from panel:



M3 D



Some bond examples under a microscope:

Top: Bond to PCB PCB is visible on the bottom edge of the image

Center: Connecting layers (via) The visible misalignment is a shrinking effect from polymerisation at 350 $^{\circ}$ C. One trace needs to be wider to absorb the tolerances.

Bottom: Vias for a bus



Bit error rate tests:

Rate Gb/s	Line	BER (95% C.L.)
1.25	all	$\leq 5.5 imes 10^{-13}$
2.5	all	$\leq 5.9 imes 10^{-13}$
3.2	short ones	$\leq 4.1 imes 10^{-13}$
	18 cm	fail
4.0	all	fail









Sketch of supplies: electrical connectivity and cooling circuits.





A busy sketch showing all connections Line styles: solid = He, dotted = water, dashed = power/signal





Three helium circuit types Δp limit to be determined on mock-up, estimated to be O(1 mbar)



Constraints;

- Very restricted space inside magnet (d = 1 m, $l \approx 2.8 \text{ m}$)
- \blacktriangleright Magnetic field of 1 T \Rightarrow solenoid valves or motors won't work inside
- Helium atmosphere everywhere
- All material must be non-magnetic
- Openings in magnet shield doors limit space for feed-throughs



The V-folds not only give mechanical strength but are used for cooling with gaseous Helium. Simulation results:



Volume	He flow speed m/s	$\begin{array}{c} {\sf Cross-section} \\ {\sf cm}^2 \end{array}$	Volume cm ³	Occurence times	Volumetric flow m ³ /min
Gap L1/L2	10	12	148	1	0.72
Gap SciFi/L3	10	39	1320	1	2.3
Gap Tile/L3	10	34	1150	2	4.2
V-folds L3	20	3.3	114	3	1.2
Gap L3/L4	10	60	2185	3	10.8
V-folds L4	20	3.9	141	3	1.4
Global flow	0.5	7600	912000	1	23
Total		7750			43

Required target flows per helium circuit:

 \Rightarrow Volumes differ up to factor ≈ 20





- Cooling simulations have been repeated
- Two power dissipation cases:
 - 400 mW/cm² (pessimistic)
 - 250 mW/cm² (realistic)
- The two cases scale linearly, hence show 400 mW/cm^2 only
- > Also pressure drop across ducts and detector volumes have been simulated



Simulated flows:

Volume	Flow speed (m/s)	Direction
Gap L1/L2	10	$\text{DS} \rightarrow \text{US}$
Gap SciFi/L3 V-folds L3/L4 Gap L3/L4	5 20 10	$\begin{array}{c} US \to DS \\ DS \to US \\ DS \to US \end{array}$
Global	0.5	$US\toDS$

Following pages show temperature of silicon as heat maps. Other parts removed for clarity.





L1/2




L3/4







Average temperature of ladder translated into thermal expansion of polyimide:



Note: This is a result calling for more studies. Plan is to optimise gas inlets. Interplay with duct layout hence non-trivial.

Expected **pressure drops** in the circuits from same simulation:

Circuit	Duct IN	Flange	Detector	Flange	Duct OUT
Gap L1/L2	25	7	< 1	9	24
Gap SciFi/L3	6	< 1	3	28	_
V-folds L3	25-50	80–90	10-20	50-70	25–25
Gap L3/L4	8	25	< 1	11	_
V-folds L4	30–50	60–70	10-20	50–70	20

All pressure values in mbar. If range given: min/max observed per compartment. Some gaps vent to global volume.

This seems manageable and the detector shouldn't "pump up".































Conclusions and outlook

▶ We have a challenging but exciting detector project:

- Thinned silicon pixel, monolithic, self-triggered pixels
- Scintillating detectors for timing (not shown here, sorry...)
- Ultra-light weight mechanics
- Gaseous helium cooling
- Readout using Aluminium flexes and micro-twisted pair cables (not shown again...)
- Plans:
 - Building a full thermo-mechanical mock-up using silicon chip heaters
 - Implementing module fabrication workflows
 - Further electrical integration



References

- [1] ATL-INDET-PROC-2015-001
- [2] CERN-LHCC-2012-016, CMS-TDR-11
- [3] arXiv:1211.4494v1
- [4] G. Contin, talk at PIXEL2016
- [5] C. Koffmane, talk at PIXEL2016



ENCORE













- The following slides show a step-by-step mounting procedure
- > Order of sequence has certain freedoms. This is one possible choice.





We start with the two beampipes.

Already mounted: copper bars and helium ducts.

Two beampipes still allowed to be mechanically independent for first few steps.



ž

Outermost endrings



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Tile detectors







Second endrings for recoil stations





Endrings for central station





Mounting frame for SciFi





Endrings for L1/2

Note: Until now, no mechanical connection between US/DS.





Two half-shells for L1

Note: From now on everything is in experiment frame and beampipes must be aligned.



and same for L2





Insert SciFi modules





Insert pixel L3 modules





and L4





L3 of US recoil station






L3 of DS recoil station







L4

Now detector is in principle ready for insertion into magnet.





And finally: insert target from DS



As you know, PDG gives you the following formula:

$$heta = rac{13.6\,{
m MeV}}{eta cp}\,z\,\sqrt{x/X_0}\,[1+0.038\ln{(x/X_0)}]$$

Allow me to illustrate that a bit...



Multiple scattering at LHC energies for an electron:



Thinner detector layers is a nice-to-have, but $1\% x/X_0$ is ok.



Multiple scattering at Mu3e energies for an electron:





Ooops. Did we loose the curve...?

Multiple scattering at Mu3e energies for an electron:



 $40\times$ scale increase. At low energies, matter matters.





The stage is a simple toy tracker. Particle enters from the left.





Let's take pixel layers with $x/X_0 = 1\%$ each. Observe the substantial scattering at such low momenta.

Note: This sketch is to scale. Per-layer contribution added in quadrature.



Reducing x/X_0 to 0.1% helps.



To measure the momentum, a *B*-field is present. Hence tracks are helices.

How can we take this to our advantage?





Assume a particle in a B-field scatters at some detector layer (blue)





Let it scatter to the right...





... or to the left...





Observe the magic point where the scattering effect cancels.



It is after a half turn.



Choose radii wisely for best preformance.



Ok, now you know our basic ingredients to do our job:

- Optimise the radii of the detector
- Minimise the material per detector layer



Ok, now you know our basic ingredients to do our job:

- Optimise the radii of the detector
- Minimise the material per detector layer
 - ► Pixel sensor: MUPIX
 - Mechanics
 - Readout
 - Cooling



- We've prepared single silicon heater assemblies.
- Consists of heater (sputtered aluminium on silicon, thinned down to 50 µm) and a flex HDI (2 layers Al/polyimide). Veryclose to final design.
- Heater designed to dissipate up to 400 mW/cm².
- Has a 1000 Ω RTD on it
- Next set of slides: graph paper viewed reflected on back of silicon heater









$$\vartheta = 30 \,^{\circ}\text{C}$$





$$\vartheta = 40 \,^{\circ}\text{C}$$





$$\vartheta = 50 \,^{\circ}\text{C}$$





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$$\vartheta = 60 \,^{\circ}\text{C}$$





$$\vartheta = 70 \,^{\circ}\text{C}$$







Before you get too shocked:

- About the magnitude expected from CTE mismatch polyimide-silicon.
- Glue pattern has not been optimised yet.
- Will calibrate finite element simulations and optimise glue pattern in simulation.
- If detector is in thermal equilibrium and stable over time, track-based alignment can handle this.
- Thermal mass is small, hence equilibrium will be reached fast.



Take endpiece. Glue gas cavity lid.







Pre-fold flexprint, glue it to endpiece. Let glue cure.

Note: Flexprint has nothing mounted on it. All you see are traces and the BGA-arrays.

Gluing must be to precision $<100\,\mu m$ w.r.t. endpiece reference.





Turn it and . . .





... place it on mounting jig (not shown). Secure with two screws (big holes).

Apply glue, place first ladder.

Remember: Ladder has a small flex at both ends with BGA array for interposer.



×.



Repeat for second ladder...







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Mag



Place interposer (4 times).


Module assembly procedure

×.



Fold in flexprint.



Module assembly procedure



Add compression bar (slides in from left under hair-pin loop)



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Module assembly procedure



Secure everything with screws, 8 in total.

Seal V-folds from back side.



 $\mu \rightarrow \textit{eee}$ in the standard model.



 $\mu \rightarrow \textit{eee}$ in the standard model.

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Current best limit: $< 1 \times 10^{-12}$ (SINDRUM 1988)

Alternative models predict BR within reach of Mu3e ($<1\times10^{-16}).$



The standard Michel decay

SM: $\approx 99.997\%$





The standard Michel decay

The radiative SM decay

SM: \approx 99.997%

SM: (3.4 \pm 0.4) imes 10⁻⁵





Bhabha scattering



A well-known process.

Sources of particles:

- ▶ e⁺: Michel decay, beam impurities
- ▶ *e*⁻: Material in experiment



Bhabha scattering



A well-known process.

Sources of particles:

- e^+ : Michel decay, beam impurities
- ▶ *e*⁻: Material in experiment

Mitigation: Reduce material, improve resolution.



Mu3e physics



A muon from the beam stops on the target...





... and it makes a Michel decay.





The positron hits an electron...





... undergoing Bhabha scattering and we have an electron track.





If another muon stops nearby...





... and makes a Michel decay...





... you get the besic signature of our signal event.





The $M\mathrm{UPIX}$ 7 chip is a DMAPS chip and consists of

- Active pixel matrix
- Mirror pixel in periphery
- State machine
- Plus support circuitry (VCO, PLL, etc., not shown)





The analog cell has

- a reverse biased sensor ($\approx -85 \text{ V}$)
- a charge sensitive amplifier
- a source follower to drive...





the **transmission line** to the corresponding partner cell in the periphery.





In the **partner cell**, the transition from analog to digital happens:

- ► an amplifier
- a comparator
- tuning capabilities

This separation protects the analog cell from digital crosstalk.





All this results in a **non-shuttered**, **self-triggered** monolithic pixel chip.

Upon a hit...





... the charge sensitive amplifier sends a pulse proportional to the charge...





... across the transmission line ...

BTW: every pixel has its own transmission line





... and the comparator in the periphery creates a digital signal, if above threshold.





The state machine provides clock for a counter...





... in order to create a timestamp.





The data (pixel location, timestamp) goes through the serialiser...





... and all the data is transmitted to the data stream at 1.25 Gbit/s.





This design choice results in a

- \blacktriangleright pixel unit cell, that is always sensitive \Rightarrow ,,self-triggered " and non-shuttered
- time-stamp allows event formation

This is tailored to our needs because we have

- no bunch structure
- no possibility for a reliable trigger with almost no material
- \blacktriangleright monolithic chips can be thinned to about 50 μm



Time domain reflectometry of LTU HDI differential lines





Several generations of MuPix chips realised:

Version	Year	Main features		
MuPix1/2	2011/12	Analog prototype chips		
MuPix3	2013	First digital readout		
MuPix4	2013	Working digital readout and timestamping		
MuPix6	2014	Readout bugs fixed, double-staged preamplifier		
MuPix7	2014	Fast serial readout (1.25 Gbit/s), internal state ma- chine, internal clock generation		

MuPix3–7 have an active area of $3.2 \times 3.2 \text{ mm}^2$, chip size is $\approx 3.5 \times 4 \text{ mm}^2$. MuPix7 pixel size: $103 \times 80 \,\mu\text{m}^2$, making up a 32×40 matrix.



- ▶ The concepts we have and they look ok
- The detector geometry is optimised for our case
- But: can we reach the permille in radiation length?
- And what abozt the performance of the detector?



Here is a material budget drilldown for pixel layers:

	thickness [µm]	Layer 1-2 X/X_0	thickness [µm]	Layer 3-4 X/X_0
MuPix Si	45	$0.48 imes 10^{-3}$	45	$0.48 imes 10^{-3}$
MuPix AI	5	$0.06 imes10^{-3}$	5	$0.06 imes 10^{-3}$
HDI polyimide & glue	45	$0.18 imes10^{-3}$	45	$0.18 imes10^{-3}$
HDI AI	28	$0.31 imes10^{-3}$	28	$0.31 imes10^{-3}$
polyimide support	25	$0.09 imes10^{-3}$	pprox 30	$0.10 imes10^{-3}$
adhesives	10	$0.03 imes10^{-3}$	10	$0.03 imes10^{-3}$
total	158	$1.15 imes10^{-3}$	163	$1.16 imes10^{-3}$

We miss the target a bit. But after we built that detector we will elarn where we can optimise more.



Our simulation lets us expect good performance.



 x/X_0 : Fraction of radiation length

Defined by

- mean distance an electron looses 1/e of its energy by bremsstrahlung
- > 7/9 of the mean free path for pair production

Used in estimating the expected scattering angle of a particle traversing matter:

$$heta = rac{13.6\,{
m MeV}}{eta c p} \, z \, \sqrt{x/X_0} \left[1 + 0.038 \ln{(x/X_0)}
ight]$$

Take-away: Material with larger X_0 create less scattering. Goes with charge number Z of nucleus.



Multiple scattering at Mu3e energies for an electron:



Tracking detectors in this energy regime need to be as thin as possible.






Ivan Perić, Nucl.Instrum.Meth. A582 (2007) 876-885

> Analog pixel electronics floats on sensor diode: monolithic design





Ivan Perić, Nucl.Instrum.Meth. A582 (2007) 876-885

- > Analog pixel electronics floats on sensor diode: monolithic design
- ► Industry standard HV CMOS process allows for E-field across diode ⇒ depletion zone of about 15 µm





Ivan Perić, Nucl.Instrum.Meth. A582 (2007) 876-885

The MUPIX chip is such a **depleted MAPS**, thinned to $50 \,\mu\text{m} \approx 0.05\% \, x/X_0$

