



Summary of the 1st FOM Meeting

Held on Tuesday 20th February 2018

Agenda <https://indico.cern.ch/event/697538/>

- 1. Welcome to FOM 2018*
- 2. Status of the machines*
- 3. Linac2 YETS activities report*
- 4. PSB YETS activities report*
- 5. PS YETS activities report*
- 6. Schedule updates*
- 7. AOB*

1. Welcome to FOM 2018

B. Mikulec chaired the meeting.

The list of presence can be found in [Annex 0](#).

The [FOM representative list](#) was updated with the information received so far.

2. Status of the machines

Linac2 & Linac3

D. Kuchler reported the status of the linacs.

The Linac2 source was restarted and the first beam down the linac is expected in the afternoon.

The Linac3 source will be restarted on Monday next week. There are still some BI equipment missing.

Linac4

JB. Lallement reported the status of the Linac4.

The Linac4 is being restarted. Yesterday, a first beam was accelerated in the RFQ.

CLEAR

A. Curcio reported the status of CLEAR ([Annex 1](#)).



The installation of beam diagnostics and of forthcoming experiments was completed. The DSO test went fine and the beam commissioning is starting today.

3. Linac2 YETS activities report

A. Berjillos reported on the Linac2 shutdown activities ([Annex 2](#)).

Main activities and usual maintenance were completed, especially the installation of two cable trays and AC cables for the LT.BHZ20 magnet power converters that was moved to the Linac2 equipment gallery. The tunnel was closed on January 31st and the beam permit was signed quite in advance (thanks to the new procedure). As already mentioned in the machine status, the 50 MeV beam will be restarted in the afternoon and available to the PSB on the 2nd March.

4. PSB YETS activities report

D. Hay reported on the PS Booster shutdown activities ([Annex 3](#)).

The list of activities that took place in the tunnel and in the surface building was given and most of them were completed. The installation of the auxiliary cables and power supplies for the BSW power converters located in the BR2 as well as the completion of the cabling needed for the POPS-B testing (VIC to take place tomorrow) will take place in parallel with the HW tests.

The status of the 4 ECRs applied during the YETS can be checked thanks to the new [EN-ACE tool](#).

Answering a question from **B. Mikulec**, **D. Hay** added that thanks to the new procedure, the lock-out went very smoothly and took only one day.

5. PS YETS activities report

F. Pedrosa reported on the PS shutdown activities ([Annex 4](#)).

The list of main activities that took place in the PS ring, the switch yard, TT2 and on surface was given. They were mainly consisting in regular maintenance, ATP cleaning, replacement of 15 magnets in the TT2, installation of new GSM cable, of 5 new power converters in Bdg. 355 and of new beam instrumentation. The decabling campaign took place in 4 different buildings and more than 4000 cables, representing 185 km, were removed from January to mid-February with only 10 cables found accidentally damaged so far. Thanks to the efficient shift work, the lock-out was removed 3 days in advance. **F. Pedrosa** concluded with acknowledgements to the different teams involved for their excellent work.

6. Schedule update

B. Mikulec presented the [injector schedule](#) (version 1.1).



The DSO tests for the PSB and the PS took place on schedule yesterday. The LHC4 tests are postponed. The Linac2 beam will be sent to the Switchyard next week and to the PSB on Friday. First physics in the East Area and nToF is scheduled for 23rd of March.

7. AOB.

B. Mikulec presented the Piquet starting dates for the 2018 run ([Annex 5](#)). Information is still missing from the TE-MPE group. All groups are invited to update the Piquet information on the OP webtool page.

The CO group (**G. Kruk**) sent the following information:

Tomorrow (Wednesday) morning at 9:00 we plan to upgrade the LIC Central Timing (PSB, LEIR, CPS, SPS) to SLC6 (64 bits version). The update requires only reboot of the LIC CT FEC and will take approximately 10 min, during which the timing will be down. The reboot of client FECs is not necessary. Note that the update does not contain any changes in logic but there are some adaptations to the 64-bit architecture and we rely on new versions of drivers to program timing hardware modules. The new version has been tested as much as possible in the Timing test bed and it all seems to work properly. If there are any issues with the new version found during the startup of different machines we can always rollback to the current (32-bit) version within 5-7min (the time of the reboot of the CT FEC).

Next Meeting: Tuesday 27th February 2018.

Minutes reported by [JB. Lallement](#) on 22nd February.