

Study of ToA Measurement with CFD Using Switched Capacitor Sampling or Bucket-Brigade Analog Time Delay Line

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Introduction

Studies of ToA measurements:

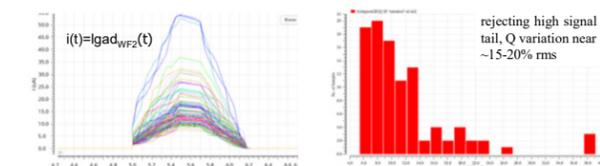
Motivation

4D (position and time) – tracking in the HL-LHC experiments.

Intended Sensor

Low-Gain Avalanche Diodes segmented into a pixelated structure with the proposed pixels sizes ranging from about 1mm² to about 3mm²; typical capacitance of the sensor segment on the order from a few pF up to about a dozen pF (for the largest pixels sizes considered), while the thickness (volume producing charge signal) typically less or equal to 50μm thick; charge produced by ionization drifts to the zone of high electric field, where multiplication with a typical gain of 10 increases the signal; ToA measurement precision constrained by variation of the total charge (Landau-like distribution with MPV of less than 10fC) and i(t) waveform fluctuation.

Current signal and variation of generated charge in LGAD sensors



Courtesy of Nicolò Cartiglio (INFN, Torino) WeightField2: a simulation program to optimize UFS; <http://personalpages.to.infn.it/~cartiglio/weightfield2>

Courtesy of Quan Sun (SMU, Dallas TX).

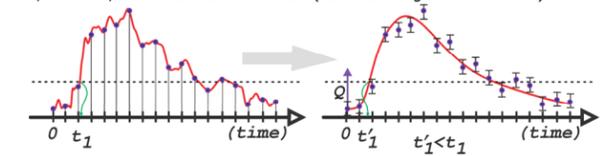
Finite duration of development of charge signal, event-to-event varied magnitude of the total produced charge, and jittering of the current waveform certainly contribute to the measurement error with each arrival time of particles can be estimated.

Time of Arrival (ToA) measurements

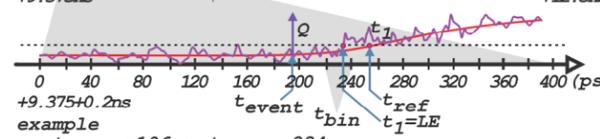
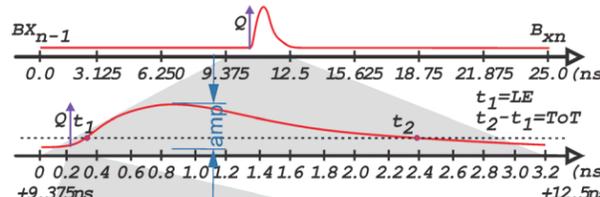
ToA measurement circuits are typically based on:

- waveform sampling and deriving of ToA from some sort of fitting, or
- finding time pointers when a waveform meets directly some criteria (level crossing)

Waveform sampling: knowing the theoretical form of signal, 'fitting' can yield t'_1 , that is a more precise estimator of threshold crossing than t_1 - resulting from comparing raw signal with threshold - (regardless of LE, CFD or other method used). The difference between time '0' and t'_1 is deterministic unless, e.g. timewalk is present and, therefore, further correction is needed (easier knowing entire waveform).



Thresholds testing: finding pointers when a raw waveform meets some criteria (level crossing) produce small quantity of data (e.g. 10 bits every event occurrence) and is practical for integrated multichannels systems due to conserving silicon area and power consumption, however, it is sensitive to stochastic errors (time walk and imperfection of circuitry: threshold offsets, time-to-flip overdrive dependence, etc. LE and CFD are typical examples of threshold testing measurements).



t_1 is an estimator of t_{ref} , instead of t_{event} , estimator of t_{ref} is measured; if $t_{ref} - t_{event}$ is deterministic, there is no error, binning adds quantization noise on t_1

ToA simulation package

symbolic-numerical calculation in Mathematica:

Motivation for Mathematica script

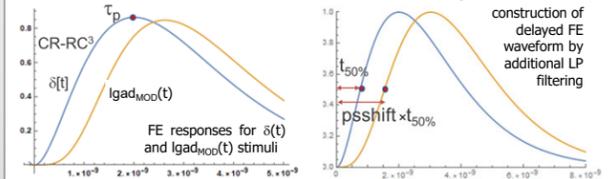
Learn what method should be chosen for the design of a readout integrated circuit and what ToA measurement error could be expected under the constraints of LGAD sensor properties, implementability in IC, power budget and resulting SNR.

Script developed to study:

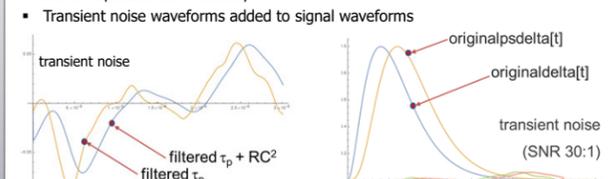
- LE based on single threshold and averaged result of multiple thresholds,
- CFD using real signal and pseudo delay by additional low pass filtering (psCFD),
 - both methods without and with correction based on amplitude measurements using ToT and using peak detection.
- Dependence of LE, CFD and psCFD on input signal form, i.e. $\delta(t)$ or $lgad_{MOD}(t)$ approximated (piece-wise-linear PCW) charge pulse (worst case variation),
- Dependence of LE, CFD and psCFD on variation of charge magnitude for $\delta(t)$ or $lgad_{MOD}(t)$ and on timing properties of $lgad_{MOD}(t)$ -approximated $i(t)$ (points p0,p1,p2,p3 are allowed to move),
- Dependence of LE, CFD and psCFD on FE filter pulse response time properties,
- Dependence of LE, CFD psCFD on noise with waveforms of filtered white noise (transient),

Features to approach IC implementation:

- Transfer function of FE preamplifier-shaper can be any function that possesses s-domain expression and analytical inverse Laplace transform (default: 2nd order CR-RC filter, $\tau_p = 1, 2, 4, 6$ ns),
- Bipolar signal for CFD created by:
 - subtraction of scaled down pre-sha and delayed pre-sha waveforms,
 - subtraction of scaled down pre-sha and additionally low-pass filtered pre-sha waveforms (default: additional 2nd order RC filter),

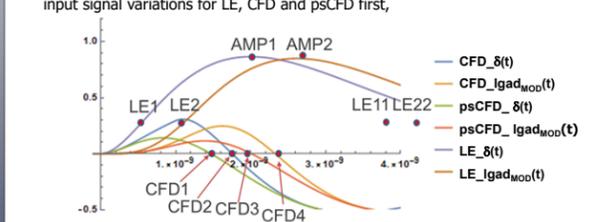


- Transient noise produced as a superposition of impulse responses to a sequence of $\delta(t)$ stimuli (white noise with sampling period = $\tau_p/100$) scaled to the tested SNR and interpolated for continuity
- Transient noise waveforms added to signal waveforms



Results of simulations:

- "Time domain" simulations are carried out taking into account transient noise and input signal variations for LE, CFD and psCFD first,



- Results LE1, AMP1, LE11, LE2, AMP2, LE22, CFD1, CFD2, CFD3, CFD4 are stored,
- "Time domain" simulations are followed by quantization studies, where each "time domain" event is randomly placed in the BX time window and time (toaquant) and amplitude (ampquant) measurements are quantized, ToT results are additionally binned (totquant).

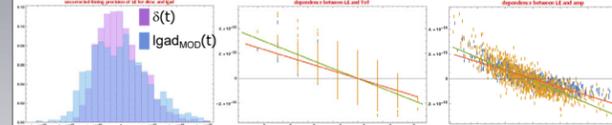
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Simulation results

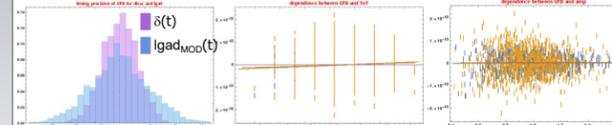
Example ToA measurements simulation results

$\tau_p = 2ns$ SNR=30 and
 input charge pulse variation: $Q \pm 20\%$, (p0, p1, p2, p3) $\pm 10\%$ - not fitted to $lgad_{WF2}(t)$ yet,
 quantization binning: $toa = 20ps$, $tot = 400ps$, amplitude=10bits

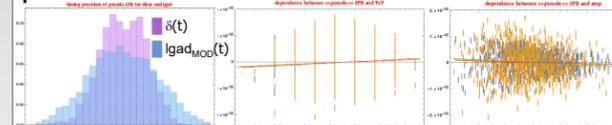
Leading Edge



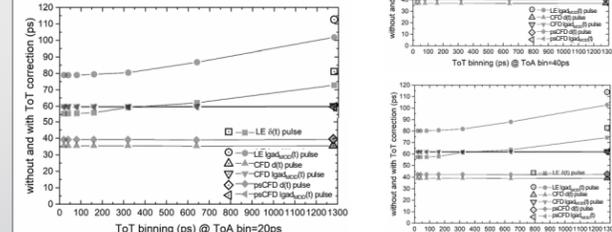
CFD



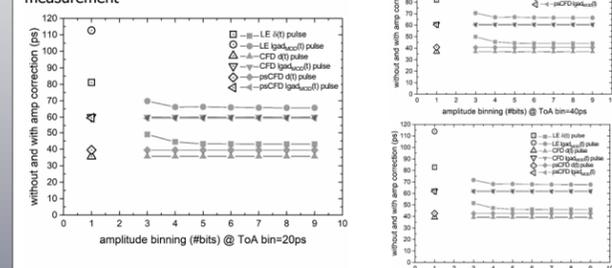
psCFD



dependence of ToA measurements on properties of the signals with correction using ToT as a function of ToT quantization and binning of time measurement



and correction using amplitude as a function of amplitude quantization and binning of time measurement



Conclusions

- CFD performs generally 20%-30% better than LE with correction using ToT or amplitude,
- Finite duration of $lgad_{MOD}(t)$ yields less good ToA measurement precision than $\delta(t)$ for the same total charge variation,
- ToT or amplitude do not have to be measured with high precision to allow LE correction (250ps and 5bits are satisfactory),
- CFD and psCFD yield close results, difference can result from nonequivalence of 'delay' and 'ratio' parameter

Circuit concepts

Extrapolating on expected SNR

50μm thick LGAD with gain=10 produces $Mean[Q] = Range[30-40ke^-]$
 What ENC to expect? $ENC = \propto \sqrt{\tau_p} \Rightarrow$ for X-ray pixel detector $\tau_p = 100ns$ yields $ENC \sim 100e^-$, so it is realistic to assume $ENC_{LGAD} > 1,000e^-$ **max SNR is worth any price**

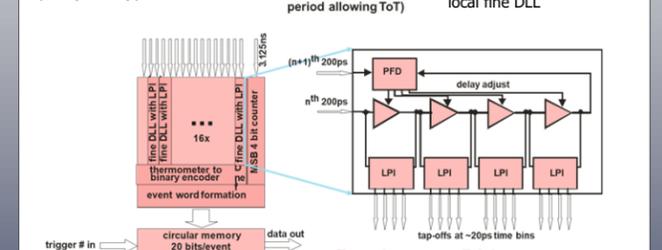
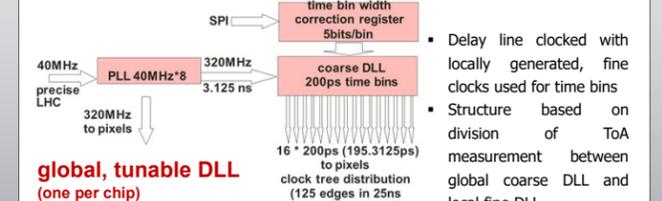
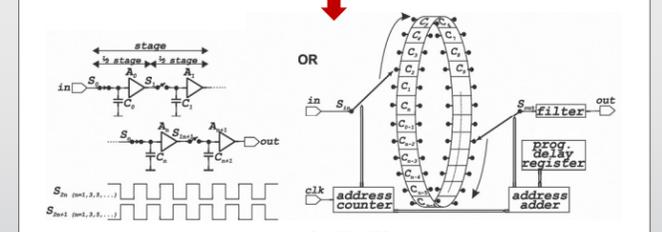
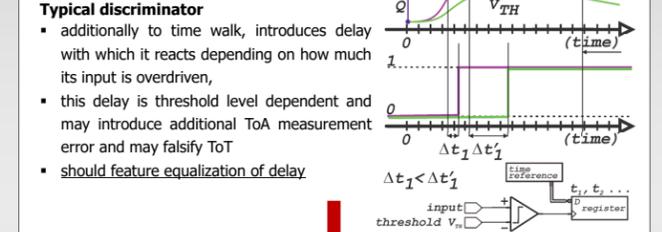
Directions for circuit:

- CFD method yields better ToA precision,
- psCFD and CFD operate with similar results but psCFD requires implementation of continuous LP filter (passive components),
- Discriminator needs to yield response free of overdrive dependency

practical issue with CFD in IC:

- needed delay $H(s) = e^{-s\tau}$ but practically achievable is $H(s) = \frac{1}{1+s\tau}$
- lack of actual delay causes errors in zero-crossing position measurements when waveform shape changes due to:
 - amplitude dependence
 - varied charge collection timing

solution could be discrete time analog delay line:



References

1. H. Spieler, "Fast Timing Methods for Semiconductor Detectors", IEEE Transactions on Nuclear Science, Vol.29, Iss. 3, 1982, pp. 1142 - 1158
2. R.A. Mao, et al., "Integrated MOS Analog Delay Line", IEEE JSSC, Vol.4, No.4, Aug. 1969, pp.196-201
3. W.J. Butler, "Practical Considerations for Analog Operation of Bucket-Brigade Circuits", IEEE JSSC, Vol. 8, No.2, Apr. 1973, pp.157-168
4. D.B. Kieda et al., "Variable Analog Delay Line for Analog Signal Processing on a Single Integrated Circuit Chip", US 6,222,409 B1, Apr. 2001