

# The Upgraded Microstrip Silicon Sensor Characterisation Facility of the University of Sheffield

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A major component of the ATLAS Phase-II upgrade is the Inner Tracker, an all-silicon detector featuring novel microstrip sensors. Motivated by the current research efforts in silicon sensor technology, a characterisation facility has been recently upgraded at the University of Sheffield. A description of the facility along with initial benchmark measurements of sensors before and after their irradiation are presented. The results found in agreement with similar measurements conducted by other facilities in the UK and support the successful commissioning of the facility.

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## 1. Introduction

Microstrip (strip) silicon sensors are used in a wide range of fields, from physical research to medicine. In many cases, as high-energy physics experiments or space-based applications, the device radiation tolerance is crucial and performance should be adequate even in harsh environments. Additionally, replacement after installation is often unrealistic, thus only extensively tested technology can be used for these demanding applications. Motivated by the upgrade of the ATLAS detector at CERN, in accordance to the HL-LHC upgrade of the Large Hadron Collider [1], a strip sensor characterisation facility has been re-commissioned, after improvements, at the University of Sheffield.

The HL-LHC is designed to deliver an order of magnitude higher instantaneous luminosity to increase the chance of observing rare physics phenomena. This comes with the price of higher detector occupancy, and thus higher radiation levels during operation. In order the ATLAS detector to maintain high performance in these new extreme standards, an upgrade on the tracking system, among others (Phase-II upgrade), is essential. The current Inner Detector is going to be replaced by the all-silicon Inner Tracker (ITk). The ITk consists of a pixel detector, at small radius close to the beam line, and a large-area strip tracking detector surrounding it. The latter expands in a four-layer barrel section and one end-cap on each side with six disks each.

Radiation simulation studies have shown that fluences up to  $10^{15}$   $1 \text{ MeV } n_{eq}/\text{cm}^2$  are expected at the innermost layer of the barrel section, located 405 mm away from the beam line, after a decade of operation [2]. As the strip sensors will be required to sustain high tracking capabilities over the whole duration of the HL-LHC program, evaluating the deterioration of different technologies and design architectures is essential for the choice.

The characterisation facility of the UoS is comparing key parameters of the sensors' operational performance before and after test irradiations to assess the significance of this damage. It is located in one of the clean-rooms of the Hicks Building of the UoS and provides either individual or collaborative measurements with similar facilities across the UK, e.g. Liverpool and Birmingham [3].

This document provides an overview of the facility (Section 3) along with commissioning measurements (Section 4), assessing its operation. A brief introduction on the radiation damage of a silicon sensor (Section 2) is given, motivating the particular measurements conducted, while a conclusion (Section 5) completes the document.

## 2. Radiation Damage on Silicon Sensors

High-energy particles that transverse a silicon sensor continuously damage it as, essentially, energy is also deposited in the material. It is useful to separate this damage into two categories as different macroscopic effects are caused depending on the sensor material affected: the bulk or the surface. The following paragraphs present few aspects of the radiation damage physics to motivate the measurements of the *leakage current*, *charge collection efficiency* and the *cluster size* in the evaluation of the performance of an irradiated strip sensor.

### Bulk damage

Particles that cross the bulk material of a sensor apart from ionising it to induce the measured signal, they also interact with the atoms of the crystal lattice. The atoms are displaced and interstitial or/and vacancy defects are created. This eventually alters the material's elementary properties and degrades its performance.

- Leakage current increases, as the defects act as electron-hole pair generation centres. This increase can be seen as excess heat that should be dissipated by cooling the sensors to keep this current, and thus the noise, low.
- Charge collection efficiency (CCE) decreases, due to defects acting as charge traps. The trapping rate is proportional to the number of defects and thus to the integrated fluence. At very high fluences ( $\geq 10^{15}$  1 MeV  $n_{eq}/\text{cm}^2$ ) this becomes the main limiting factor of silicon usage as charge is trapped before it arrives at the collection electrodes [4].

### Surface damage

By surface damage are described the defects on the  $\text{SiO}_2$  insulating surface material. This is induced by ionisation, in contrast to the bulk damage, and it has more permanent nature. Electrons and holes have very different mobilities in the  $\text{SiO}_2$ , thus after the ionisation are quickly separated and holes are accumulated to the Si -  $\text{SiO}_2$  interface. Consequently, electrons are attracted from the bulk side towards the interface and accumulate there too. This configuration results in a decreased inter-strip resistance and increased cross-talk. Effectively the sensor's spatial resolution is deteriorated by measuring wider clusters, i.e. larger number of strip collects the charge induced.

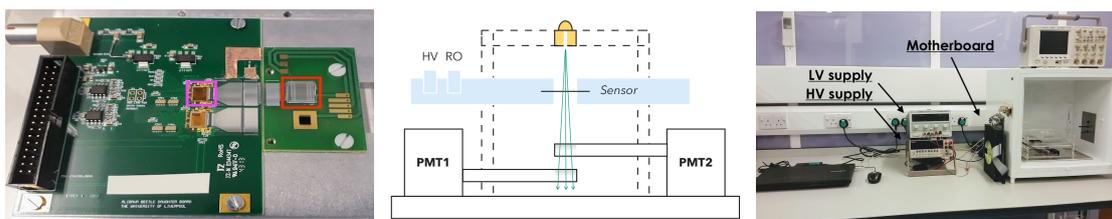
## 3. Characterisation Facility

Two key elements on the characterisation of a sensor is its signal readout and testing environment. As the measurement is heavily dependent on both these factors, the design of the facility at the UoS has been driven to closely resemble the operational conditions on HL-LHC.

The sensors' signal is acquired using the ALiBaVa system [5]. The system is composed by three units, the daughterboard, a custom build triggering sub-system and the motherboard. The core of the system is the Beetle ASIC chip that readouts the strip sensor providing an analogue output that is sampled with the frequency of the chip clock (40 MHz). Two Beetle chips, sensor mountings and power supplies—low-voltage for the chips and high-voltage for the sensor—are all incorporated on the daughterboard shown in Figure 1 (*left*). In order to excite the sensor and measure the signal, a radioactive source emitting  $\beta$ -radiation ( $^{90}\text{Sr}$ ) is used. It is placed right above the sensor so the particles are crossing it with the minimum solid angle, thus higher flux, as possible. Underneath the daughterboard, a custom-made system is triggering the ALiBaVa readout when the charged particles are crossing it. It is composed by two plastic scintillators mounted on photomultiplier and acting in coincidence to reduce the random environmental background. The whole setup is schematically shown in Figure 1 (*middle*). Finally, the purpose of the motherboard is to process the analogue data provided by the chip in accordance with the trigger signal. The latter allows the motherboard to read a particular position in the pipeline. Only one sample is needed for

each event while the Landau pulse shape is reconstructed by acquiring enough number of events. The peak of the pulse is proportional to the collected charge by the sensor. The motherboard's FPGA controls the system and its interference with the computer.

Exploiting the separability of the above three components, everything except of the motherboard (Figure 1 (middle)) is placed in a control environment. To resemble the operational conditions, measurements can be conducted at temperatures down to about  $-30^{\circ}\text{C}$  and humidity down to 10% RH. The environmental conditions are controlled by a fridge with dry nitrogen supply and monitored by a probe placed next to the under-test sensors. The whole facility described is depicted in Figure 1 (right), where the high and low voltage supplies can be also seen.



**Figure 1:** (left) The daughterboard with the Beetle chip (pink) and the sensor mounted on (red). (middle) The source, daughterboard and triggering setup. (right) The UoS strip sensor characterisation facility.

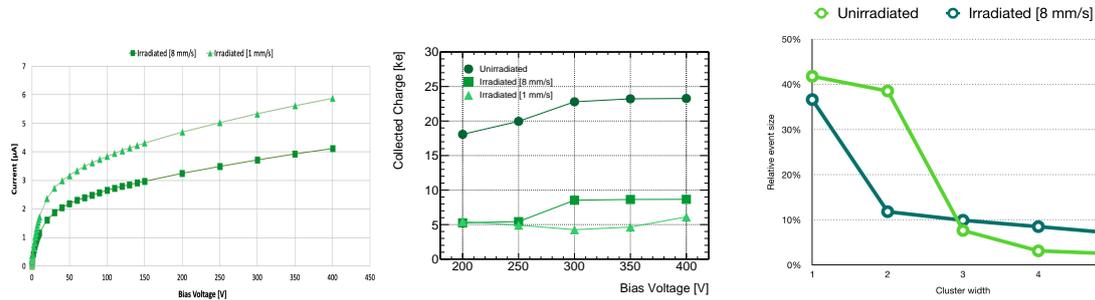
#### 4. Commissioning Measurements

In order to assess the operational performance of the facility, commissioning measurements have been taken comparing the leakage current, CCE and cluster size of an unirradiated sensor with two irradiated ones. The irradiation had been performed at the Birmingham MC40 irradiation facility [3] in 2014 with the purpose of defining benchmark irradiated sensors to compare among the characterisation facilities of the UK.

In particular,  $n^+$ -in-p miniature sensors (1 cm x 1 cm) of the ATLAS12 design [2], which have been fabricated to test the performance standards needed for the strip modules of the ITk are used. Two sensors were irradiated at fluence of  $10^{15}$  1 MeV  $n_{eq}/\text{cm}^2$  using a 24 MeV proton beam of  $1 \mu\text{A}$  current. The methodology of irradiation followed referred as scanning irradiation; the samples are placed in front of the incident beam and are scanned horizontally using a XY-axis robotic system. This mode allows beam non-uniformities to be eliminated. The two sensors were irradiated at different scanning speeds (8 mm/s and 1 mm/s) in order potential thermal annealing effects during irradiation to be apparent on the measurements. The sensors have been subsequently stored and transferred in sub-zero temperatures to reduce any post-irradiation annealing effects to negligible levels.

The irradiated sensors leakage currents measurements obtained are presented in Figure 2 (left) and shows an increase compared to the unirradiated sensor where the maximum current measured at few nA. In Figure 2 (middle) the CCE curve as function of the bias voltage applied is shown, where a degradation of the performance of the irradiated sensors is apparent, compared to the unirradiated one. In particular, 65-80% lower CCE is measured on the full depletion regime ( $> 300$  V), depending on the thermal damage of the sensor during the irradiation. Finally a cluster size analysis per event is performed; neighbor strips to the one with the higher signal are added to it.

In Figure 2 (*right*) can be seen a slight broadening of the cluster size distribution of the irradiated sensor compared to the unirradiated one.



**Figure 2:** (*left*) The characteristic curves of the two irradiated sensors used for the leakage current measurement. (*middle*) The cluster size distribution of the unirradiated and the irradiated sensor with scanning speed of 8 mm/s at 400 V bias. Each distribution is normalised to the total number of events per sensor measurement. (*right*) The CCE as function of the bias voltage applied for the unirradiated and the two irradiated ATLAS12 sensors.

Overall, it can be seen that the sensor irradiated with the lower scanning speed (1 mm/s) has much lower performance measured; higher noise due to higher leakage current and lower CCE. Generally, all the measurements found in agreement with other facilities in the UK [3] and abroad [2].

## 5. Conclusion

A description of the recently upgraded silicon strip sensor characterisation facility at the UoS was presented. The facility is using an analogue readout chip to measure the sensor's signal while the tests are performed in a control environment resembling the operational one. Benchmark sensors were measured before and after their irradiation in order to assess the performance of the facility. Leakage current, CCE and cluster size measurements show reasonable agreement with equivalent ones conducted by other facilities at the same type and conditions of sensors, and thus support the successful commissioning of the facility.

## References

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