

TWEPP 2018 Topical Workshop on Electronics for Particle Physics

Tuesday, 18 September 2018

ASIC - CAR 0.05/0.06 (aula) (09:00 - 10:15)

-Conveners: Grzegorz Deptuch

time	[id] title	presenter
09:00	[99] Characterization of the MPA prototype, a 65 nm pixel readout ASIC with on-chip quick transverse momentum discrimination capabilities.	CERESA, Davide
09:25	[141] MIMOSIS, the CMOS Pixel Sensor for the CBM Micro-Vertex Detector	MOREL, Frederic
09:50	[135] Two Designs of 4 × 14-Gbps VCSEL Array Driver in 65 nm CMOS for HEP Applications	YE, Jingbo

ASIC - CAR 0.05/0.06 (aula) (14:00 - 15:15)

-Conveners: Jorgen Christiansen

time	[id] title	presenter
14:00	[82] Characterization of GEMINI, a 16-channels programmable readout interface for Triple-GEM detectors in 180 nm CMOS.	MANGIAGALLI, Luca
14:25	[60] PACIFIC: The readout ASIC for the SciFi Tracker of the LHCb detector	COMERMA MONTELLS, Albert
14:50	[158] A 10-Gbps 4-channel VCSEL array driver with an on-chip charge pump power supply	YE, Jingbo

Wednesday, 19 September 2018

ASIC - CAR 0.05/0.06 (aula) (09:00 - 10:15)

-Conveners: Angelo Rivetti

time	[id] title	presenter
09:00	[129] Development of ultra-low power, rad-hard SAR ADCs in 130 nm CMOS technologies	MORON, Jakub
09:25	[120] HGCROC-V1: a prototype ASIC for CMS HGCALE	Dr DE LA TAILLE, Christophe
09:50	[137] Design and performance of GEMROC2 – a readout ASIC for Micro Pattern Gas Detectors	Mr ŁACH, Bartłomiej

ASIC - CAR 0.05/0.06 (aula) (14:00 - 15:15)

-Conveners: Wladyslaw Dabrowski

time	[id] title	presenter
14:00	[131] Characterization of the first prototype of the Silicon Strip readout ASIC (SSA) for the CMS Outer Tracker Phase II upgrade	CARATELLI, Alessandro
14:25	[126] FLAME - A readout ASIC for a luminosity calorimeter at a future linear collider	IDZIK, Marek
14:50	[75] Algorithm for an asynchronous approximation of a center of gravity for charge sharing compensation in pixel detectors' readout circuits	Dr OTFINOWSKI, Piotr

Thursday, 20 September 2018

ASIC - CAR 0.05/0.06 (aula) (09:00 - 10:15)

-Conveners: Christine Guo Hu

time	[id] title	presenter
09:00	[52] RD53A: a large scale prototype for HL-LHC silicon pixel detector phase 2 upgrades	MONTEIL, Ennio
09:25	[44] Test results of irradiated hybrid and monolithic CMOS pixel circuits in LFoundry 150 nm technology for the ATLAS Inner Tracker Upgrade	Mr CHEN, Zongde

Friday, 21 September 2018

ASIC - CAR 0.05/0.06 (aula) (09:00 - 10:15)

-Conveners: Gui Ping

time	[id] title	presenter
09:25	[101] Development of the monolithic ``MALTA'' CMOS sensor for the ATLAS ITK outer pixel layer	HITI, Bojan
09:50	[173] COLDATA PLL and Serializer: A 2.56GHz Phase Locked Loop (PLL) and a 1.28Gbps 10:1 Serializer Capable of Operating at Cryogenic Temperature	Mr WANG, Xiaoran