

TWEPP 2018 Topical Workshop on Electronics for Particle Physics

Thursday, 20 September 2018

Programmable Logic, Design Tools and Methods - CAR 1.09 (aula) (12:00 - 12:25)

-Conveners: Gregory Michiel Iles

time	[id] title	presenter
12:00	[36] Sorting of STS-XYTER2 data for microslice building for CBM experiment	GUMIŃSKI, Marek

Programmable Logic, Design Tools and Methods - CAR 1.09 (aula) (14:00 - 15:15)

-Conveners: Jorgen Christiansen

time	[id] title	presenter
14:00	[78] Improved tapped-delay-line Time-to-Digital Converter (TDC) with time-over-threshold (TOT) measurement for a new generation of Resistive Plate Chamber (RPC) detectors	Mr CHEN, Xiushan
14:25	[61] A collaborative HDL management tool for ATLAS L1Calo upgrades	GONNELLA, Francesco
14:50	[13] FELIX: developing a new detector interface for ATLAS trigger and readout	UNEL, Gokhan

Programmable Logic, Design Tools and Methods - CAR 1.09 (aula) (16:35 - 17:00)

-Conveners: Jorgen Christiansen

time	[id] title	presenter
16:35	[59] Upgrade of the CMS Barrel Muon Track Finder for HL-LHC featuring a Kalman Filter algorithm and an ATCA Host Processor with Ultrascale+ FPGAs	KARATHANASIS, Georgios