Session Program

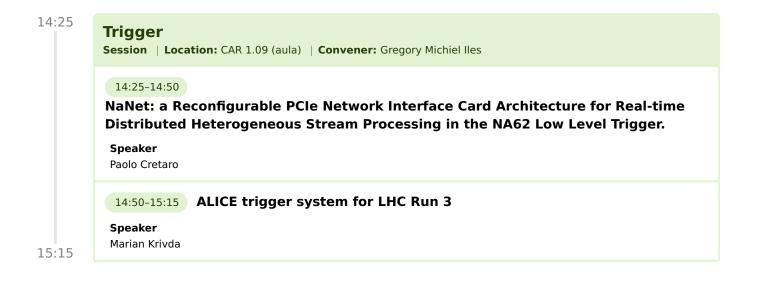
17-21 Sept 2018



TWEPP 2018 Topical Workshop on Electronics for Particle Physics

Trigger

Tuesday 18 September



Wednesday 19 September

11:35-12:00	
	ploying deep learning on FPGAs for L1 trigger and Data Acquisition
Speaker Javier Mauricio E	Duarte
12:00-12:25	
	Central Trigger Processor Interface for the Upgrade of the ATLAS r for Run-3
Speaker	
Marcos Vinicius	Silva Oliveira
Trigger Session Locat 14:00-14:25	tion: CAR 1.09 (aula) Convener: Marcus Julian French Hardware Trigger Processor for the ATLAS NSW System
Session Locat	Hardware Trigger Processor for the ATLAS NSW System
Session Locat 14:00-14:25 Speaker	Hardware Trigger Processor for the ATLAS NSW System
Session Locat 14:00-14:25 Speaker Thiago Costa De 14:25-14:50	Hardware Trigger Processor for the ATLAS NSW System
Session Locat 14:00-14:25 Speaker Thiago Costa De 14:25-14:50 Jets and top	Hardware Trigger Processor for the ATLAS NSW System
Session Locat 14:00-14:25 Speaker Thiago Costa De 14:25-14:50 Jets and top FPGA Speaker Marek Palka	Hardware Trigger Processor for the ATLAS NSW System
Session Locat 14:00-14:25 Speaker Thiago Costa De 14:25-14:50 Jets and top FPGA Speaker Marek Palka	Hardware Trigger Processor for the ATLAS NSW System Paiva ological trigger selection performed with the last generation Xilina