

Session Program

17-21 Sept 2018



TWEPP 2018 Topical Workshop on Electronics for Particle Physics

Trigger

Tuesday 18 September

14:25

Trigger

Session | **Location:** CAR 1.09 (aula) | **Convener:** Gregory Michiel Iles

14:25–14:50

NaNet: a Reconfigurable PCIe Network Interface Card Architecture for Real-time Distributed Heterogeneous Stream Processing in the NA62 Low Level Trigger.

Speaker

Paolo Cretaro

14:50–15:15

ALICE trigger system for LHC Run 3

Speaker

Marian Krivda

15:15

Wednesday 19 September

11:35

Trigger

Session | **Location:** CAR 1.09 (aula) | **Convener:** Ken Wyllie

11:35–12:00

HLS4ML: deploying deep learning on FPGAs for L1 trigger and Data Acquisition

Speaker

Javier Mauricio Duarte

12:00–12:25

The Muon to Central Trigger Processor Interface for the Upgrade of the ATLAS Muon Trigger for Run-3

Speaker

Marcos Vinicius Silva Oliveira

12:25

14:00

Trigger

Session | **Location:** CAR 1.09 (aula) | **Convener:** Marcus Julian French

14:00–14:25

Hardware Trigger Processor for the ATLAS NSW System

Speaker

Thiago Costa De Paiva

14:25–14:50

Jets and topological trigger selection performed with the last generation Xilinx FPGA

Speaker

Marek Palka

14:50–15:15

Serenity - An ATCA prototyping platform for CMS Phase-2

Speaker

Dr Andrew Rose

15:15