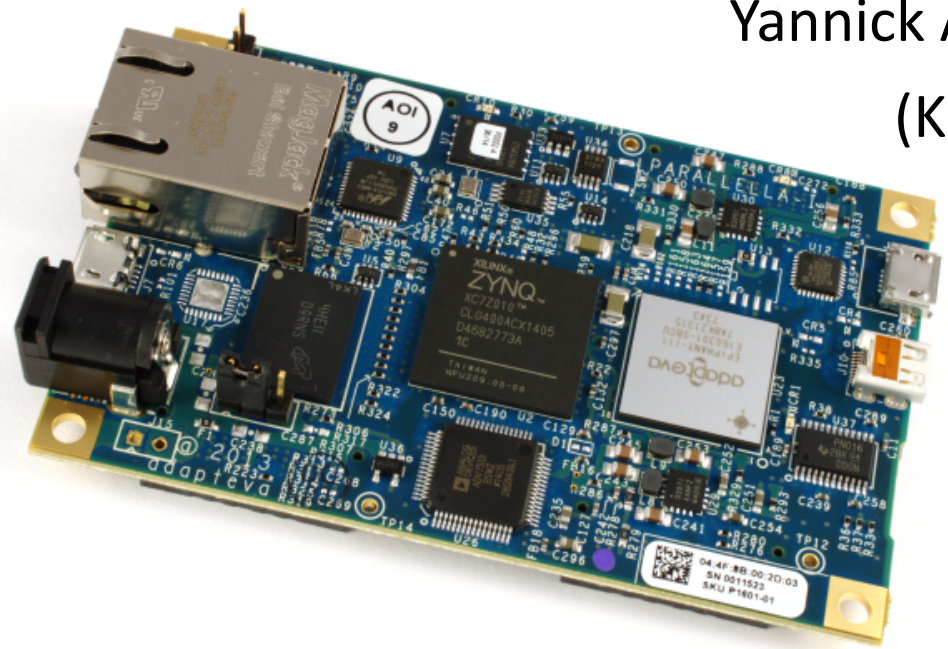
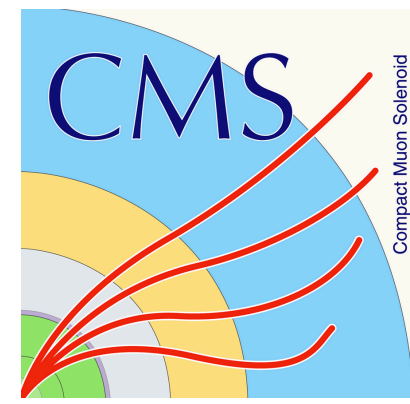


# Trackella status

Yannick Allard, Buğra Bilin, Louis Moureaux  
(Kirill Skovpen, Pascal Vanlaer)



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# Done...

- Previous simulation problem is solved
- Input and output memories tested and are consistent
- CPU Side
  - Finished the refactoring
  - Finished the implementation of two versions of the same algorithm:
    - All computations using integers
    - The same using floats
  - Compared performance
    - On the A9, the float version is 5% faster
    - On an Intel CPU (macbook pro: 2,2 GHz Intel Core i7)
      - they are compatible

# To do...

- Measure speed of hit memory loading
  - in simulation
- Implement hardware sorting
  - First in simulation
- Doublet finding on epiphany 😊