

Hello FPGA Update

3rd Patatrack Hackathon 23/05/18 Afternoon Scram
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Progress

- Lukas:
 - Implementing more OpenCL kernels for FPGA (bits of Kalman Filter)
- Sioni
 - Interface CMSSW KF to kernel
 - Host code in CMSSW launches kernel, sends data, receives garbage
- Shahzad
 - Added Intel/Altera compile to SCRAM (since this morning)

Up Next

- Lukas
 - Keep developing bigger FPGA kernel
- Sioni
 - Check kernel is executing on FPGA (emulator)
 - Pick up Shahzad's latest SCRAM
- All
 - Run on hardware