Hello FPGA Update

3rd Patatrack Hackathon 24/05/18 Afternoon Scram
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Progress & To do

```
Platform: Intel(R) FPGA SDK for OpenCL(TM)
Using 1 device(s)
  EmulatorDevice : Emulated Device
Using AOCX: vector_subtract.aocx
Launching for device 0 (1 elements)
inputs: 5.500000, 2.500000inputs: 4.500000, 1.500000
Time: 0.544 ms
Kernel time (device 0): 0.267 ms
verification r[0] @
Output: 3.000000
Reference: 3.000000
verification r[1]@
Output: 3.000000
Reference: 3.000000

Verification: PASS
```

- Lukas:
  - Standalone testing of kernels: so far so good
  - Progress to testing more complicated kernels
- Sioni
  - Attempting to execute simple kernel from CMSSW: segfault loading kernel binary
    - Debug, continue trying to execute kernel
  - HW build of demo kernel fails: debug
- Shahzad
  - Built more examples with SCRAM
  - Ran "hello world" on the board

"Hello"

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