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High-Density Low Gain Avalanche Detectors (HD-LGAD)

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The FBK, Universities and INFN of Turin and Trento groups are proposing an R&D project to RD50 collaboration, aimed at developing a new detector structure named high-density Low Gain Avalanche Detectors (HD-LGAD). The novel structure consists in a thin LGAD sensor with segmented multiplication junction, featuring small pixels and a reduced inter-pixel border region.

Standard LGADs made on thin (30-50 μ m) substrates recently demonstrated a superb timing resolution down to ~30 ps. On the other hand, the pixel size of such detectors is typically limited to 0.5-1 mm, due to the presence of an inter-pixel region (~60-70 μ m wide), which hosts the pixel guard ring and the junction termination edge. In such a region, the gain is completely suppressed and, consequently, the pixel fill factor (FF) is strongly reduced.

The final goal of the proposed project is to produce pixel arrays and micro-strips sensors with pixels and strips dimension down to $100 \mu m$, keeping at the same time the FF higher than 80%. We are investigating different strategies to reduce the inter-pixel border region: i) re-design the pixel guard-ring by using shallow trench isolation (STI); ii) improve the microfabrication process to reduce the feature size of all the structures at the pixel border (trenches, contacts, field plates). In this presentation, we will describe the project goals and motivations and we will present the first simulation results on the new designed structures.

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