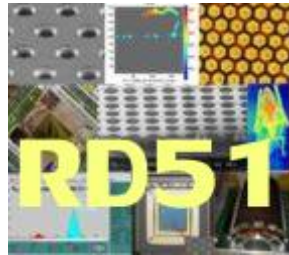


Session Program

23-25 Nov 2009



4th RD51 Collaboration Meeting

WG5: MPGD Related Electronics

CERN

Monday 23 November

14:00

WG5: MPGD Related Electronics

Session | Location: CERN, 6/2-004

14:00-14:05 **Welcome, Announcements, EVO**

Speakers

Hans Muller, Dr Jochen Kaminski

14:05-14:25 **From Chip matrix to 'ideal chip' for MPGDs**

Speaker

Dr Sorin Martoiu

14:25-14:50

MSGCROC - An ASIC for high count rate readout of position sensitive gas chambers

Speaker

Dr Bartosz Mindur

14:50-15:10

VFAT : A 128 channel chip for charge sensitive readout of multi-channel silicon & gas particle detectors

Speaker

Paul Aspell

15:10-15:30

Ethernet-driven control and data acquisition scheme for the Timepix-based TPC readout

Speaker

Uli Schaefer

15:30-15:50 **break**

15:50-16:05 **News from the Scalable Readout System**

Speaker

Hans Muller

16:05-16:25 **Analogue chip adapter for SRS**

Speaker

Sorin Martoiu

16:25-16:40 **Porting the DATE detector readout to GbE**

Speaker

Filippo Costa

16:40-16:50 **DATE status for the NEXT experiment**

Speaker

Mr Luis Diaz

16:50-17:10

Status VME-based readout system for Jlab GEM tracker

Speaker

Evaristo Cisbani

17:10-17:25

News on DTC link for SRS

Speaker

Dong Wang

17:25-17:35

ESD diode integration on MM chamber frontend

Speaker

Hans Muller

19:00