

CMOS Workshop

Round table



What are **common areas** that can be identified across the different fields – design, post-processing, assembly, test,...?

What are typical **timelines** for projects in the different fields – from R&D to installation?

Smaller pixels, faster readout are these the main needs in the near future?
What about **radiation damage**?

Which are the **industry trends** and how can we adapt them to our needs?