



AGH UNIVERSITY OF SCIENCE  
AND TECHNOLOGY



# Raport from the Hardware Working Group

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## Main Objectives of Hardware Group

- Presently the main Hardware Goal is to build a prototype of Compact Multilayer FCAL Detector
  - Within the AIDA-2020 project it is also our deliverable with the deadline originally in first part of 2019, but after the extension of AIDA2020 by one year, the deadline is moved forward by nine months.
- Proceeding with hardware R&Ds: on sensors, ASICs, mechanics, alignment, DAQ, back-end electronics, ...
- Test-beam preparation and data analyses – topic shared with Software&Analyses group

## Summary of meetings held

### Technical info

- From the last (May 2018) FCAL Workshop in Kraków we have not had HWG meeting
  - Vacations...
  - I was 100% busy with LHCb Upgrade/problems. In these days we are submitting “final” LHCb chip. I hope that we will come back to standard schedule very soon...
- Because of accumulated delays hardware works regarding the prototype of compact calorimeter (AIDA2020 delivery) are of primary importance and should progress efficiently
  - FLAME ASIC design (some results were shown at TWEPP2018). Jakub will give a talk...
  - FPGA-based readout. Efficient plan should be discussed here between JINR, IFJ-PAN and AGH-UST
- ..

*Thank You for Attention*