

Session Program

14-15 Jun 2018

EPIX / FPIX workshop

First day

Zurich
University of Zurich and PSI

Thursday 14 June

09:00

First day: Introduction

Session | **Location:** UZH Irchel, 36-K-08, University of Zurich Irchel campus Building 36

09:00–09:15 Overview & introduction

Speakers

Ben Kilminster, James Alexander

09:15–09:30 Phase 1 overview / lessons learned : mechanics / supply tube

Speakers

Peter Robmann, Peter Robmann

09:30–09:45 Phase 1 overview / lessons learned : system tests

Speaker

Lea Michaela Caminada

09:45–10:00 Phase 1 overview / lessons learned : modules / other

Speaker

Wolfram Erdmann

10:00–10:30 Discussion

10:30

11:00

First day: Modules

Session | **Location:** UZH Irchel, 36-K-08, University of Zurich Irchel campus Building 36

11:00–11:30 Modules (especially differences BPIX/EPIX/FPIX)

Speaker

Malte Backhaus

11:30–12:00 HDI FPIX

Speaker

Timothy Matthew Jones

12:00–12:15 EPIX module arrangement

Speaker

Wolfram Erdmann

12:15–13:00

Discussion (1x1 in EPIX/FPIX, module placement precision, assembly - gantry ?)

Speaker

Rachel Bartek

13:00

14:30

First day: Electronics

Session | **Location:** UZH Irchel, 36-K-08, University of Zurich Irchel campus Building 36

14:30–14:45 Hit rates studies for phase 1 : reliability of simulation

Speaker
Magnus Gienal

14:45-15:00 Data rates expected in phase 2

Speaker
Danek Kotlinski

15:00-15:30 TFPX portcards, etc.

Speaker
Karl Ecklund

15:30-15:45 Cables / connectors

Speaker
Charles Ralph Strohman

15:45-16:15 Discussion

16:15-16:45 coffee break

16:45-17:00 Elinks

Speaker
Sadiah Khalil

17:00-17:15 EPIX portcard

Speaker
Lea Michaela Caminada

17:15-17:30 Data Trigger Control (DTC) board

Speaker
Deborah Pinna

17:30-18:00 Serial powering

Speaker
Malte Backhaus

18:00-18:30 Discussion

18:30