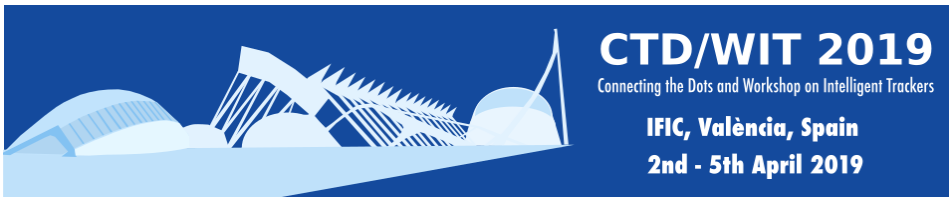

Contribution ID: **26** Type: **Talk**

A GPU High Level Trigger 1 for the upgraded LHCb detector

Thursday, April 4, 2019 3:30 PM (25 minutes)

Beginning in 2021, the upgraded LHCb experiment will use a triggerless readout system collecting data at an event rate of 30 MHz. A software-only High Level Trigger will enable unprecedented flexibility for trigger selections. During the first stage (HLT1), a sub-set of the full offline track reconstruction for charged particles is run to select particles of interest based on single or two-track selections. After this first stage, the event rate is reduced by at least a factor 30. Track reconstruction at 30 MHz represents a significant computing challenge, requiring a renovation of current algorithms and the underlying hardware. In this talk, we present the approach of executing the full HLT1 chain on GPUs. This includes decoding the raw data, clustering of hits, pattern recognition, as well as track fitting. We will discuss the infrastructure of our software project and the design of HLT1 algorithms optimized for many-core architectures. Both the computing and physics performance of the full HLT1 chain will be presented. Ses. 2

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Track Classification: 2: Real-time pattern recognition, fast tracking and performance evaluation