Contribution ID: **4**   Type: **not specified**

Emerging Use of DNNs

Monday 17 September 2018 10:35 (30 minutes)

Abstract: Data is being created at an alarming rate and we need faster and more efficient machines and algorithms to make sense of this data. Though we will still need the performance of traditional high performance computing, there are characteristics and relationships in the data that are needing more non-traditional computing approaches for greater efficiency. This need is also coupled with the fact that, with the slowing of Moore's Law, such new architectures not only have to be faster but need to compensate the changes in device reliability. This talk will focus on the emerging use of Deep Neural Networks (DNN's), their architecture, their memory requirements and the need to understand the application needs in order to improve the performance of such systems.

Bio: Steve Pawlowski is advanced computing solutions vice president at Micron Technology. He is responsible for defining and developing innovative memory solutions for the enterprise and high-performance computing markets. Prior to joining Micron in July 2014, Mr. Pawlowski was a senior fellow and the chief technology officer for Intel's Data Center and Connected Systems Group. Mr. Pawlowski's extensive industry experience includes 31 years at Intel, where he held several high-level positions and led teams in the design and development of next-generation system architectures and computing platforms. Mr. Pawlowski earned bachelor's degrees in electrical engineering and computer systems engineering technology from the Oregon Institute of Technology and a master's degree in computer science and engineering from the Oregon Graduate Institute. He also holds 58 patents.

Presenter: Mr PAWLOWSKI, Stephen (VP of Advanced Computing Solutions, Micron)

Session Classification: Day 1