

SW tests done

- Acquisition of 16k samples at 25 MHz using current SIS boards
- Fits in 200 ms duty cycle between injections

SW tasks to schedule

- Envelope detection algorithm needed
- If VFC is chosen:
 - Need to update source code for driver and test
 - If VFC driver already available, may start early
 - Re-use of AWAKE solution?
- If using SIS boards:
 - Acquisition of 16k sples @ 25 MHz already tested
 - Boards limited to 100 MHz anyway