

DAQ-ROC4Sens

Wednesday 28 November 2018 09:30 (20 minutes)

Where we will present to the RD50 community our new DAQ card project, now near its completion, capable of managing up to 4 ROC4Sens hybrid pixel detectors, ideal for testbeams, with only one DAQ Card and no external server (the data server is incorporated into the hybrid FPGA, with simple TCP/IP external access). The system is even able to make data processing on site.

Primary authors: Dr PALOMO PINTO, Francisco Rogelio (ETSI-Use); Dr MUÑOZ CHAVERO, Fernando (Dept. Ingeniería Electrónica ETSI Sevilla); Dr MILLÁN VÁZQUEZ DE LA TORRE, Rafael (Dpto. Ingeniería Electrónica ETSI Sevilla); Dr HINOJO, Jose María (Dpto. Ingeniería Electrónica ETSI Sevilla); Mr BLANCO CARMONA, Pedro (Dpto. Ingeniería Electrónica ETSI Sevilla); Mr SÁNCHEZ, Jorge (Dpto. Ingeniería Electrónica ETSI Sevilla); Dr VILA ALVAREZ, Iván (IFCA)

Presenters: Dr PALOMO PINTO, Francisco Rogelio (ETSI-Use); PALOMO PINTO, Francisco Rogelio (Universidad de Sevilla (ES)); PALOMO PINTO, Francisco Rogelio (Universidad de Sevilla (ES))

Session Classification: Pixel and Strip Detectors