

# Test beam results of irradiated RD53A 3D modules for ATLAS ITk pixel detector

Giulia Giannini, Sebastian Grinstein, Stefano Terzo,  
Maria Manna, Giulio Pellegrini, David Quirion

*33rd RD50 workshop  
November 28th, 2018*



**Institut de Física  
d'Altes Energies**



**"la Caixa" Foundation**

- LHC is headed towards **High Luminosity upgrade** to improve physics reach by increasing the luminosity
  - Average of **200 simultaneous interactions**

## Challenges:

- Large pile-up
- Higher particle fluence



Better resolution -> **smaller pixel size**



**Radiation hard:** develop novel sensor concepts that can survive very high radiation exposure

**Need for new detectors!**

- LHC is headed towards **High Luminosity upgrade** to improve physics reach by increasing the luminosity
  - Average of **200 simultaneous interactions**

## Challenges:

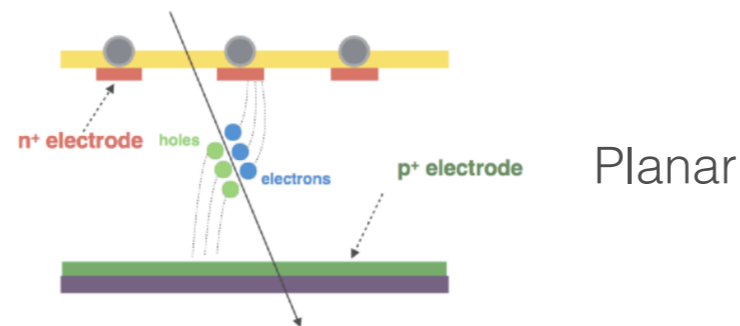
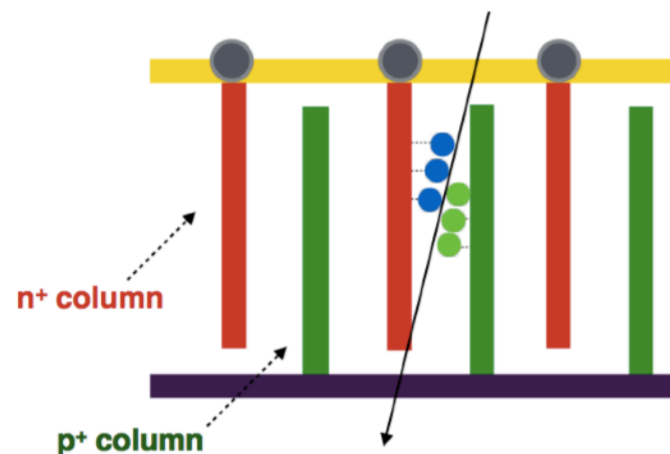
- Large pile-up
- Higher particle fluence



Better resolution -> **smaller pixel size**

**Radiation hard:** develop novel sensor concepts that can survive very high radiation exposure

## 3D silicon sensors



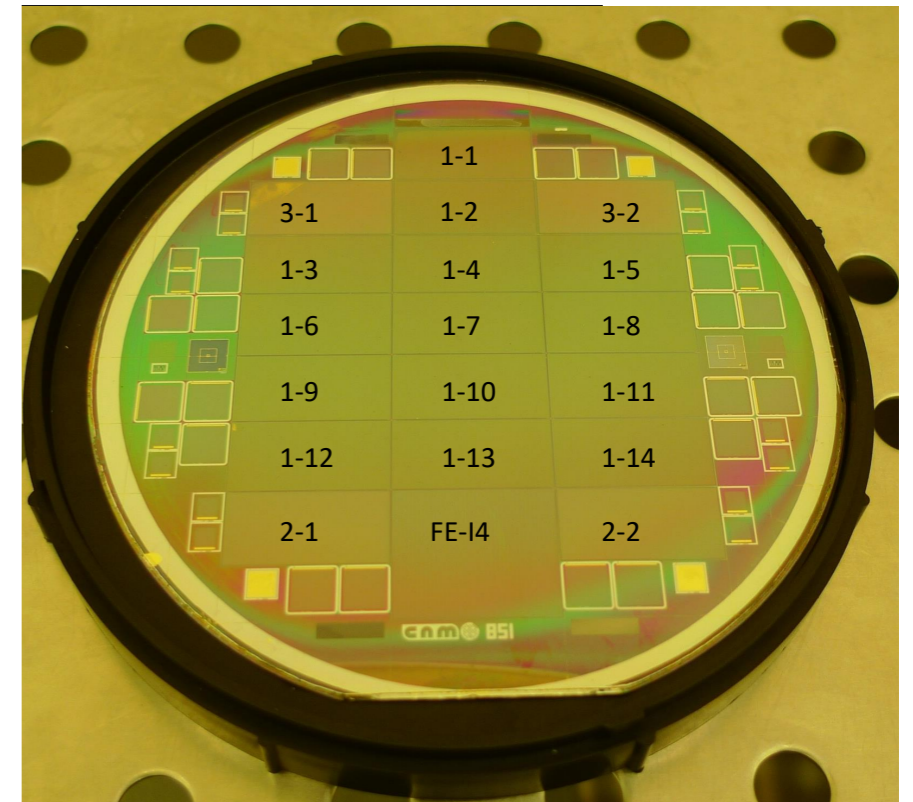
- Electrodes not placed on the surface but columns inserted in the bulk
- Short drift path for electrons-holes pairs towards columns
- Drift path decoupled from active thickness

**Radiation hardness!!**

**Baseline for the innermost layer of ATLAS ITk pixel detector**

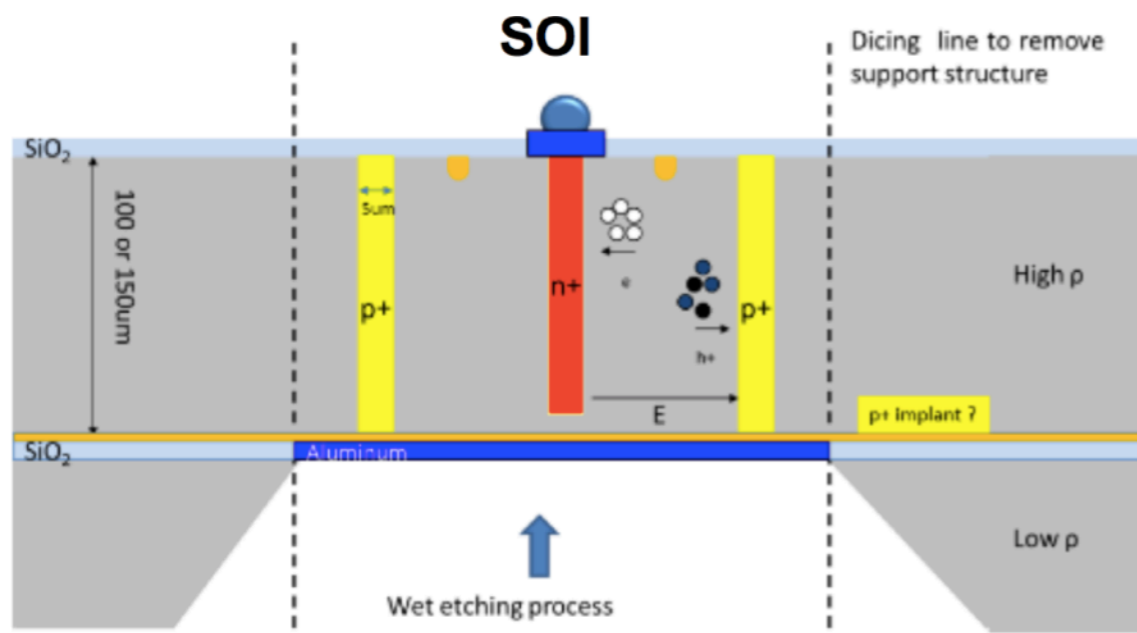
# RD53A modules

- **RD53A** chip first prototype of HL-LHC pixel ASIC
- CNM -> 3D sensor production **compatible with RD53A chip**
  - Single sided process
  - SOI wafers
  - 150  $\mu\text{m}$  active thickness, 300  $\mu\text{m}$  support wafer
  - 50x50  $\mu\text{m}^2$  and 25x100  $\mu\text{m}^2$  1E
  - Electroless UBM at CNM (not always perfect but not going to be used in final production)
- Flip chip and assembly at IFAE
- Characterisation with YARR system

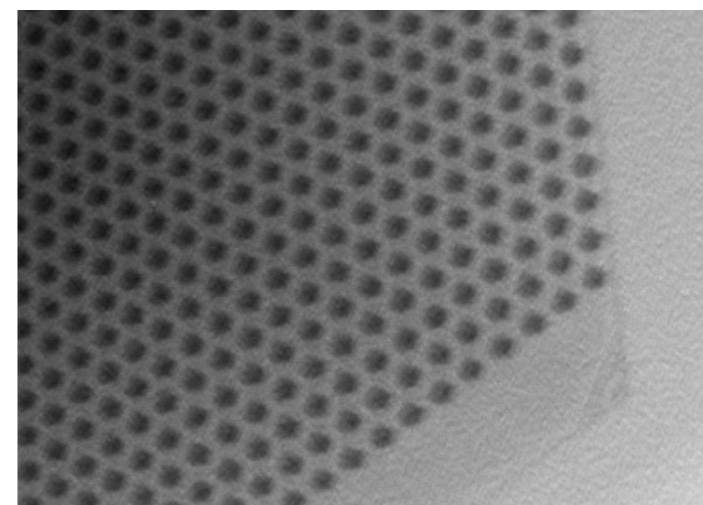


Wafer 3

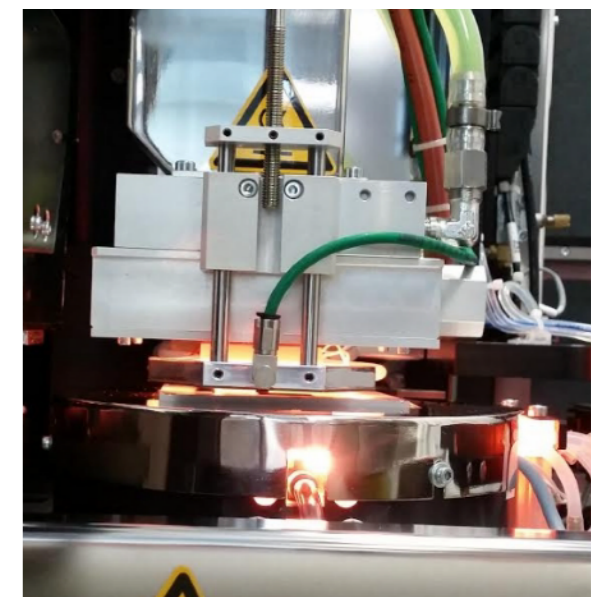
**Overall good yield** especially for 50x50  $\mu\text{m}^2$



X-ray image of bump bonds



Flip-chip at IFAE



# Produced RD53A modules



	Wafer	Sensor	RD53A Chip (GP1 W1 3AS180701#11)	Bump Bonding	Geometry ( $\mu\text{m}^2$ )	
1	No sensor		3-3	-	-	-
2	W4	1-5	3-10	Good	50x50	<b>IRRADIATED AT KIT TO 5E15</b> <b>Measured at August tb</b>
3	W3	3-1	6-5	Good	25x100 1E	<b>Measured at July tb</b> <b>BUT died when moving to another PCB</b>
4	W3	1-9	4-12	Good	50x50	<b>IRRADIATED AT PS 1E16</b> <b>Measured at October tb</b>
5	W3	1-7	4-11	Good	50x50	<b>Measured at July tb</b> <b>BEING IRRADIATED AT CYRIC TO 1E16</b>
6	W3	3-2	3-11	Failed	50x50	<b>Flip-chip failed due to bad UBM</b>
7	W1	1-4	3-11 (reused!)	Bad	50x50	<b>IRRADIATED AT PS 1E16</b> <b>Mostly disconnected</b>
8	W1	1-6	6-4	Good	50x50	<b>IRRADIATED AT PS 1E16</b> <b>Measured at October tb</b>
9	W1	3-2	6-3	Good	25x100 1E	<b>Shorted (problematic wire bonding)</b>
10	W1	3-1	7-3	Edge off	25x100 1E	<b>IRRADIATED AT KIT 5E15</b> <b>Measured at October tb</b>
11	W1	1-11	7-4	Good	50x50	<b>Fell apart disconnect. now in reworking</b>

- **BDAQ** system to readout RD53As
- Cooling box available in H6A for irradiated devices

## July

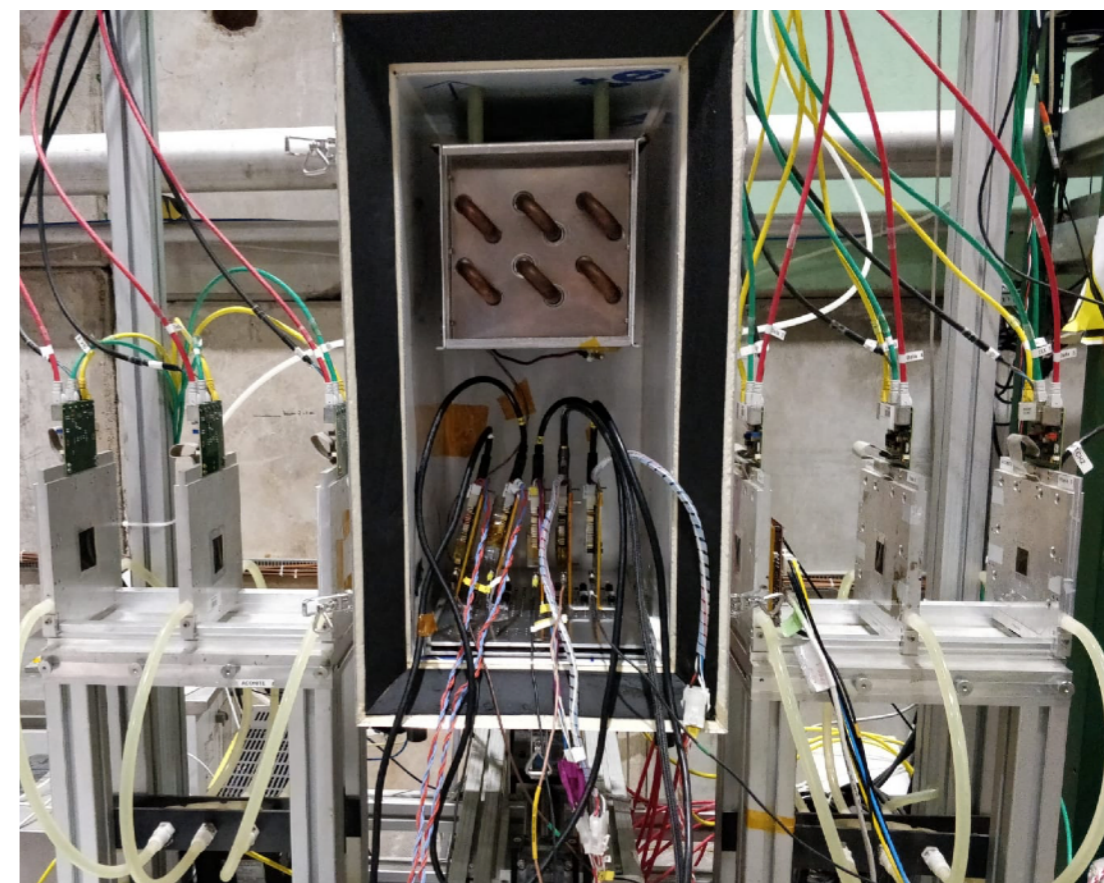
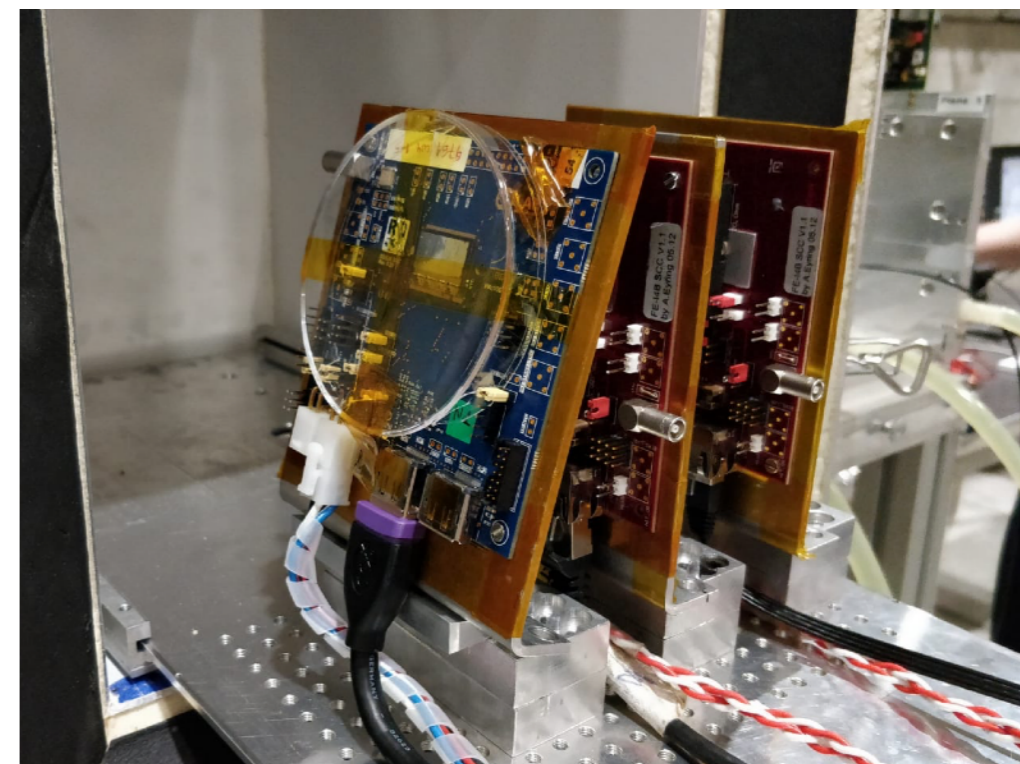
- RD53A unirradiated modules,  $50 \times 50 \mu\text{m}^2$  and  $25 \times 100 \mu\text{m}^2$  1E

## August

- First irradiated RD53A to  **$5e15 \text{ n}_{\text{eq}}/\text{cm}^2$** 
  - **$50 \times 50 \mu\text{m}^2$**
  - Uniformly irradiated at KIT with protons to  $5e15 \text{ n}_{\text{eq}}/\text{cm}^2$
  - Both  $0^\circ$  and  $15^\circ$  measured

## October

- Irradiated RD53A to  **$5e15 \text{ n}_{\text{eq}}/\text{cm}^2$** 
  - **$25 \times 100 \mu\text{m}^2$  1E**
  - Uniformly irradiated at KIT with protons to  $5e15 \text{ n}_{\text{eq}}/\text{cm}^2$
  - Both  $0^\circ$  and  $15^\circ$  measured
- 3 irradiated RD53As to  **$1e16 \text{ n}_{\text{eq}}/\text{cm}^2$** 
  - **$50 \times 50 \mu\text{m}^2$**
  - Non-uniformly irradiated at PS with protons to  $1e16 \text{ n}_{\text{eq}}/\text{cm}^2$



- **BDAQ** system to readout RD53As
- Cooling box available in H6A for irradiated devices

## July

- RD53A unirradiated modules, 50x50  $\mu\text{m}^2$  and 25x100  $\mu\text{m}^2$  1E

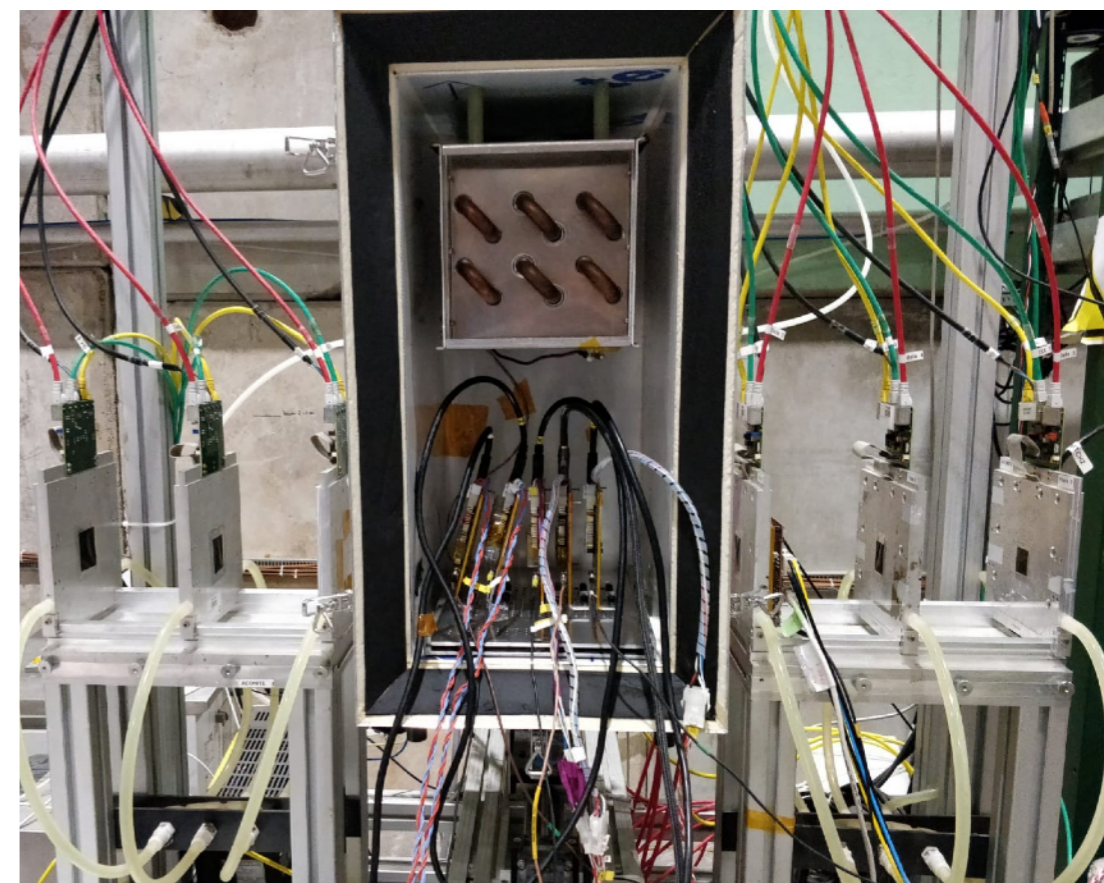
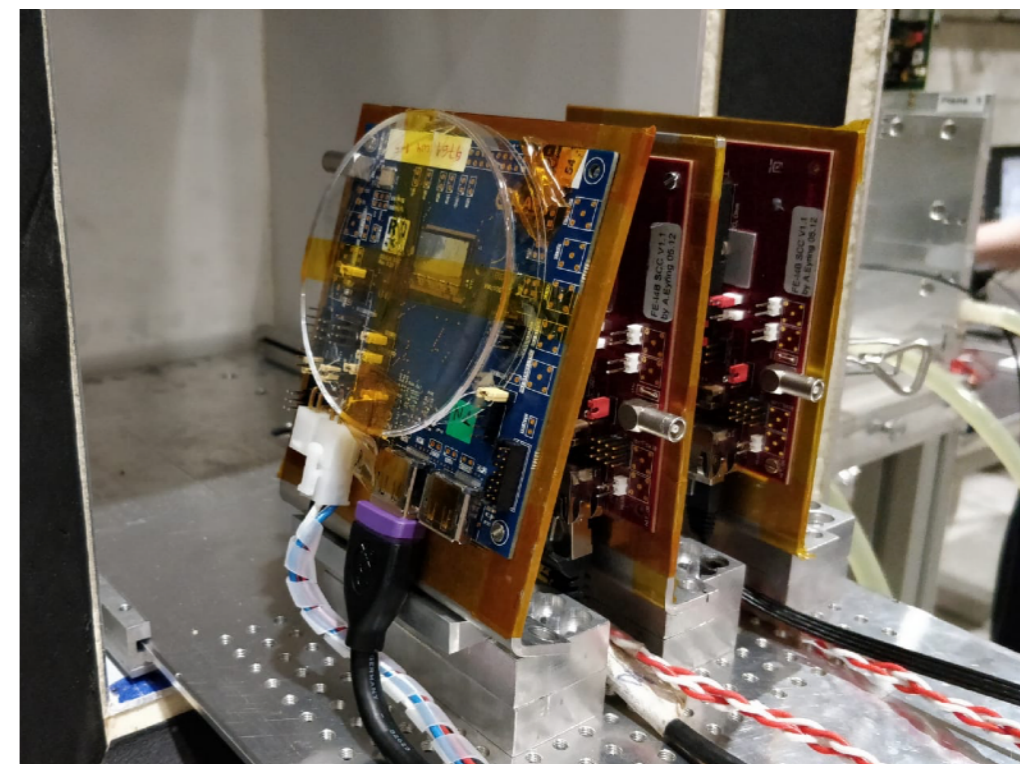
## August

- First irradiated RD53A to **5e15 n<sub>eq</sub>/cm<sup>2</sup>**
  - **50x50  $\mu\text{m}^2$**
  - Uniformly irradiated at KIT with protons to 5e15 n<sub>eq</sub>/cm<sup>2</sup>
  - Both 0° and 15° measured

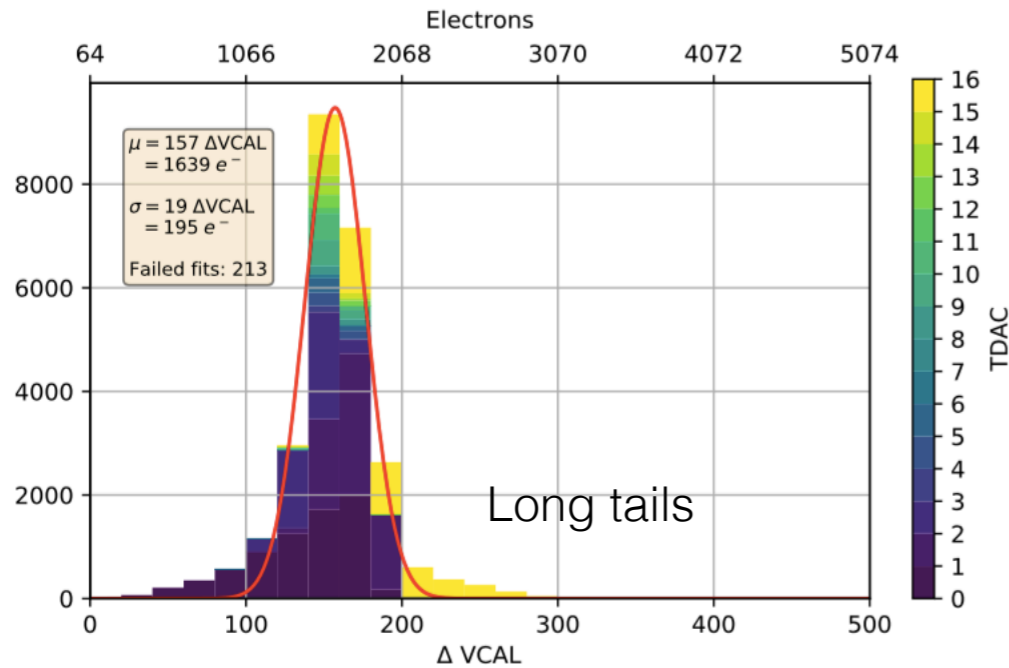
## October

- Irradiated RD53A to **5e15 n<sub>eq</sub>/cm<sup>2</sup>**
  - **25x100  $\mu\text{m}^2$  1E**
  - Uniformly irradiated at KIT with protons to 5e15 n<sub>eq</sub>/cm<sup>2</sup>
  - Both 0° and 15° measured
- 3 irradiated RD53As to **1e16 n<sub>eq</sub>/cm<sup>2</sup>**
  - **50x50  $\mu\text{m}^2$**
  - Non-uniformly irradiated at PS with protons to 1e16 n<sub>eq</sub>/cm<sup>2</sup>

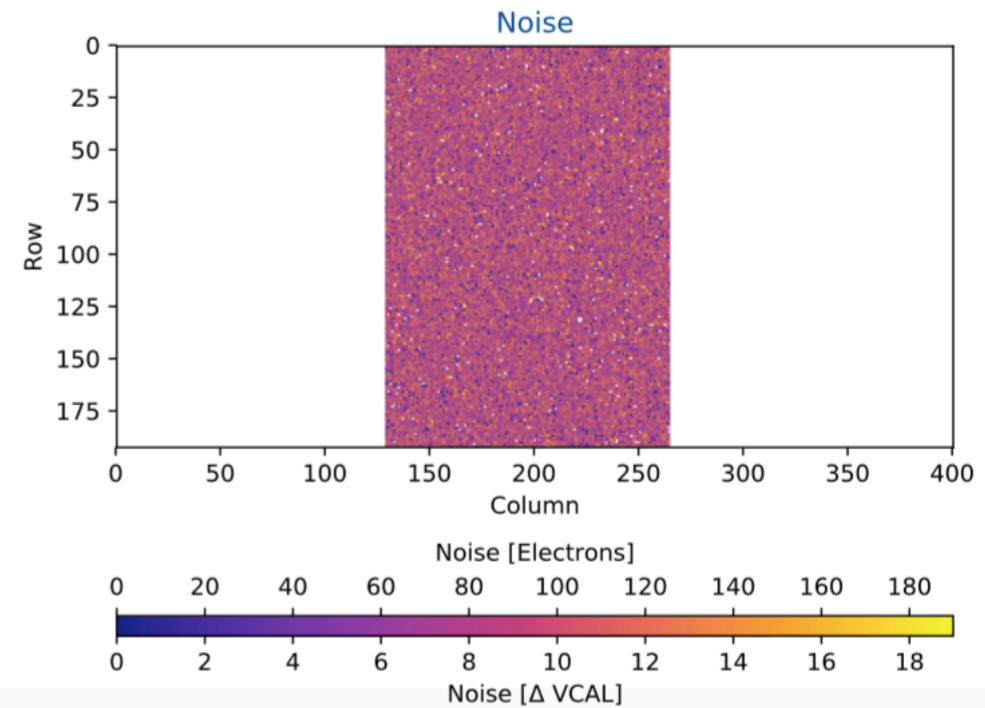
**Analysis on going**



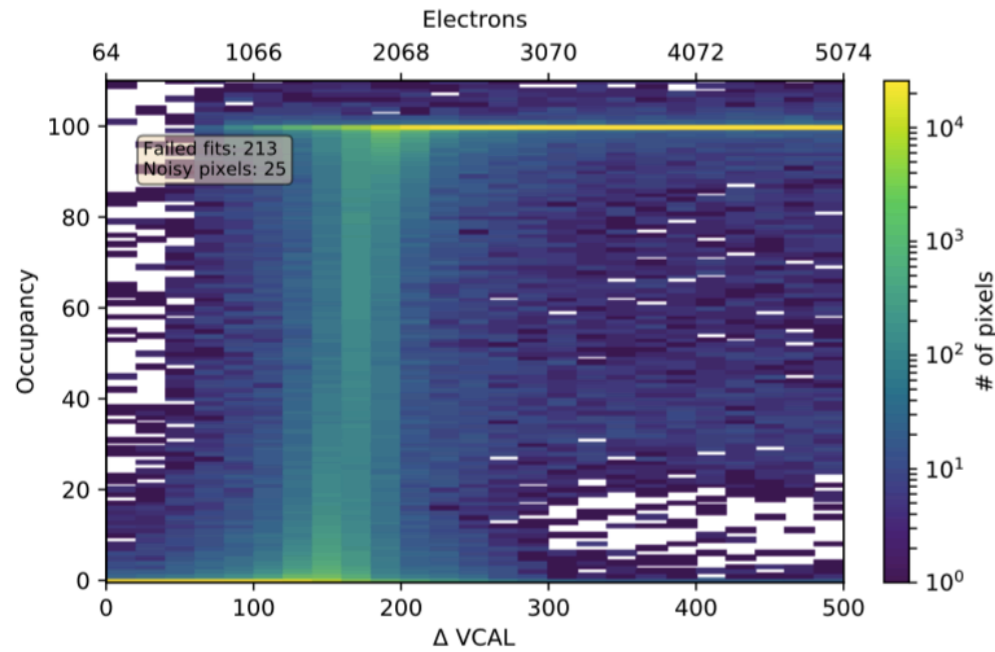
RD53A preliminary Chip S/N: W4\_1-5\_5e15  
Threshold distribution for enabled pixels



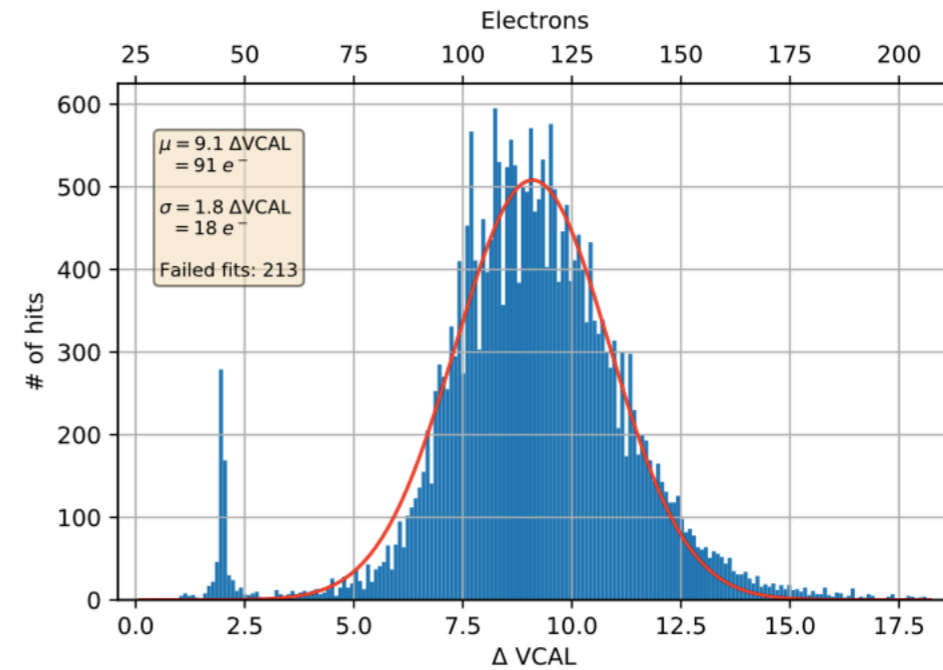
RD53A preliminary Chip S/N: W4\_1-5\_5e15



RD53A preliminary Chip S/N: W4\_1-5\_5e15  
S-curves for 26112 pixel(s)



RD53A preliminary Chip S/N: W4\_1-5\_5e15  
Noise distribution for enabled pixels



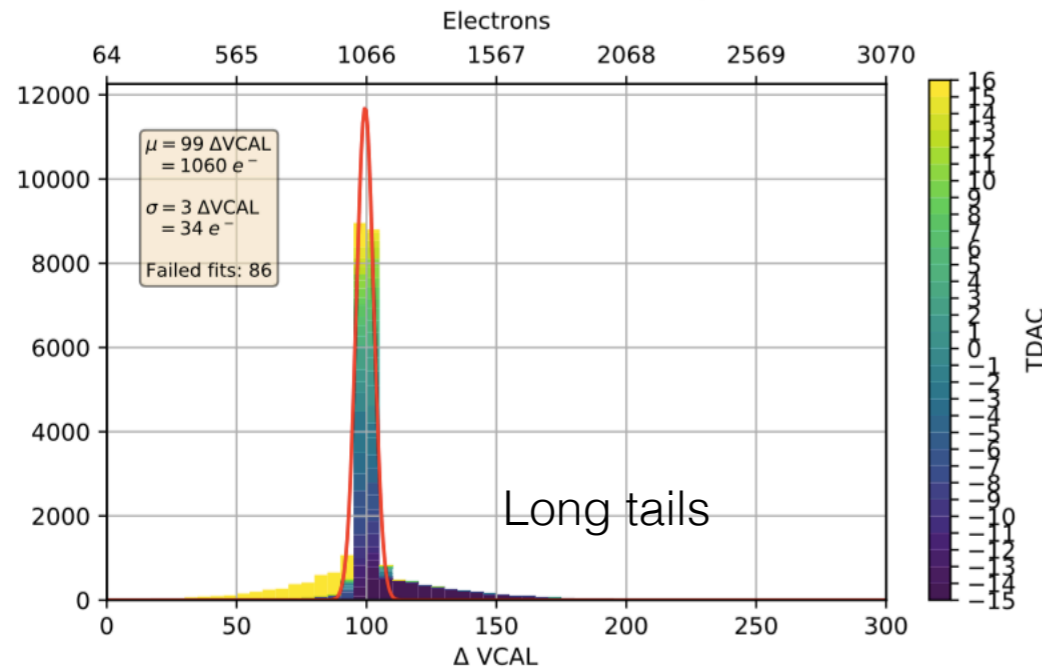
Linear FE calibrated to threshold of  $\sim 1600e$  with a noise dispersion of  $\sim 110e$



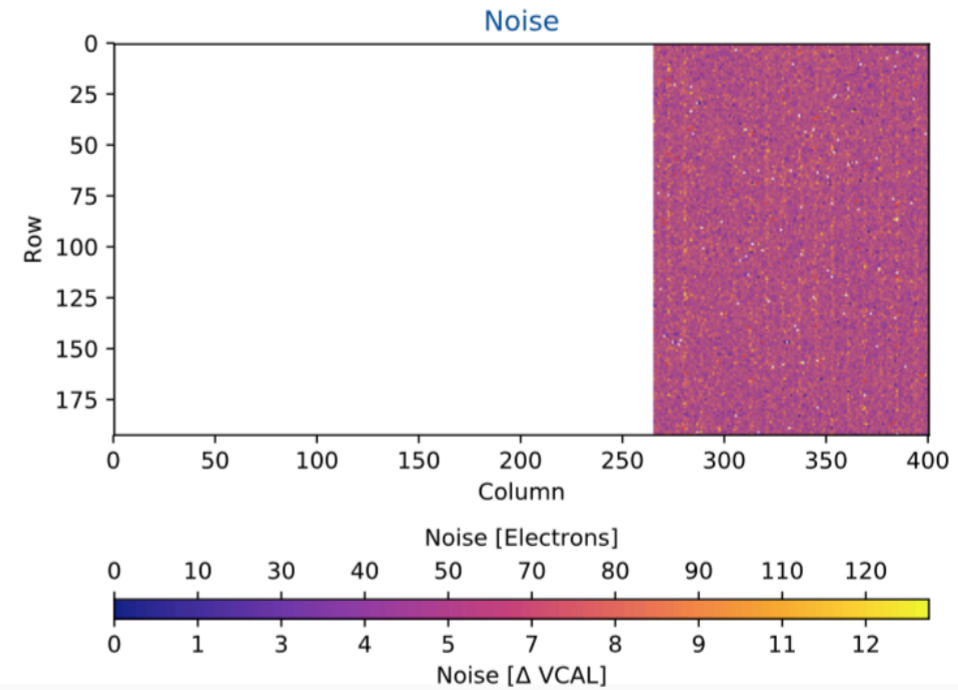
# 50x50 $\mu\text{m}^2$ - $5e15$ $n_{\text{eq}}/\text{cm}^2$ KIT - Tuning diff. FE



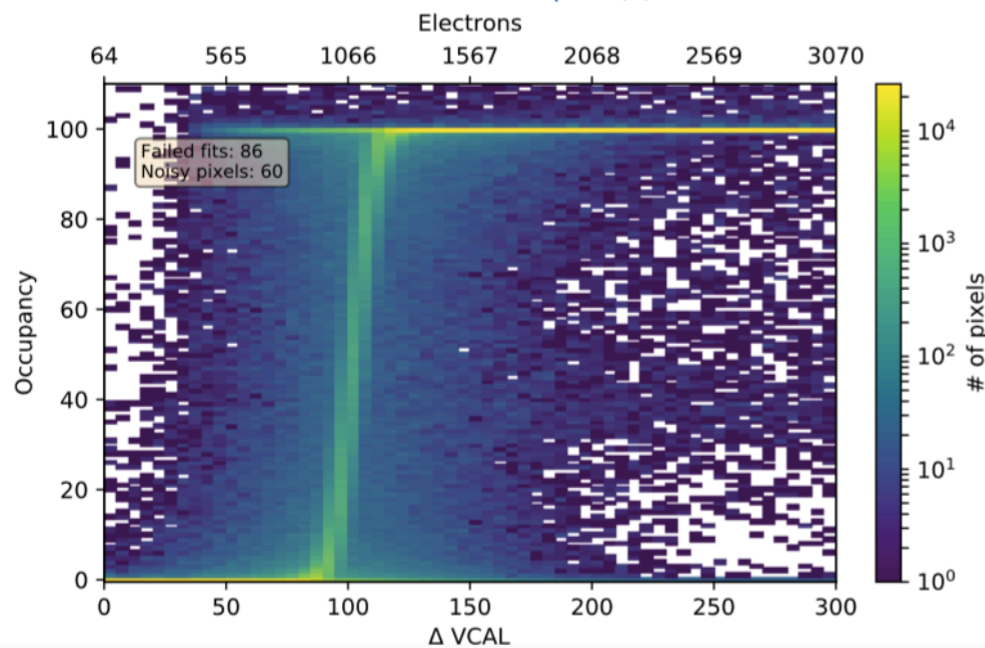
RD53A preliminary Chip S/N: W4\_1-5\_5e15  
Threshold distribution for enabled pixels



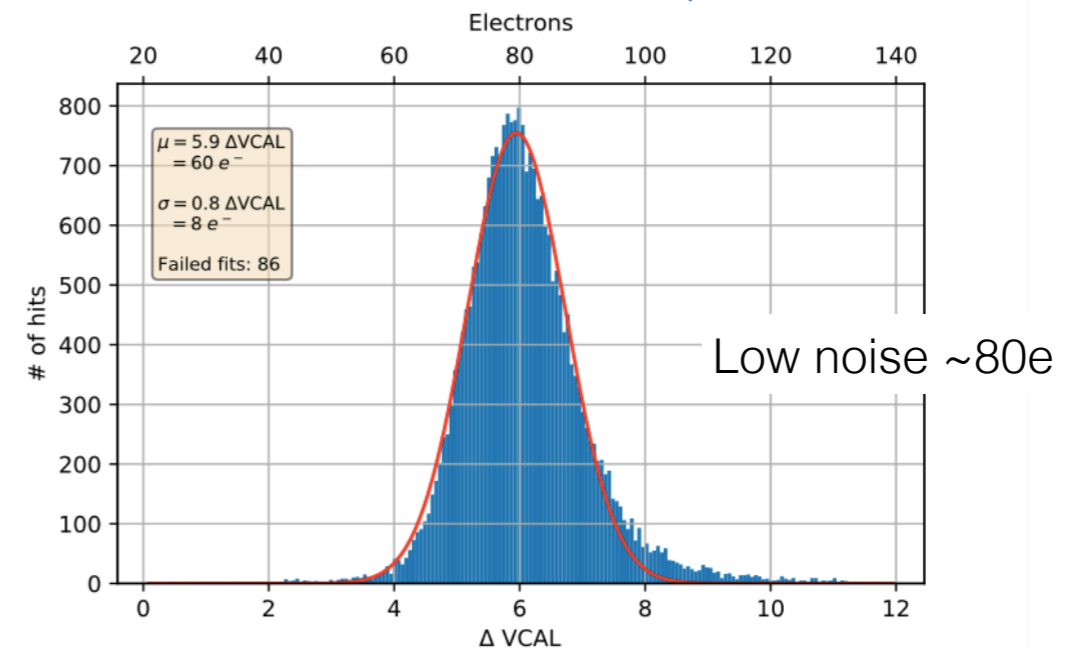
RD53A preliminary Chip S/N: W4\_1-5\_5e15



RD53A preliminary Chip S/N: W4\_1-5\_5e15  
S-curves for 26112 pixel(s)

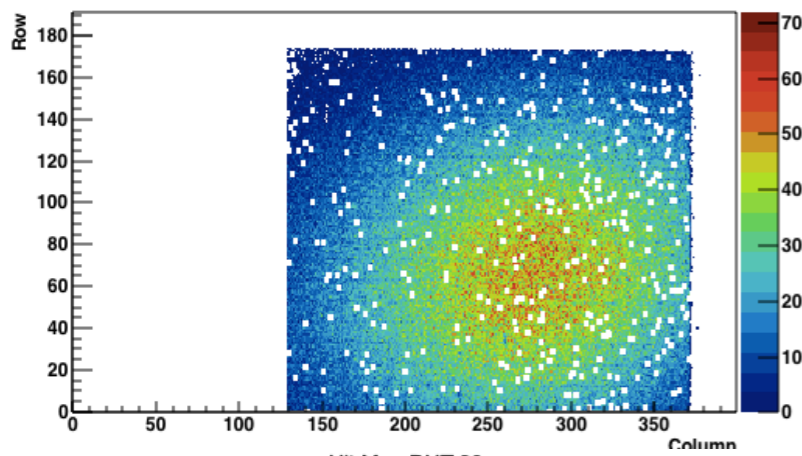


RD53A preliminary Chip S/N: W4\_1-5\_5e15  
Noise distribution for enabled pixels

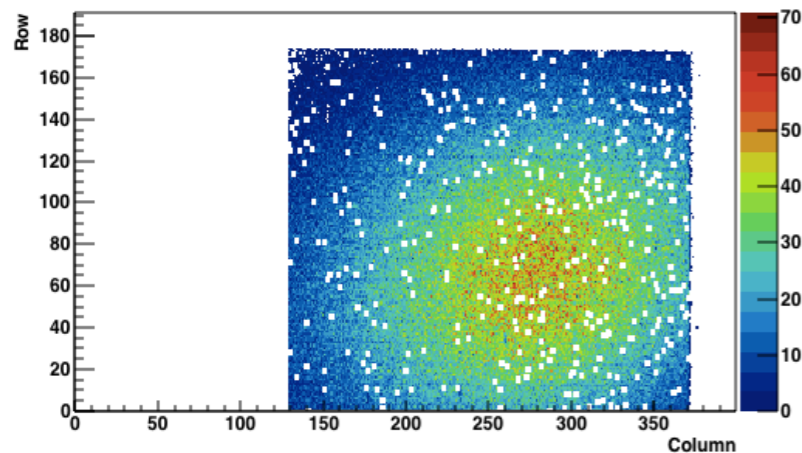


Differential FE calibrated to threshold of  $\sim 1000e$  with a noise dispersion of  $\sim 80e$

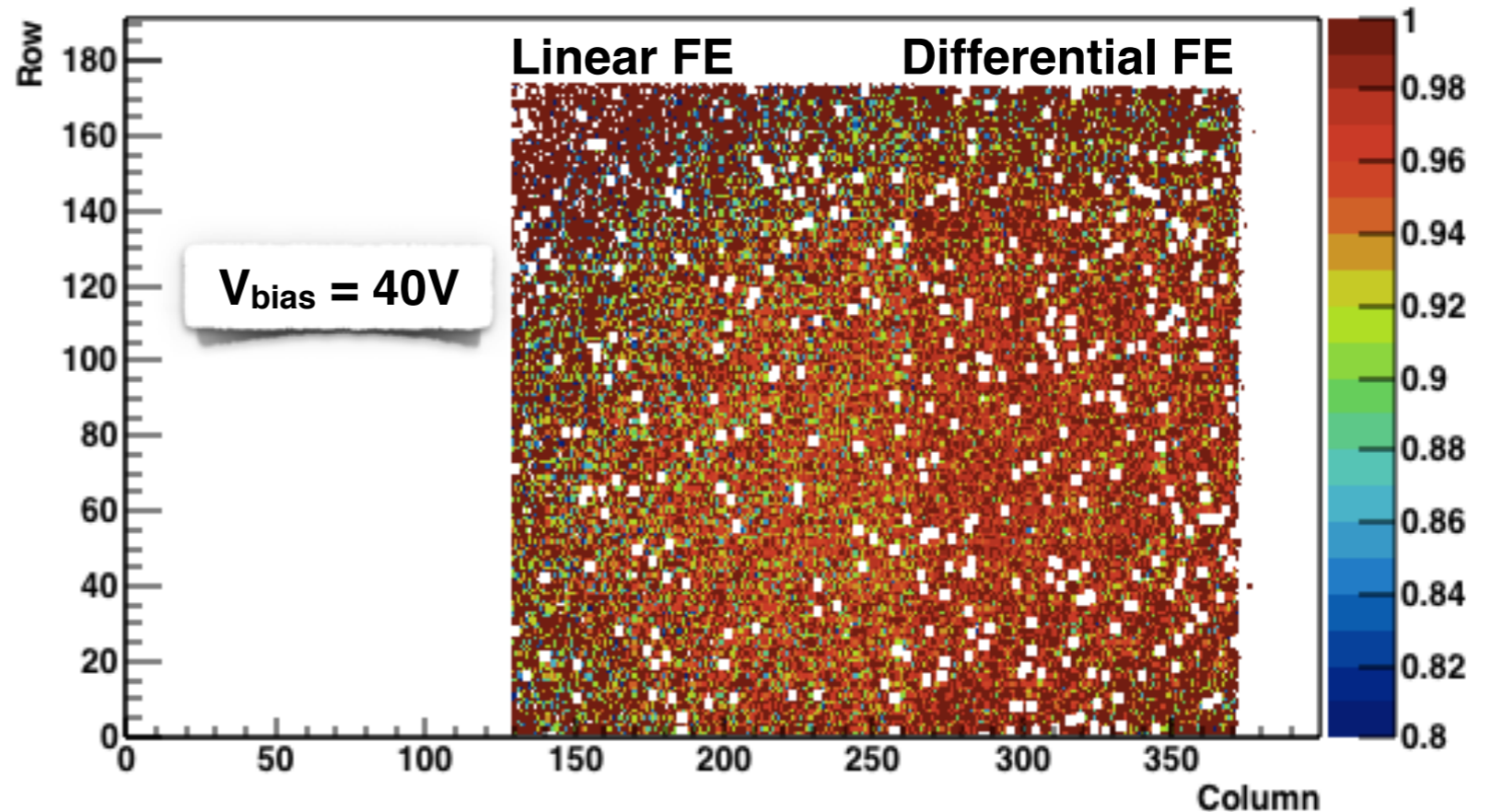
Track Map DUT 30



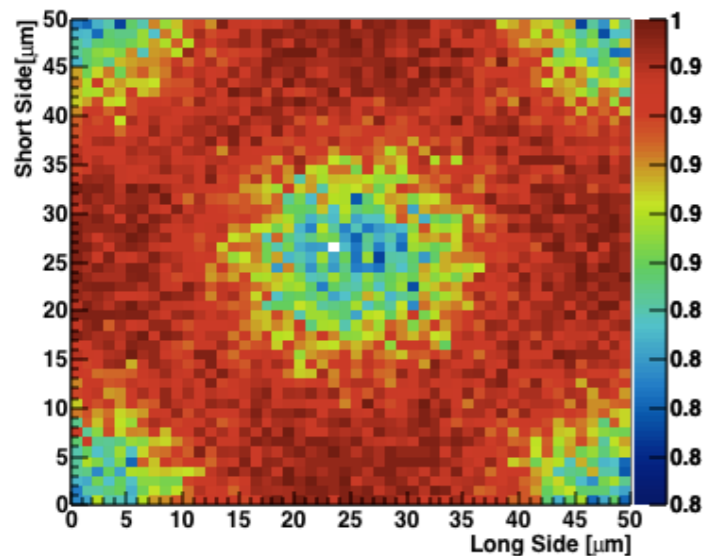
Hit Map DUT 30



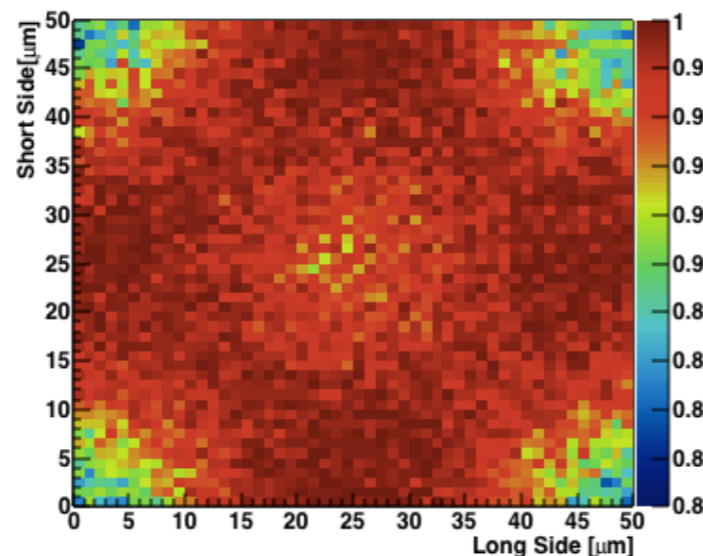
Efficiency Map DUT 30



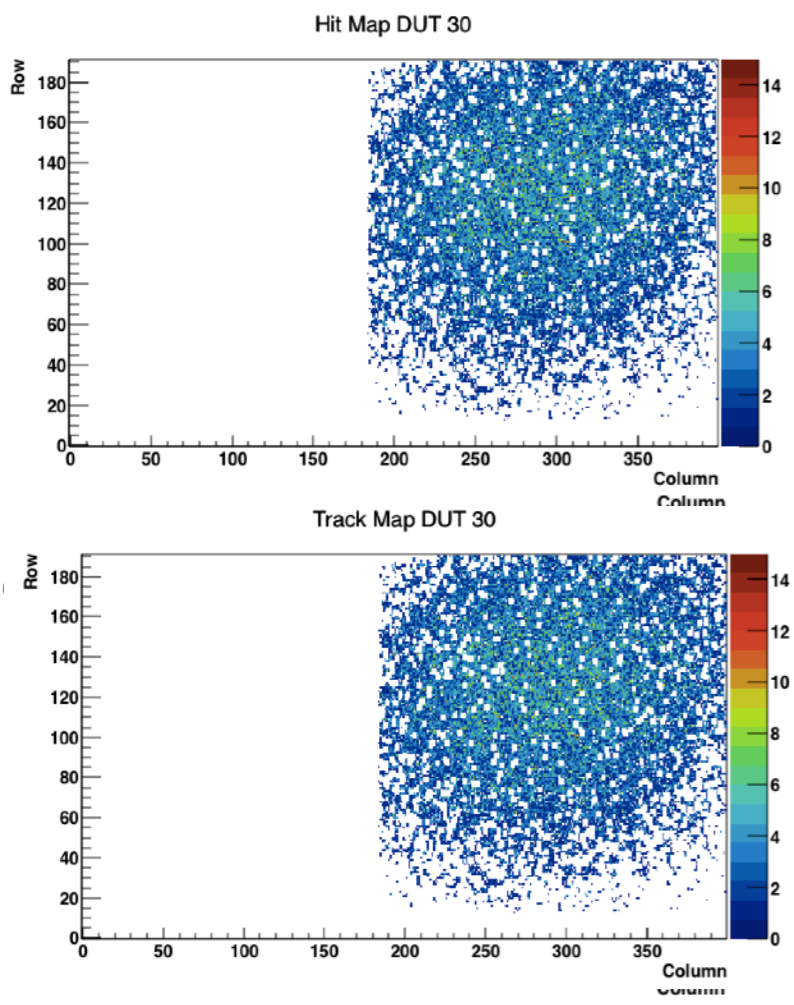
Linear FE



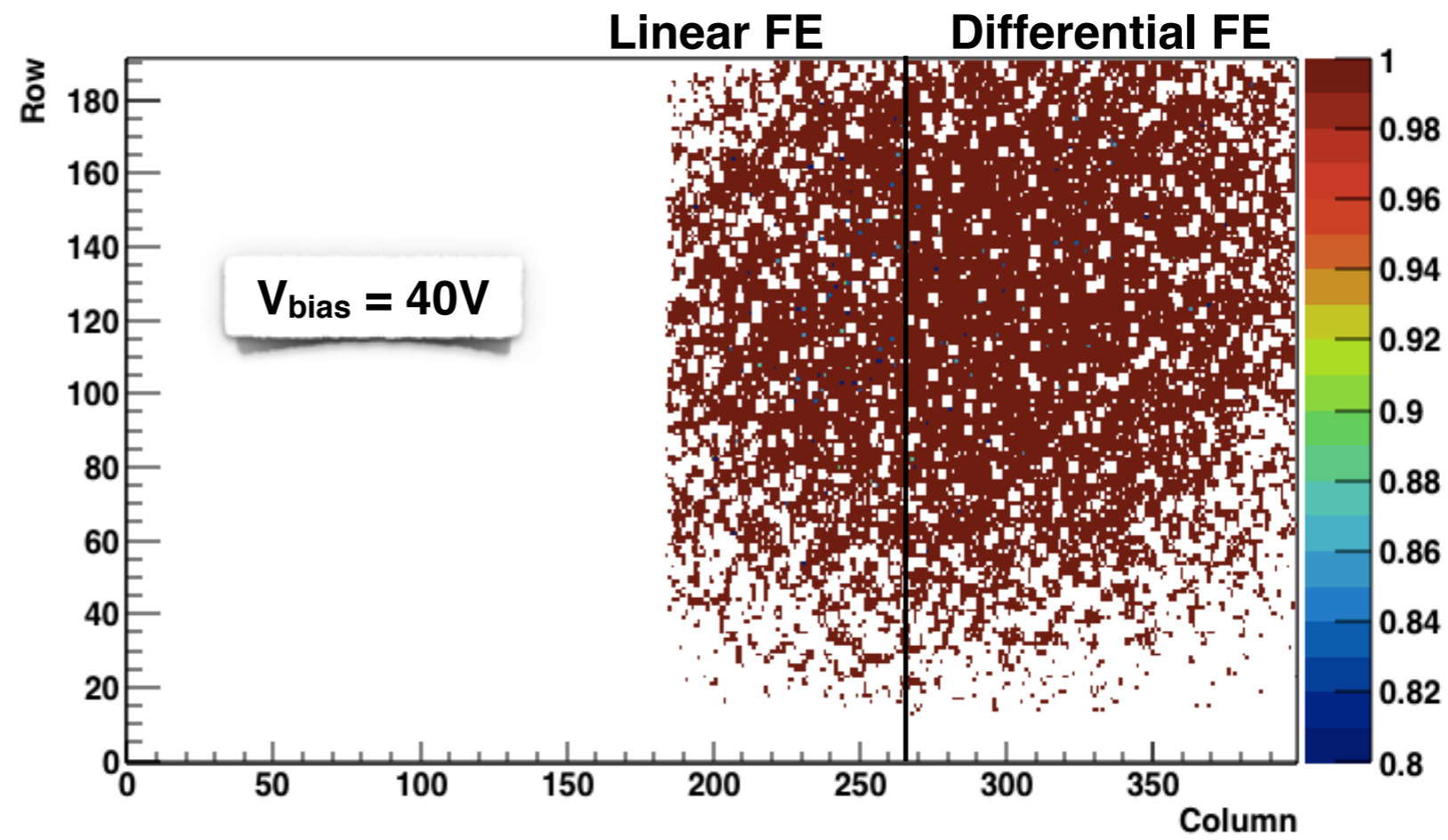
Differential FE



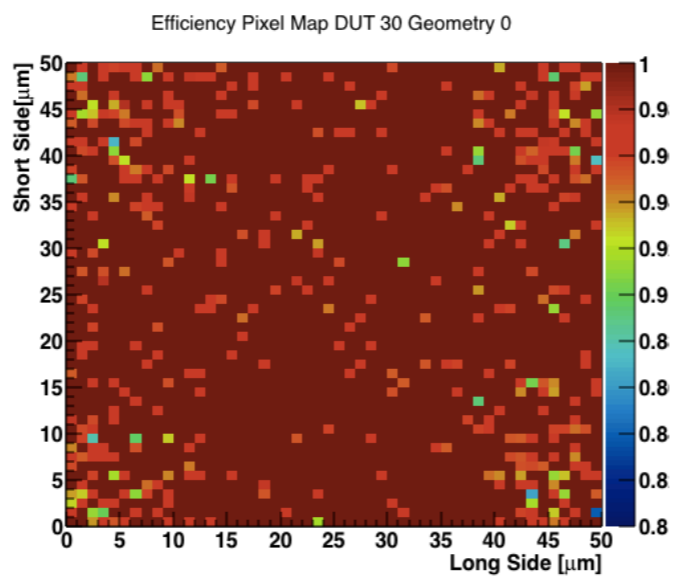
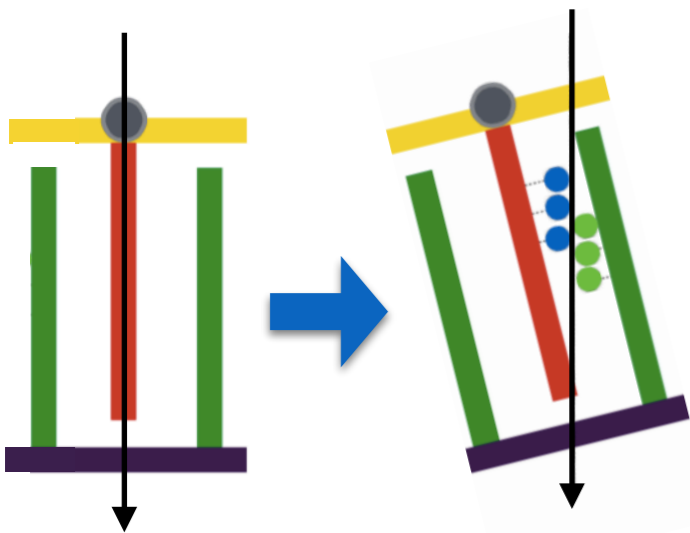
- Efficiency map:
  - Differential FE more uniform and slightly more efficient (lower threshold: 1ke vs 1.6ke linear)
- In pixel map:
  - Inefficiency due to columns very visible
  - Differential FE shows higher efficiency



Efficiency Map DUT 30



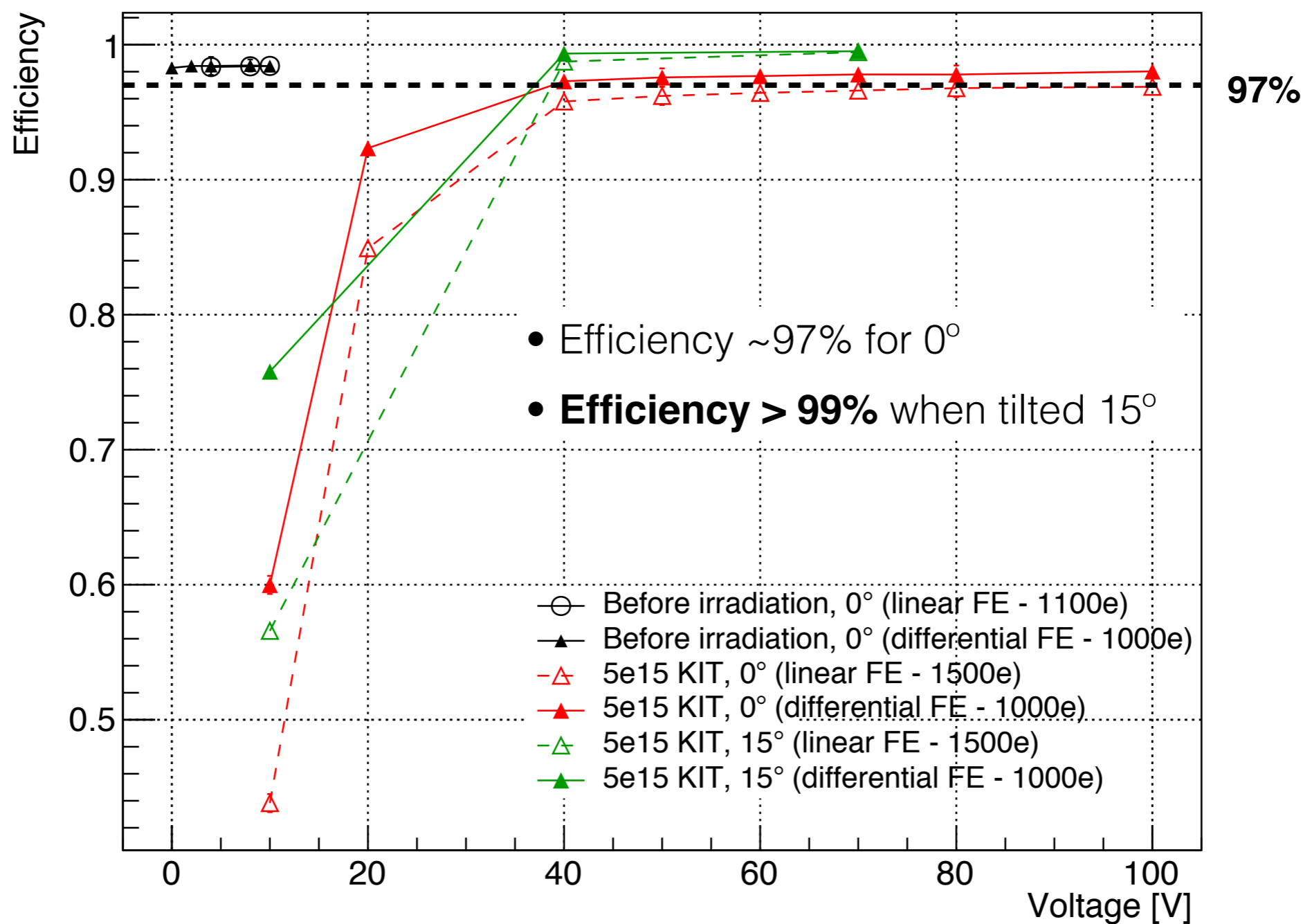
Linear & Differential FE



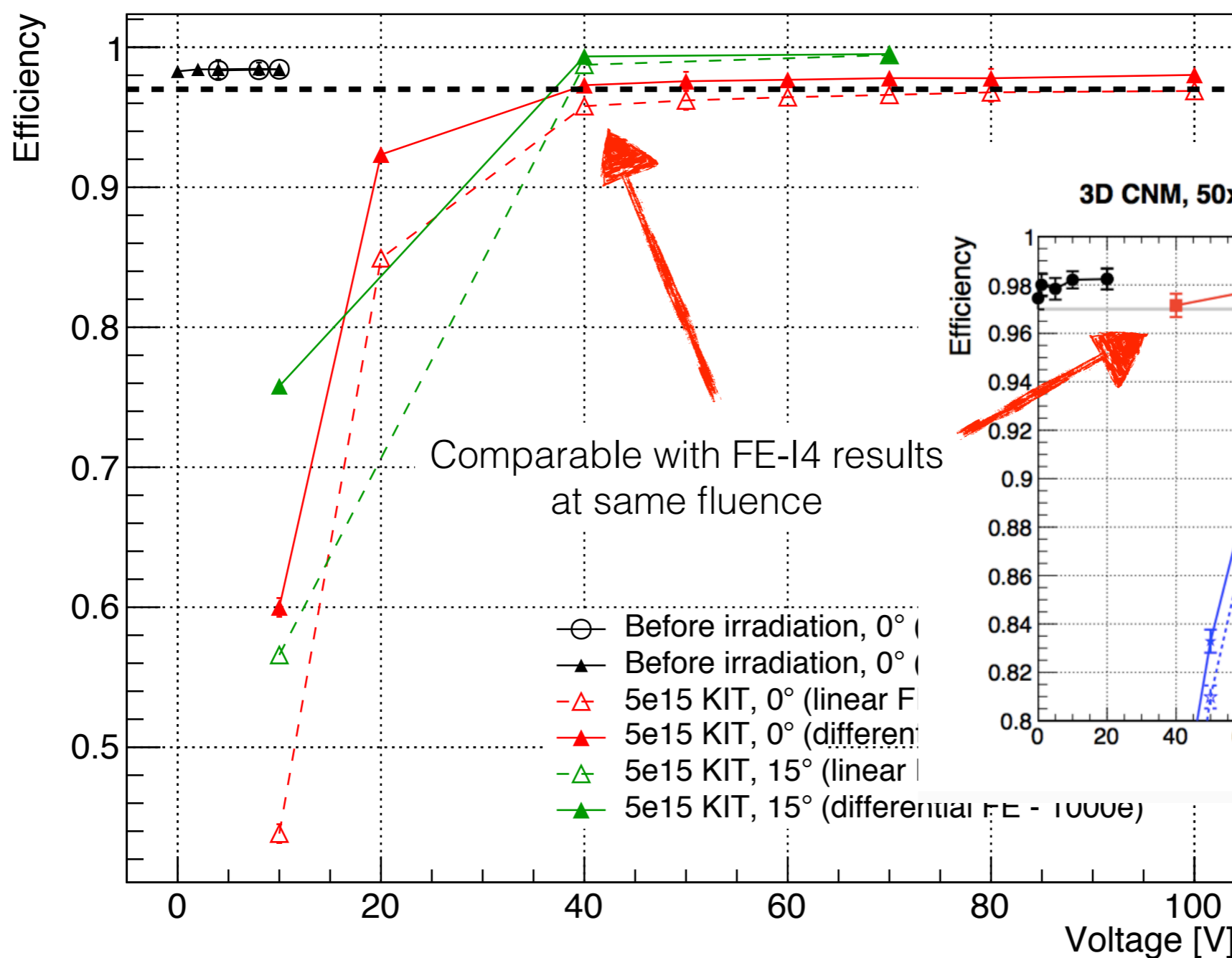
Measuring the module tilted (incidence angle 15°) increases the efficiency by recovering most events where the hit is lost because of the track passing through a column

- Efficiency map:
  - Uniform response of linear and differential FEs
- In pixel map:
  - In both FEs columns inefficiency is less visible

## 3D CNM, RD53A 50x50 $\mu\text{m}^2$ , d=150 $\mu\text{m}$

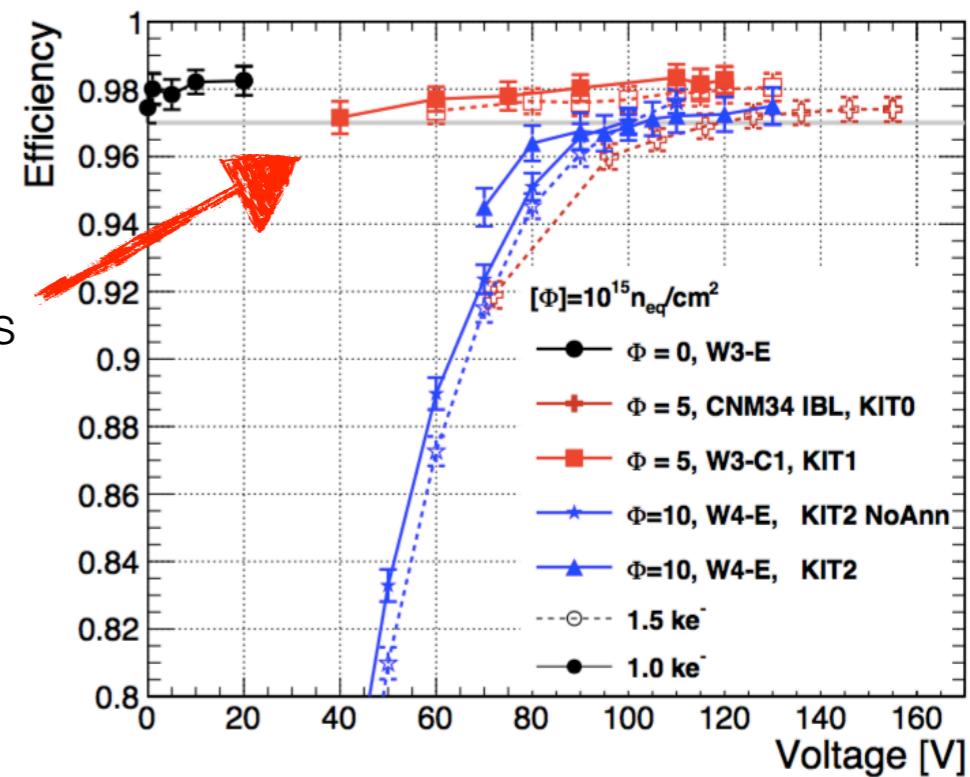


## 3D CNM, RD53A 50x50 $\mu\text{m}^2$ , d=150 $\mu\text{m}$

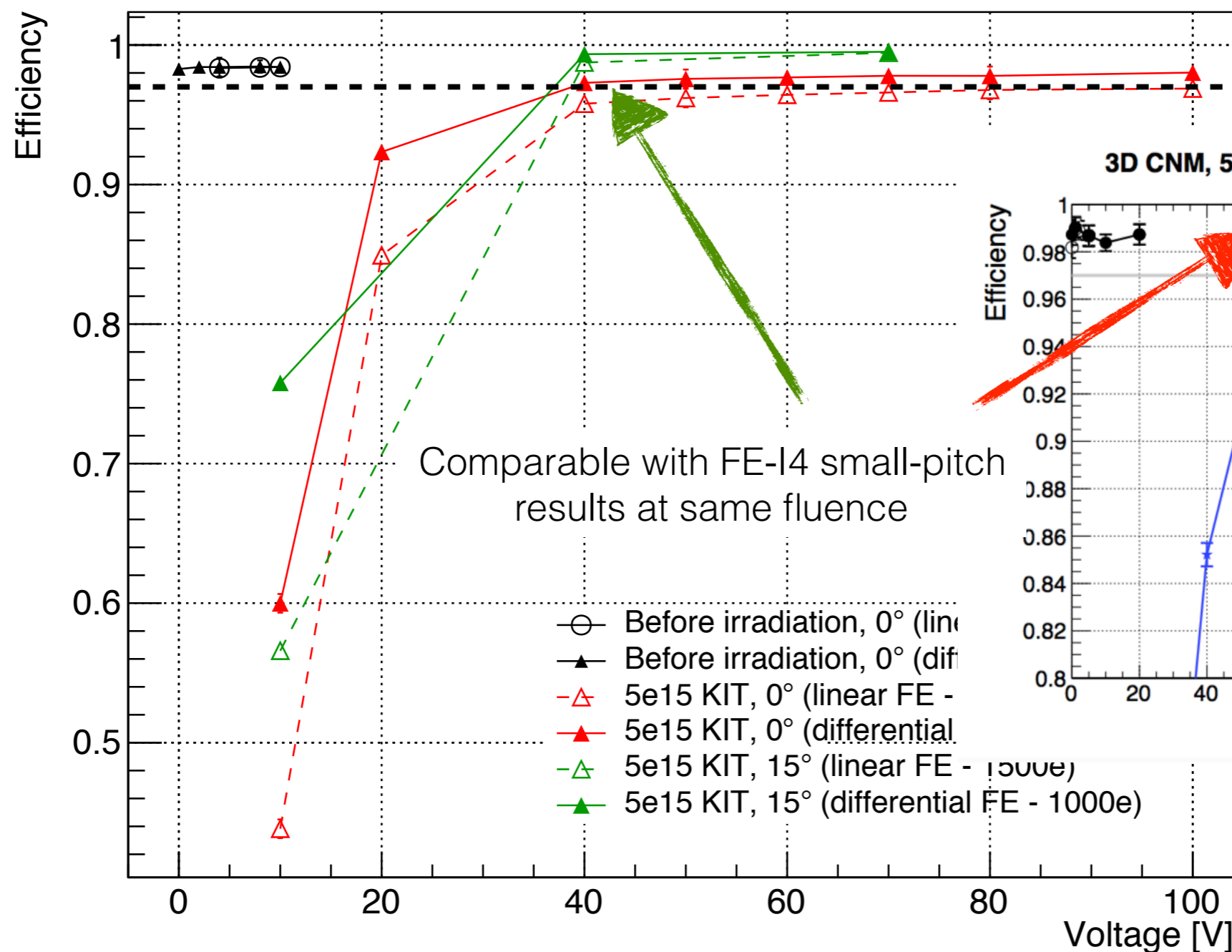


J. Lange et Al., Radiation hardness of small-pitch 3D pixel sensors up to a fluence of 3x10<sup>16</sup> n<sub>eq</sub>/cm<sup>2</sup>, 2018, JINST

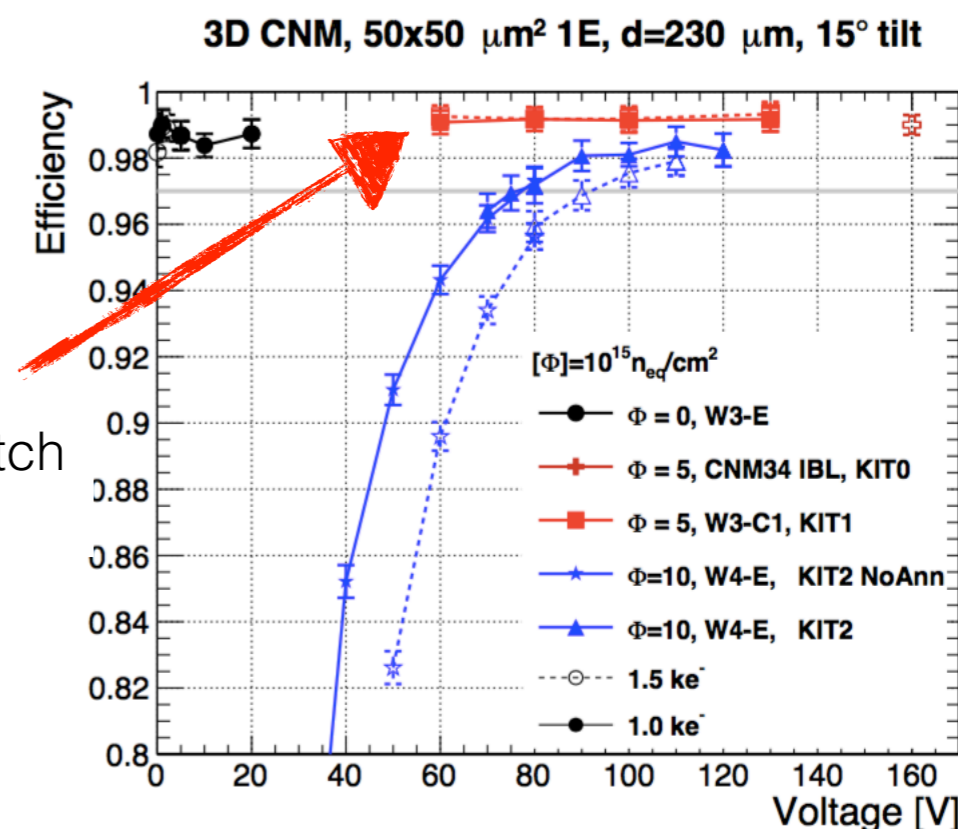
## 3D CNM, 50x50 $\mu\text{m}^2$ 1E, d=230 $\mu\text{m}$ , 0° tilt



## 3D CNM, RD53A 50x50 $\mu\text{m}^2$ , d=150 $\mu\text{m}$

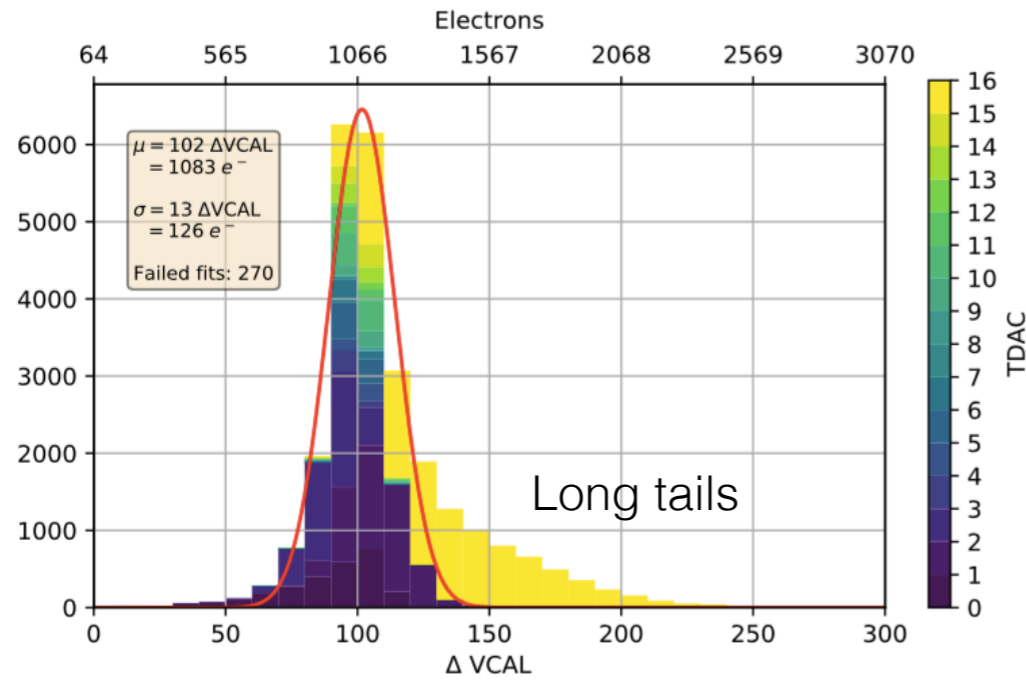


J. Lange et Al., Radiation hardness of small-pitch 3D pixel sensors up to a fluence of 3x10<sup>16</sup> n<sub>eq</sub>/cm<sup>2</sup>, 2018, JINST



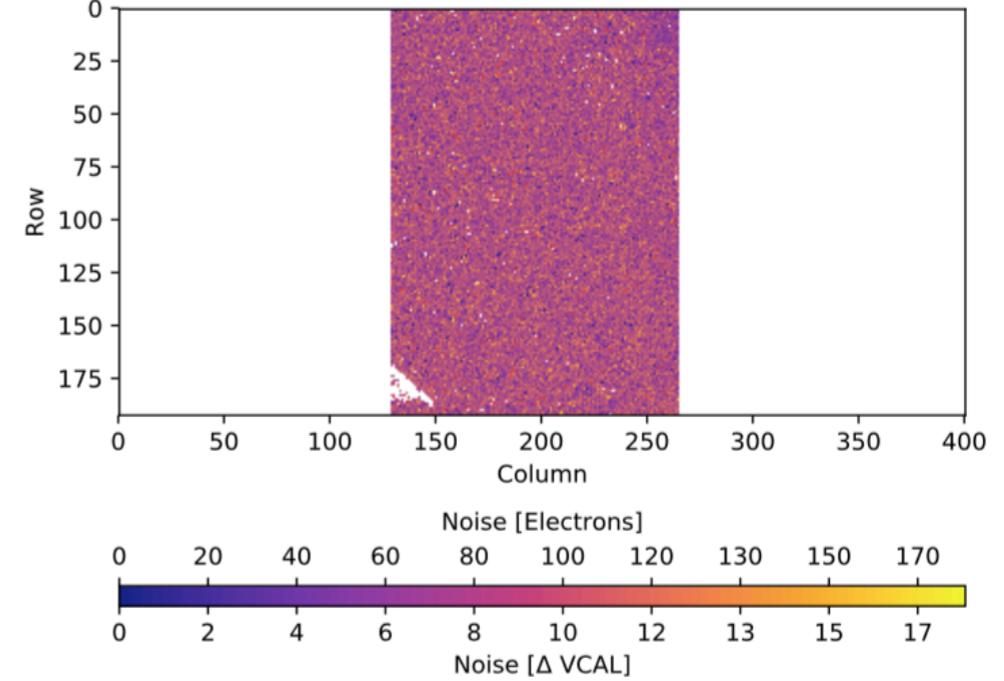
RD53A preliminary Chip S/N: W1\_3\_1\_5e1!

Threshold distribution for enabled pixels



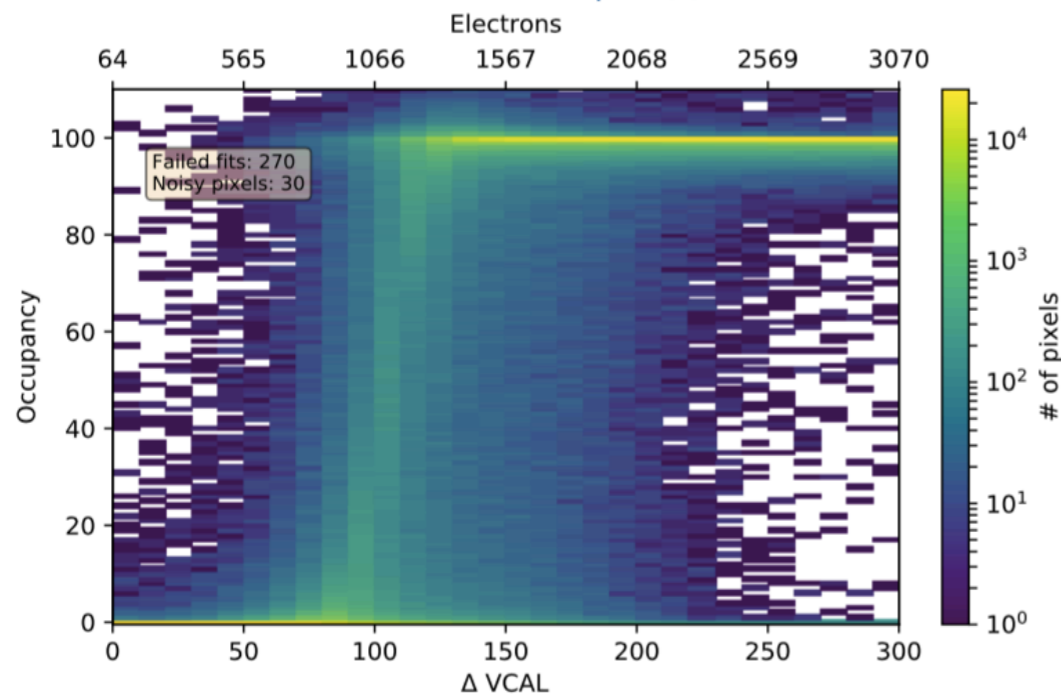
RD53A preliminary Chip S/N: W1\_3\_1\_5e1!

Noise



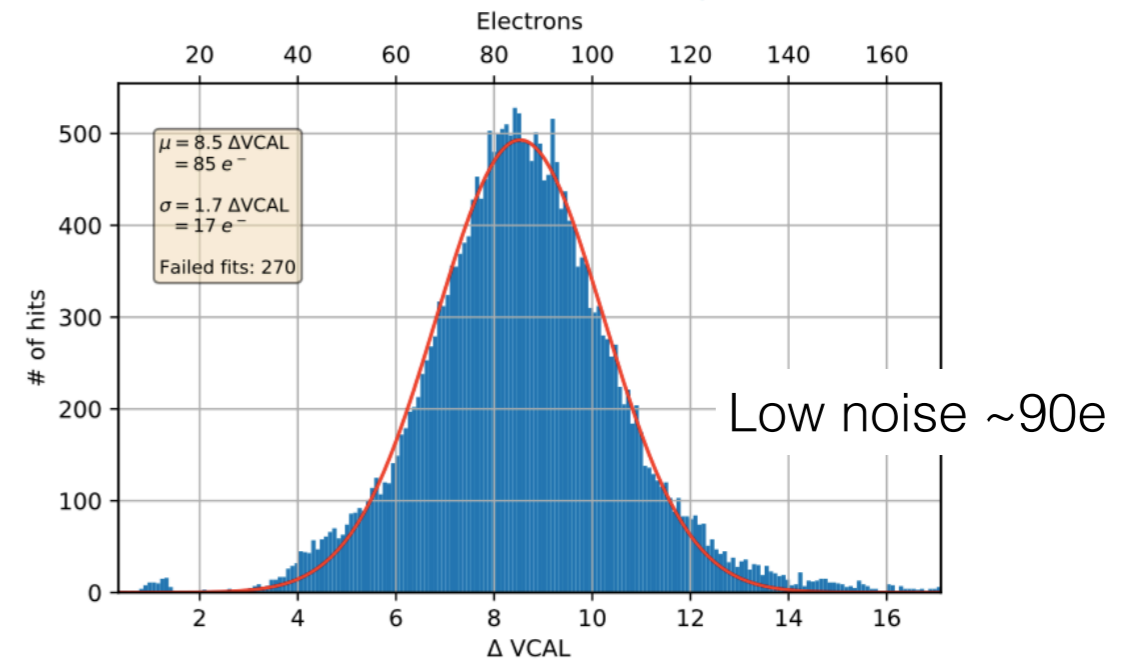
RD53A preliminary Chip S/N: W1\_3\_1\_5e1!

S-curves for 26112 pixel(s)



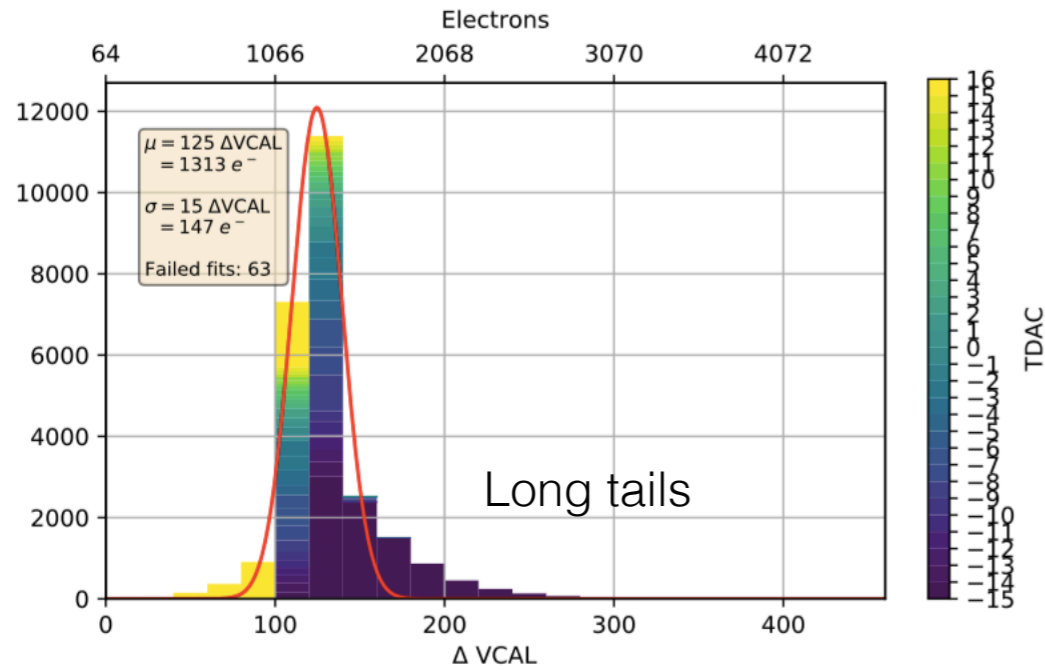
RD53A preliminary Chip S/N: W1\_3\_1\_5e1!

Noise distribution for enabled pixels

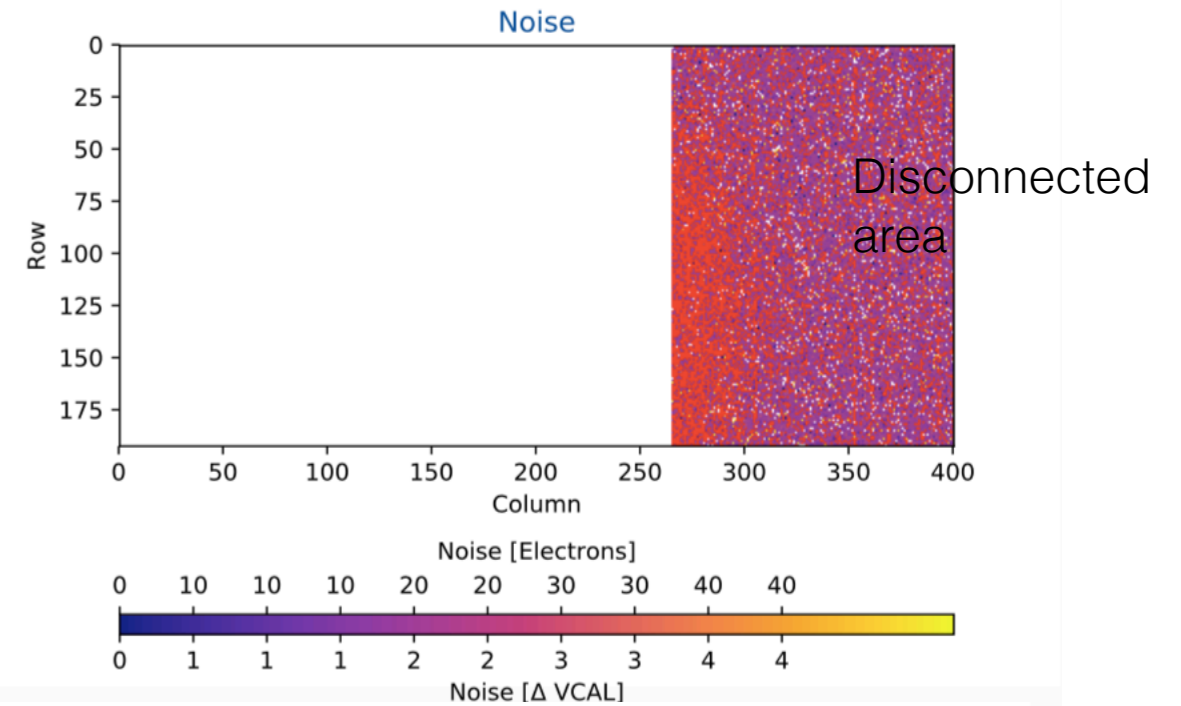


- Differential FE calibrated to threshold of  $\sim 1000e$  with a noise dispersion of  $\sim 90e$

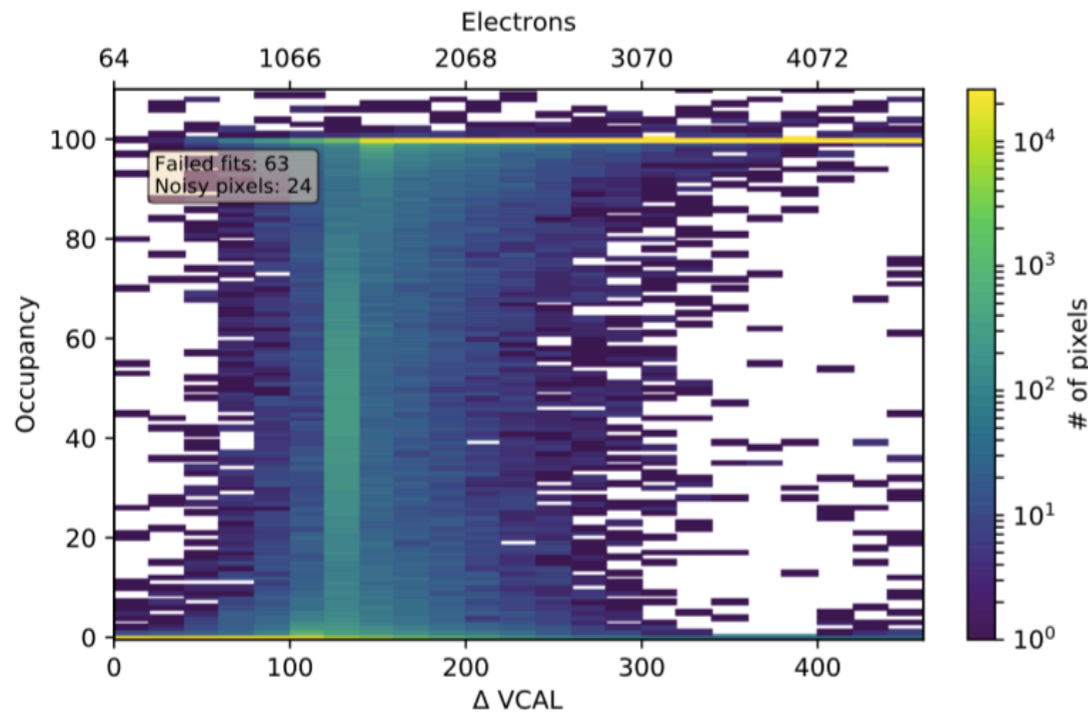
RD53A preliminary Chip S/N: W1\_3\_1\_5e15  
Threshold distribution for enabled pixels



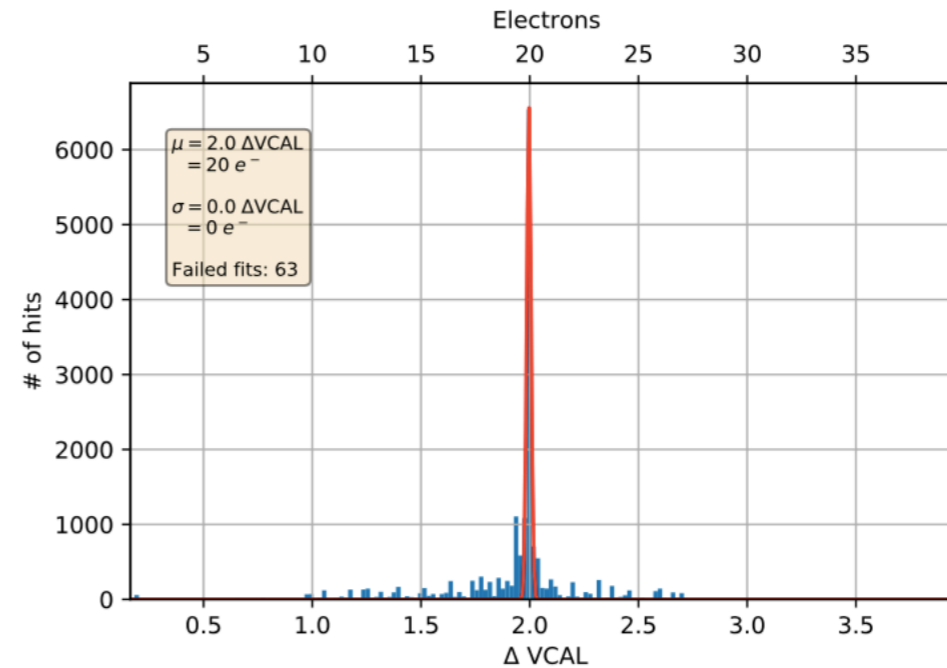
RD53A preliminary Chip S/N: W1\_3\_1\_5e15



RD53A preliminary Chip S/N: W1\_3\_1\_5e15  
S-curves for 26112 pixel(s)

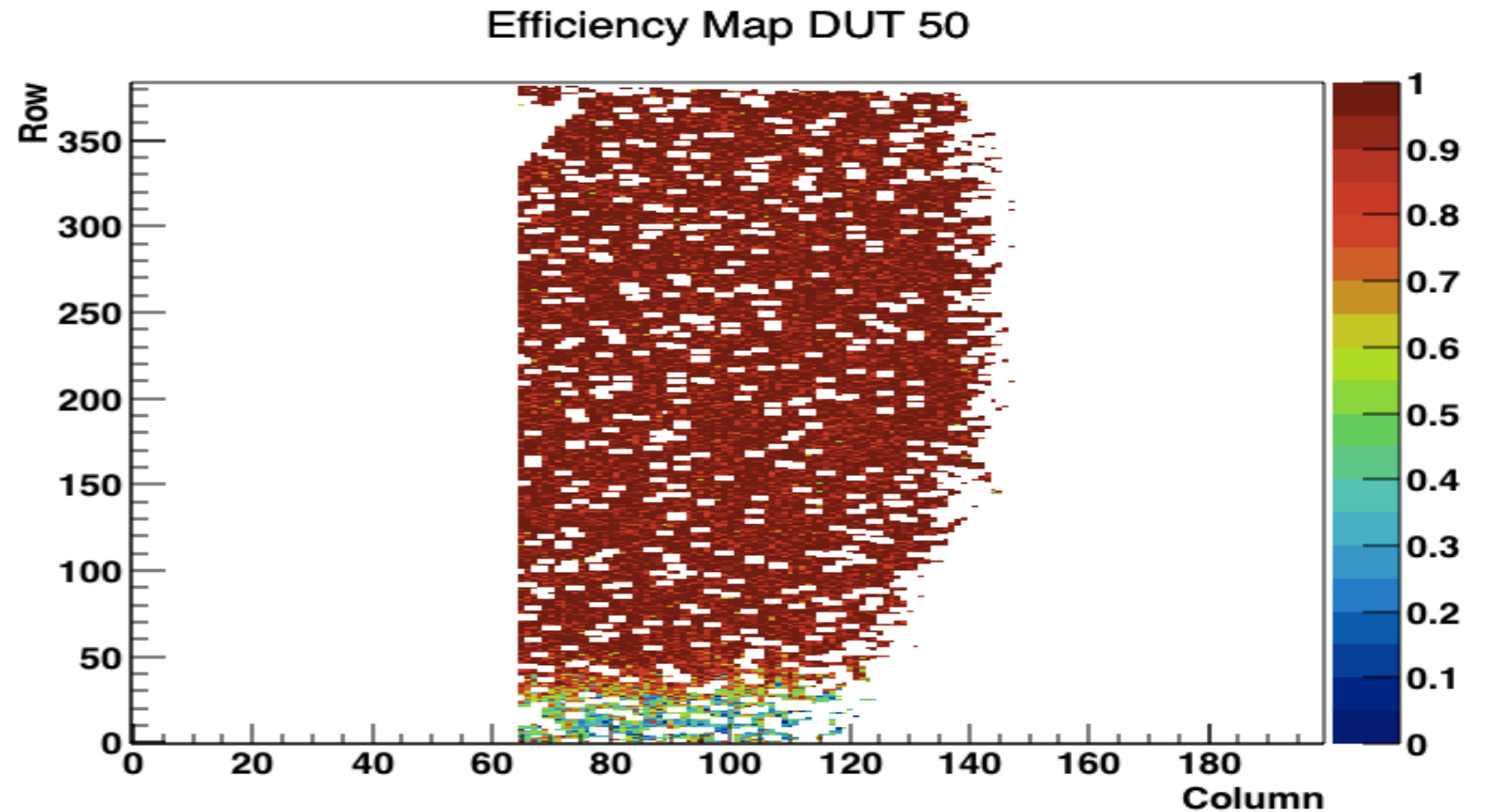
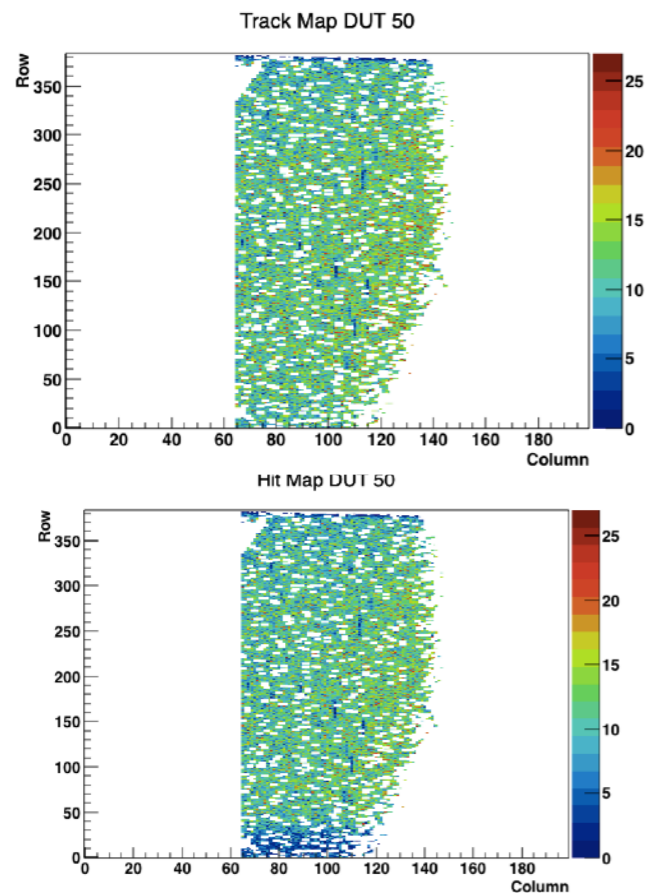


RD53A preliminary Chip S/N: W1\_3\_1\_5e15  
Noise distribution for enabled pixels



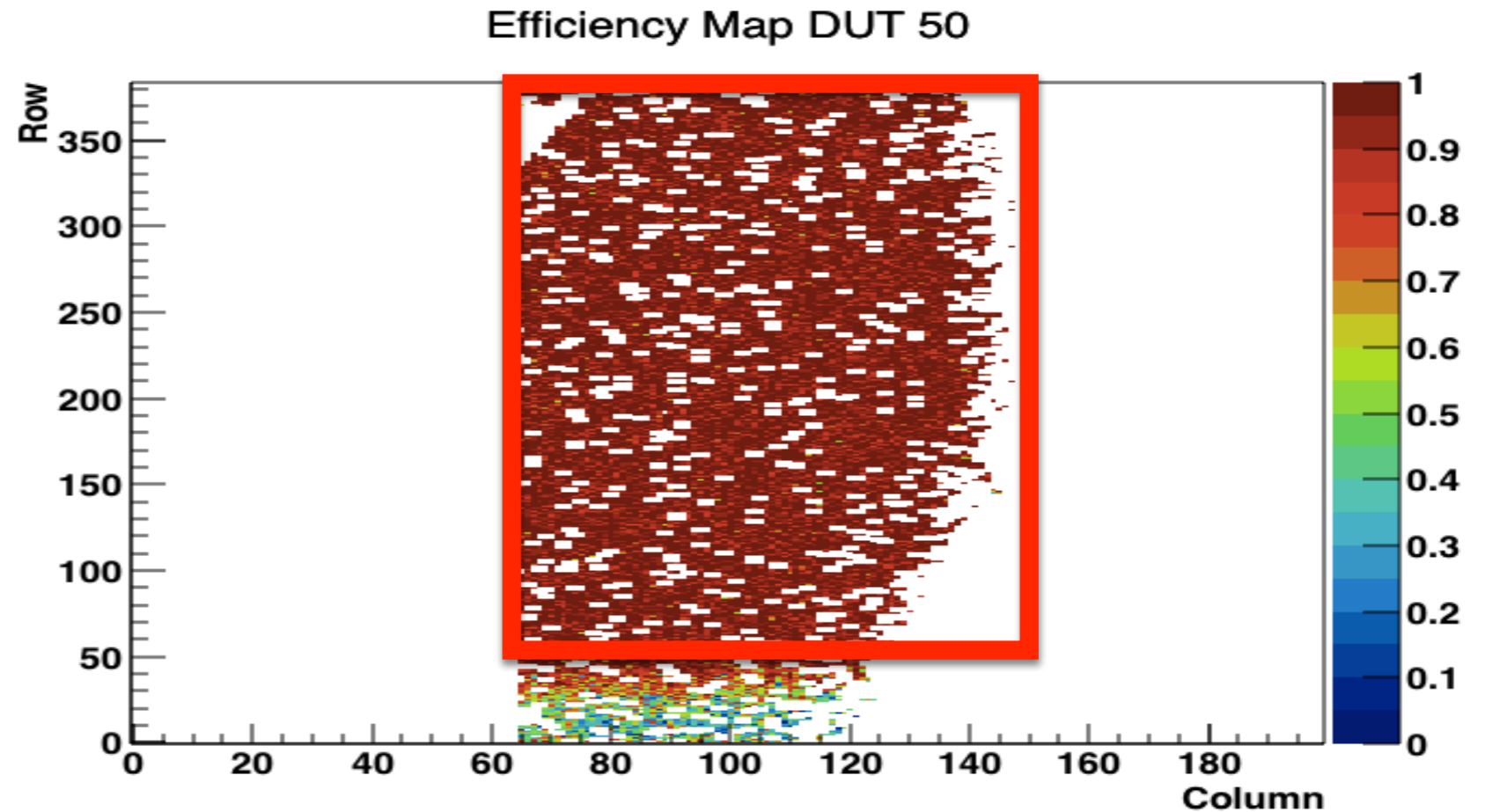
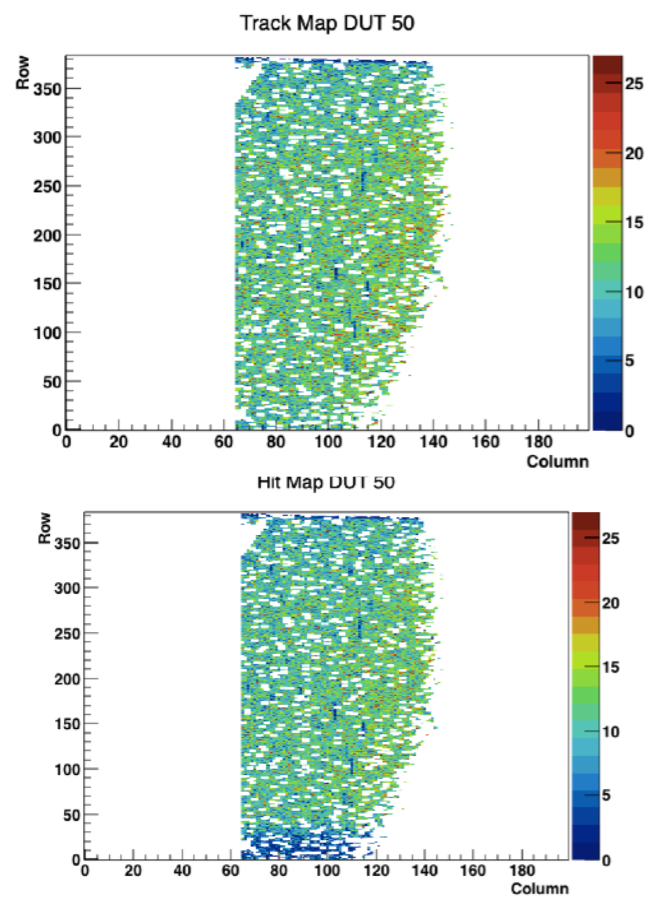
- Differential FE calibrated to threshold of  $\sim 1200e$





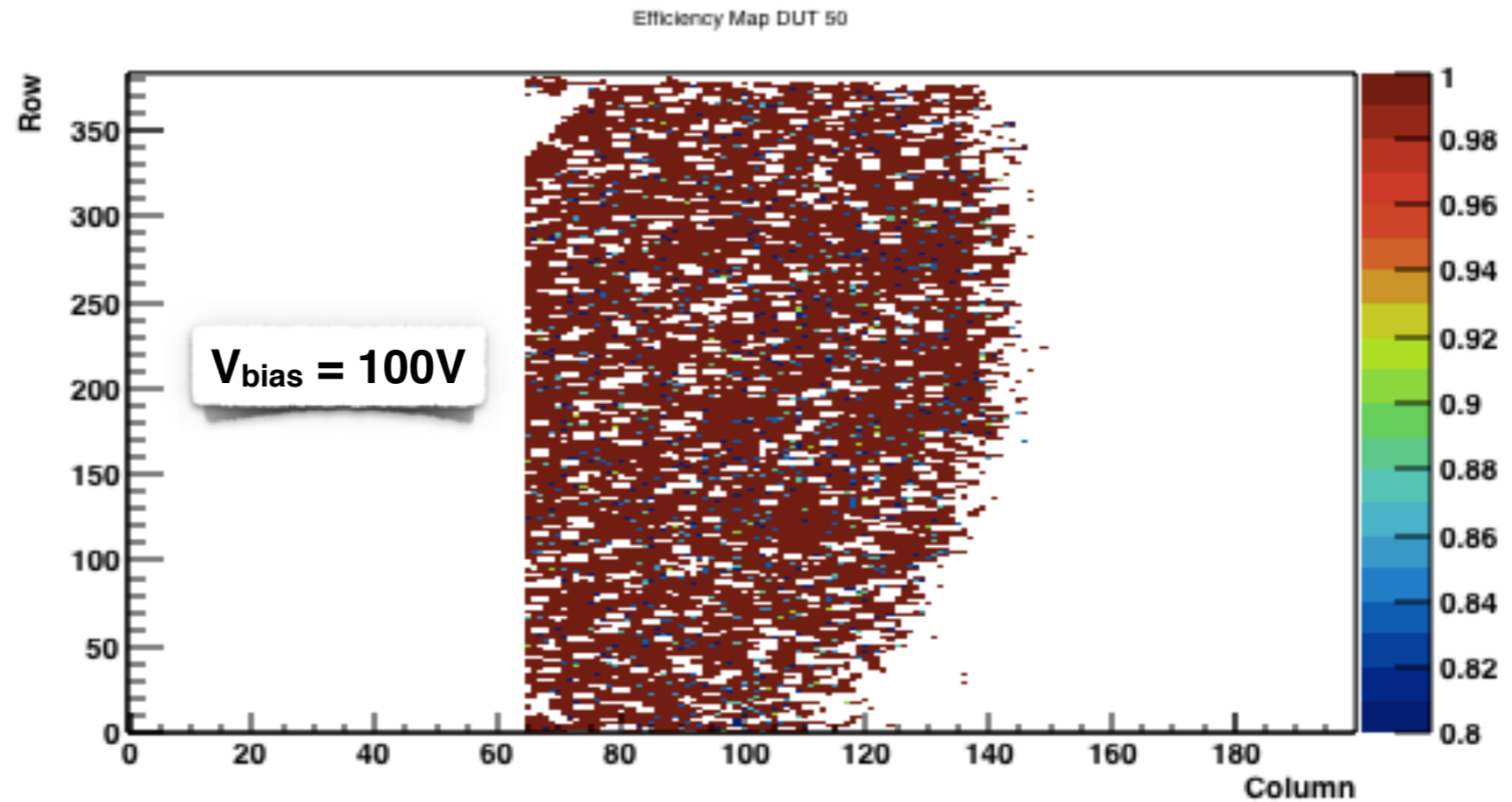
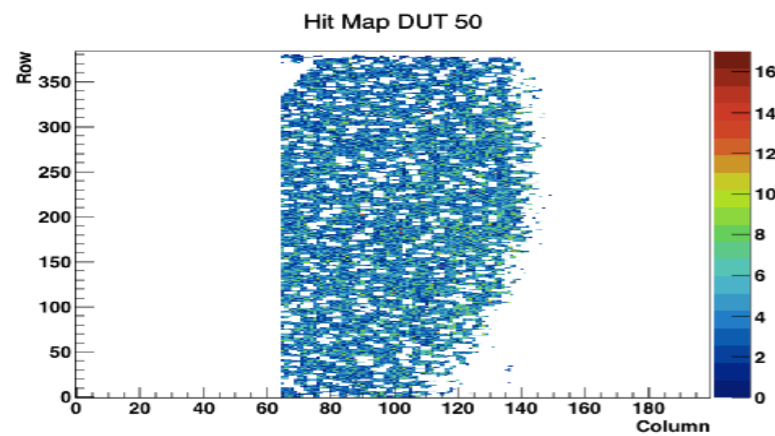
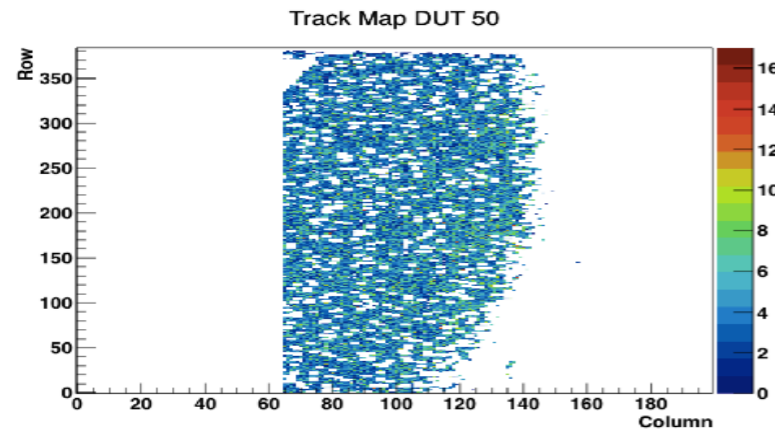
Efficiency map:

- A lower efficiency band is visible at bottom part of the sensor (disappearing at higher voltages)
  - Suspect it has been irradiated to higher fluences due to a scattering of proton beam on the metal protection of the board during irradiation
    - ➔ As already observed for irradiations of AFP module at KIT ([J. Lange et al., JINST 10 C03031 \(2015\)](#))
- Restricted region of interest to calculate efficiency

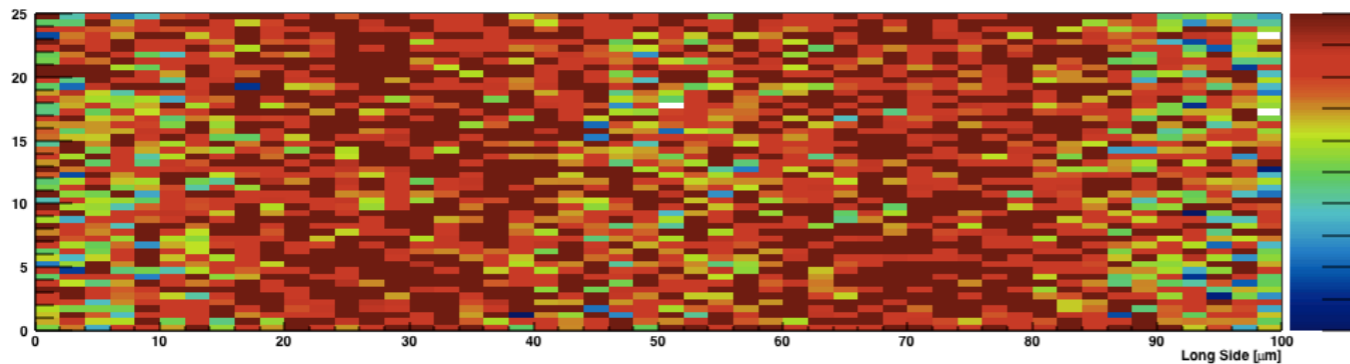


Efficiency map:

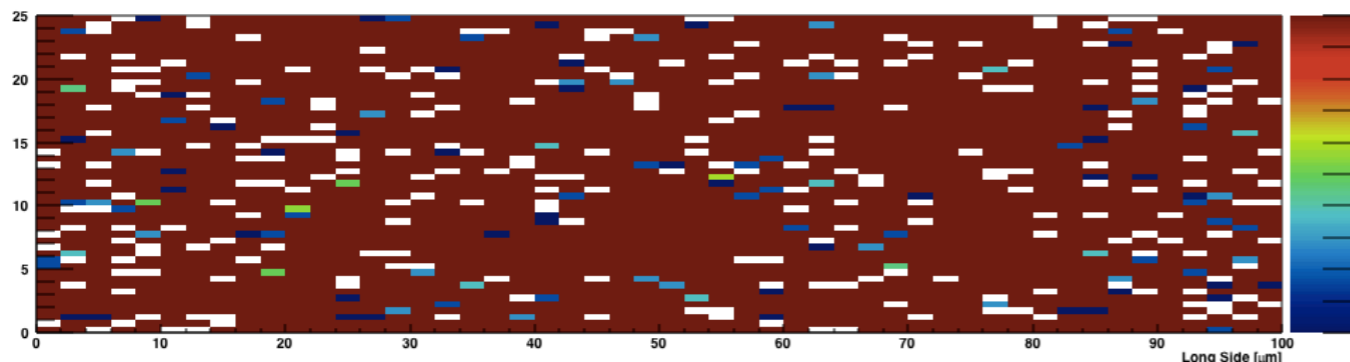
- A lower efficiency band is visible at bottom part of the sensor (disappearing at higher voltages)
  - Suspect it has been irradiated to higher fluences due to a scattering of proton beam on the metal protection of the board during irradiation
    - ➔ As already observed for irradiations of AFP module at KIT ([J. Lange et al., JINST 10 C03031 \(2015\)](#))
- Restricted region of interest to calculate efficiency



Efficiency Pixel Map DUT 50 Geometry 1

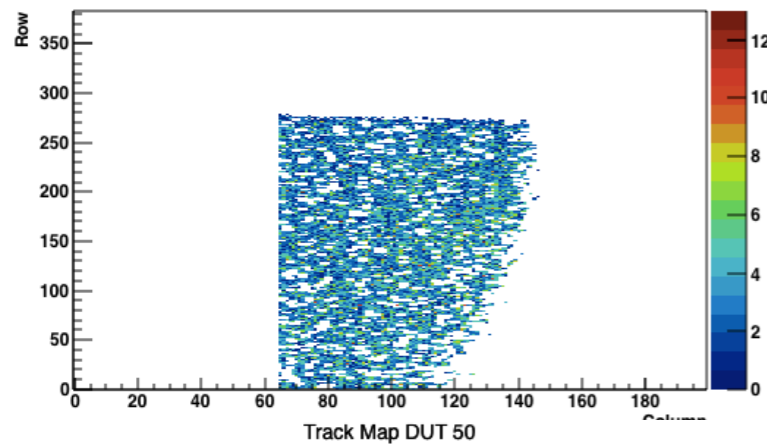


Efficiency Pixel Map DUT 50 Geometry 2

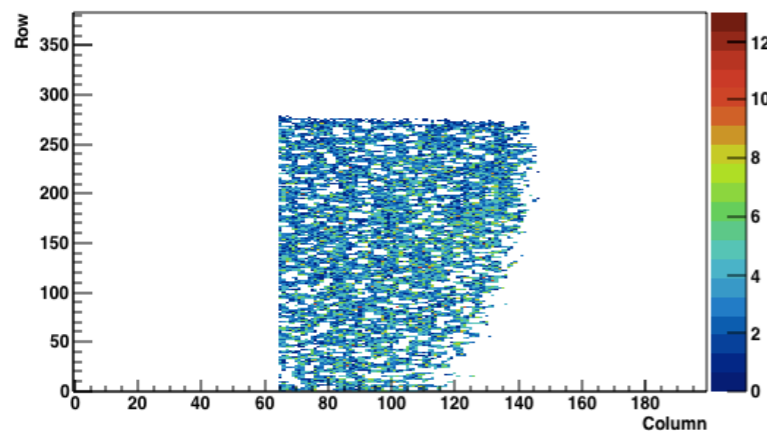


- Efficiency map:
  - Visible disconnected areas
  - Uniform overall efficiency
- In pixel map:
  - LIN: visible inefficiency due to columns
    - Smearred, probably due to not perfect alignment
  - DIFF: more uniform efficiency distribution
    - low statistic due to large portion of FE disconnected

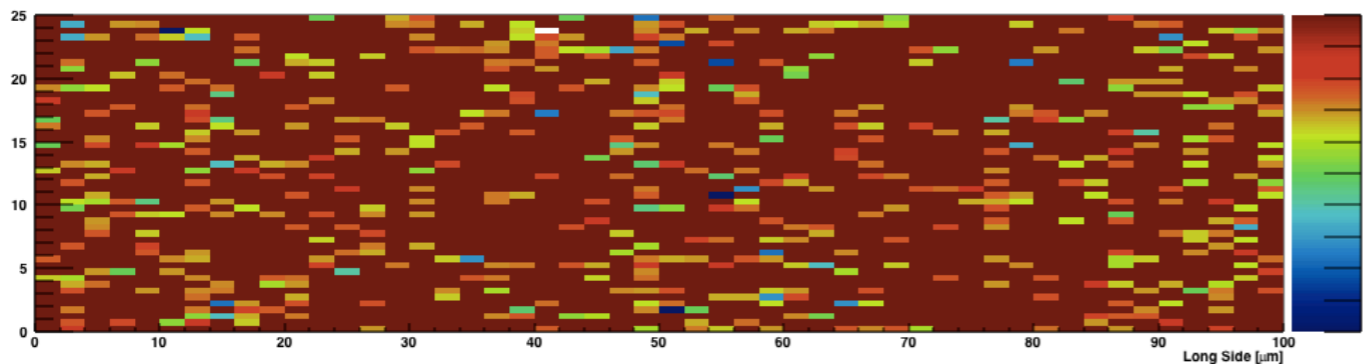
Hit Map DUT 50



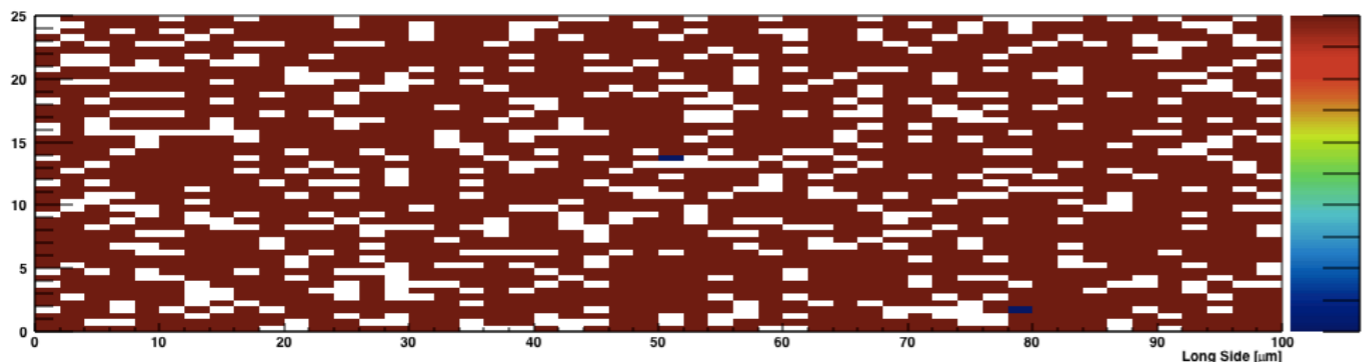
Track Map DUT 50



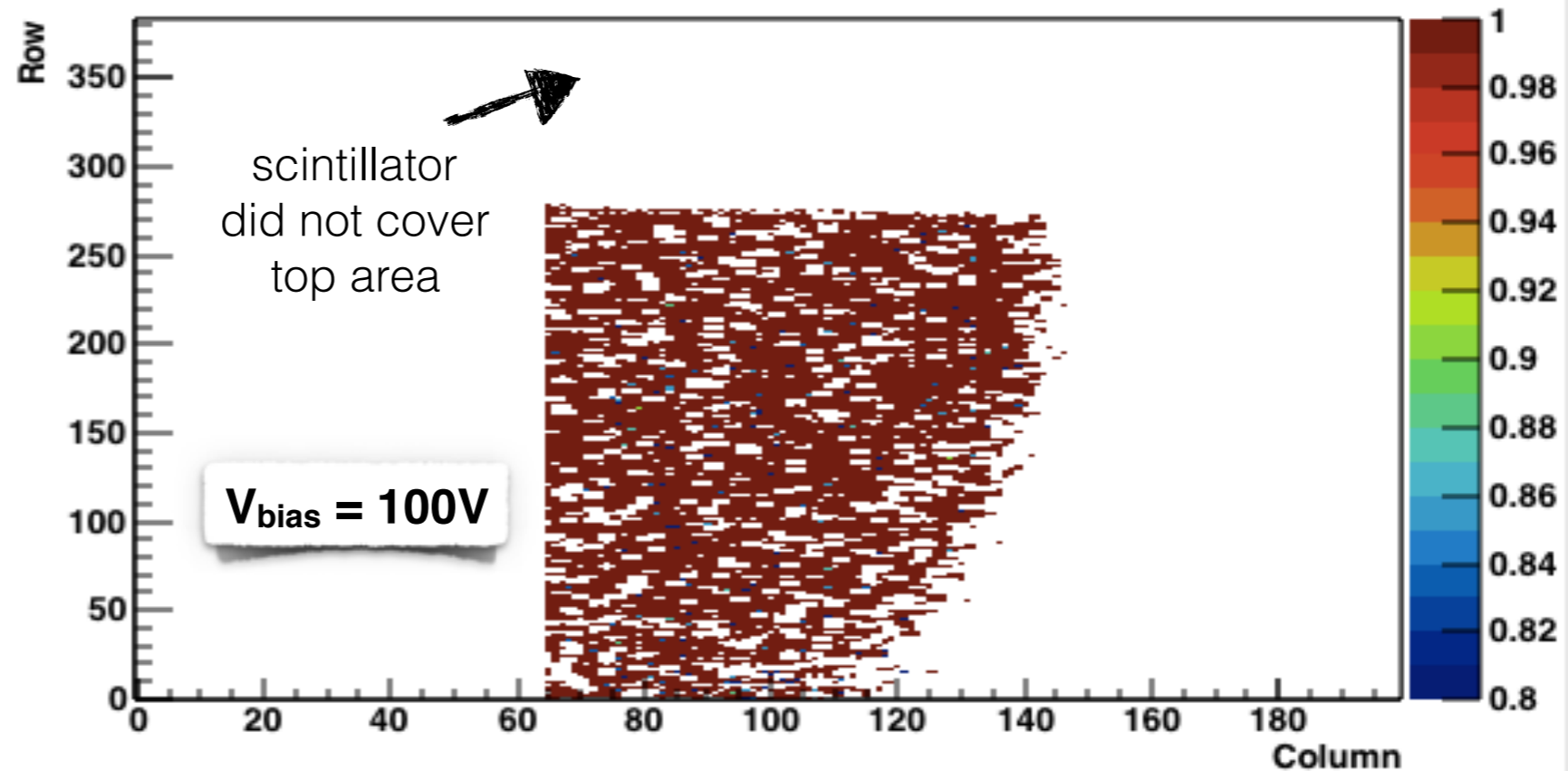
Efficiency Pixel Map DUT 50 Geometry 1



Efficiency Pixel Map DUT 50 Geometry 2

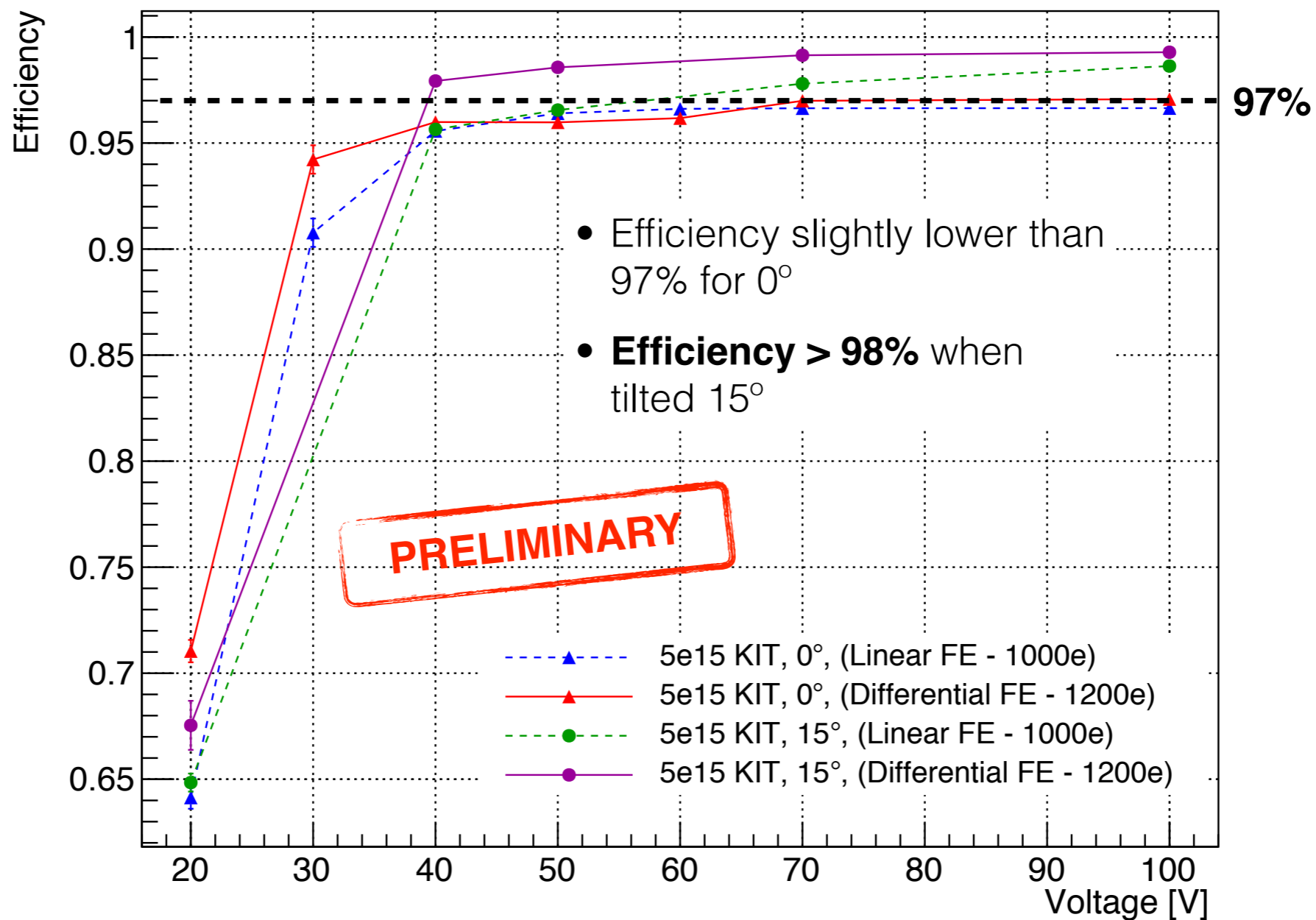


Efficiency Map DUT 50

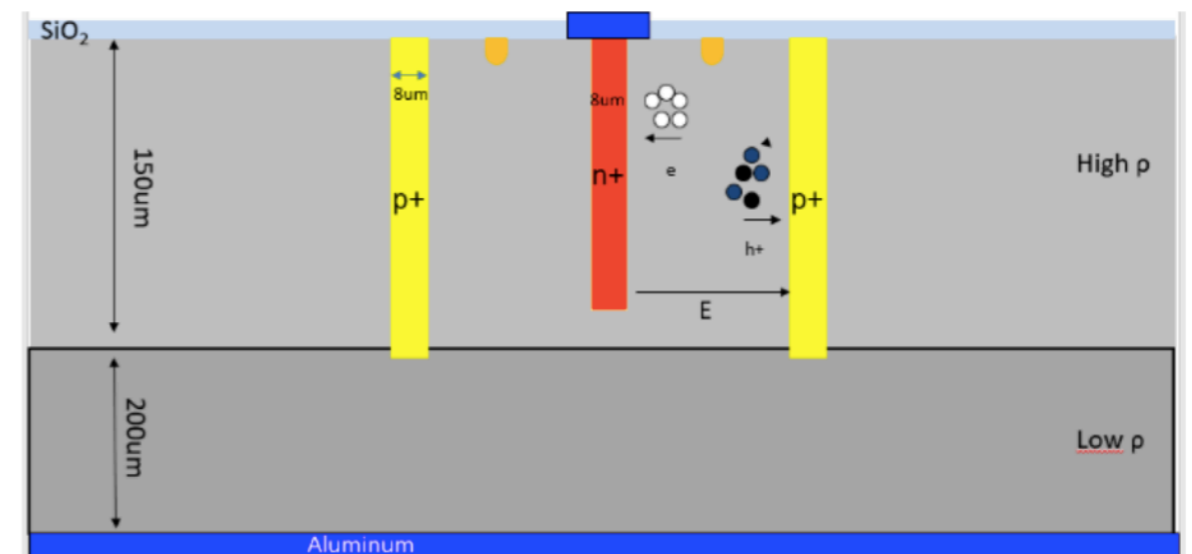
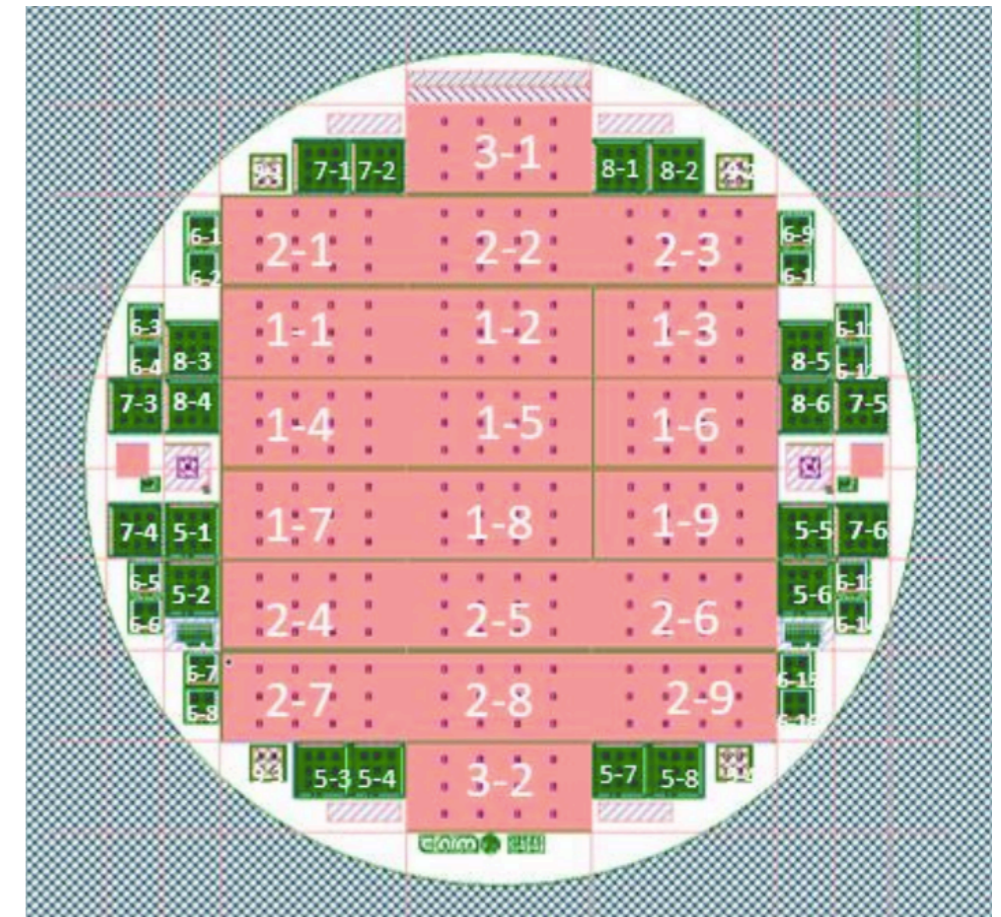


- Efficiency map:
  - Visible disconnected areas
  - Uniform overall efficiency
- In pixel map:
  - LIN: inefficiency due to columns undistinguishable
  - DIFF: more uniform efficiency distribution
    - low statistic due to large portion of FE disconnected

RD53A 3D CNM, 25x100 $\mu\text{m}^2$  1E, d=150 $\mu\text{m}$ , 5e15  $n_{\text{eq}}/\text{cm}^2$  KIT



- On going CNM run (AIDA)
  - Design fully compatible with 3D sensor specifications
    - Single Sided process on **Si-Si** wafers,
    - 150  $\mu\text{m}$  active thickness, 200  $\mu\text{m}$  handle wafer
    - 130  $\mu\text{m}$  column depth, 8 $\mu\text{m}$  column diameter
  - 9 RD53 50x50  $\mu\text{m}^2$  (1-x)
  - 9 RD53 25x100  $\mu\text{m}^2$  2E (2-x)
  - 2 RD53 25x100  $\mu\text{m}^2$  1E (3-x)
- New sensors ready early next year (February)



- **First results of 150  $\mu\text{m}$  thick RD53A uniformly irradiated to  $5\text{e}15\text{ n}_{\text{eq}}/\text{cm}^2$  at KIT**
  - **50x50  $\mu\text{m}^2$** 
    - 97% hit efficiency at 40V at 1ke for differential FE at 1ke
    - Slightly lower (96.6%) for linear FE at 1.5ke
    - **About 99% hit efficiency in both linear and differential FEs when tilted  $15^\circ$**
  - **25x100  $\mu\text{m}^2$  1E**
    - ~97% hit efficiency at 70V for both FEs
    - **Respectively about 98% and 99% hit efficiency for linear differential FE at 70V when tilted  $15^\circ$**

## Future plans:

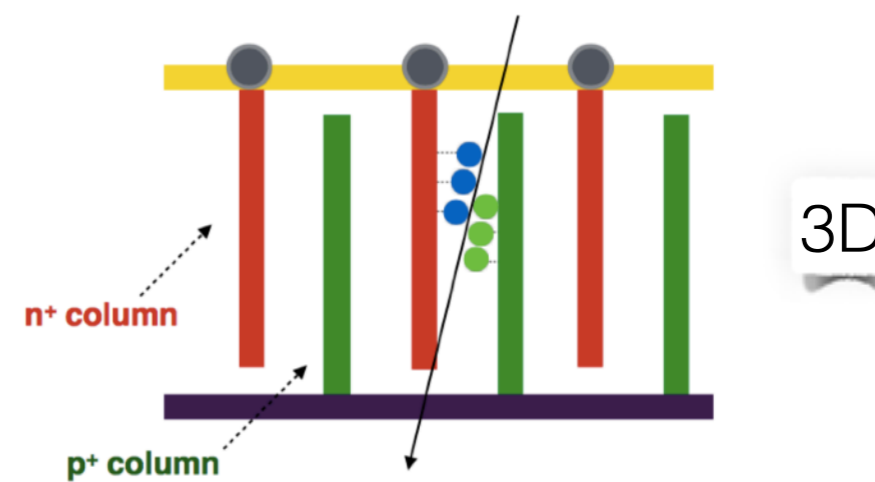
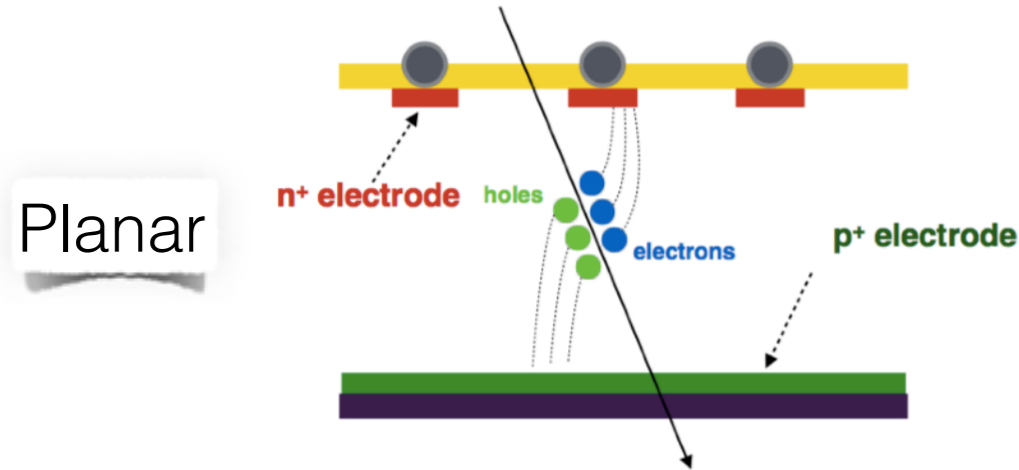
- Analysis and new measurements on going for modules irradiated at PS to  $1\text{e}16\text{ n}_{\text{eq}}/\text{cm}^2$
- One module sent for **irradiation at CYRIC (Japan) to  $1\text{e}16\text{ n}_{\text{eq}}/\text{cm}^2$**
- New CNM run of **Si-Si wafer** on going
  - New sensors early next year

---

**Thanks for your attention!**

**Backup**





- **Inter-electrode distance** = sensor **thickness**
  - ↕ number of produced electrons-holes pairs = initial **signal**
- How to maintain high efficiency though whole lifetime?
  - **Thin detector**: reduce probability of electron-hole pairs being trapping by bulk defects by decreasing inter-electrode distance
    - BUT -> smaller signal ☹️
  - **Increase bias voltage** to keep bulk depleted
    - BUT -> higher power dissipation ☹️

- Electrodes not placed on the surface but columns inserted in the bulk
- Electrons-holes pairs drift towards columns
- **Inter-electrode distance decoupled from sensor thickness** -> inter-electrode distance can be reduced without decreasing the signal!

**Pros:**

- Lower depletion voltage
- Faster charge collection
- Smaller trapping probability



**Radiation hardness!!**

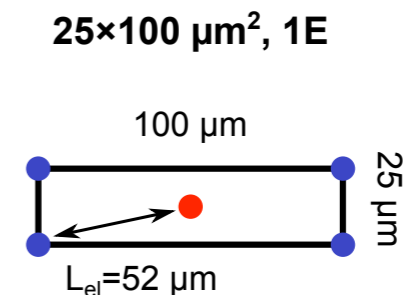
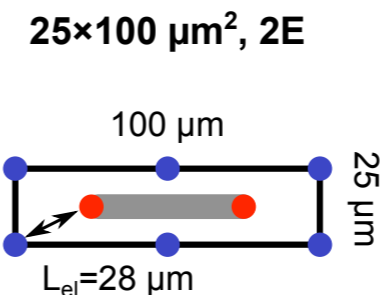
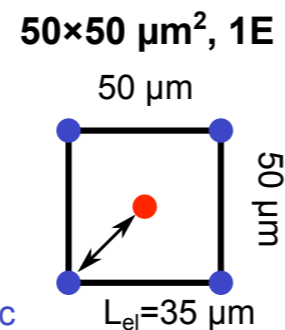
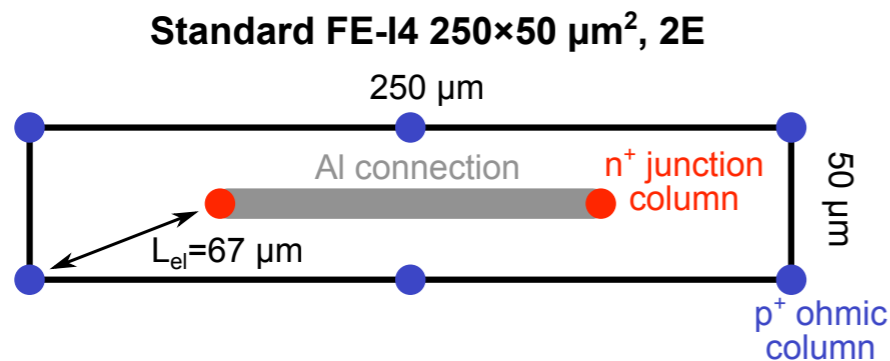
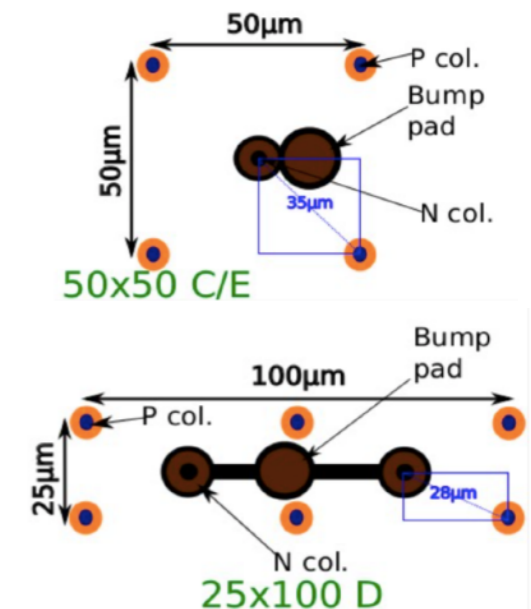
**Con:**

- Very complex production process

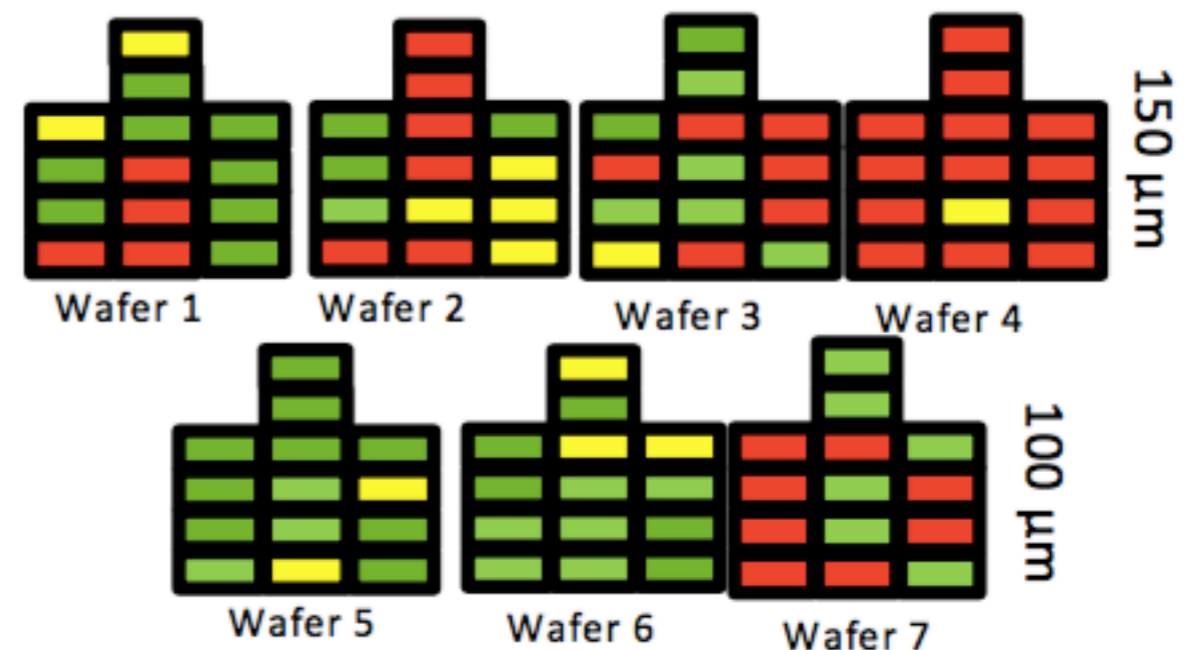
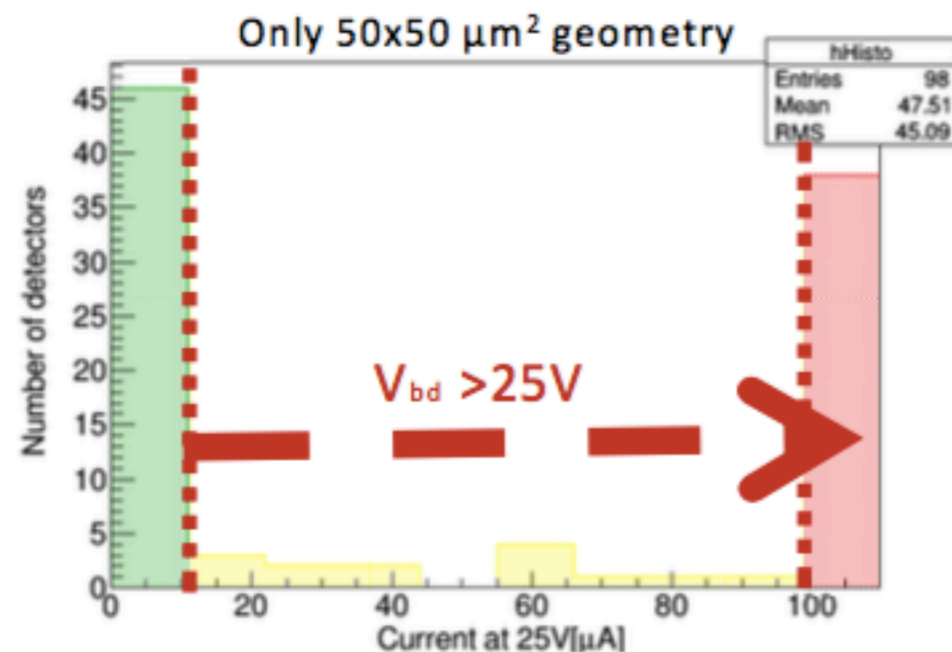
- ✓ Collected charge, production yield
- ✗ Hit occupancy, especially for forward tracks

## Pixel size?

- IBL: standard FE-I4 50x250  $\mu\text{m}^2$  with two n+ columns, 67  $\mu\text{m}$  inter-electrode distance
- HL-LHC options:
  - **50x50  $\mu\text{m}^2$** : easier to produce, 35  $\mu\text{m}$  inter-electrode distance
  - **25x100  $\mu\text{m}^2$** : better resolution in  $\phi$ 
    - 2 n+ columns (2E): smaller inter-electrode distance (28  $\mu\text{m}$ ) but difficult to fit solder bump inside
    - 1 n+ columns (1E): less problematic, but radiation hard enough with 53  $\mu\text{m}$  inter-electrode distance?



- **Overall promising yield of 50x50  $\mu\text{m}^2$  design**
  - Breakdown voltage  $> 25\text{ V}$  and Leakage current  $< 10\ \mu\text{A}$
- **Large number of sensors available for characterisation**
  - Including also 25x100  $\mu\text{m}^2$  geometries both 1E and 2E



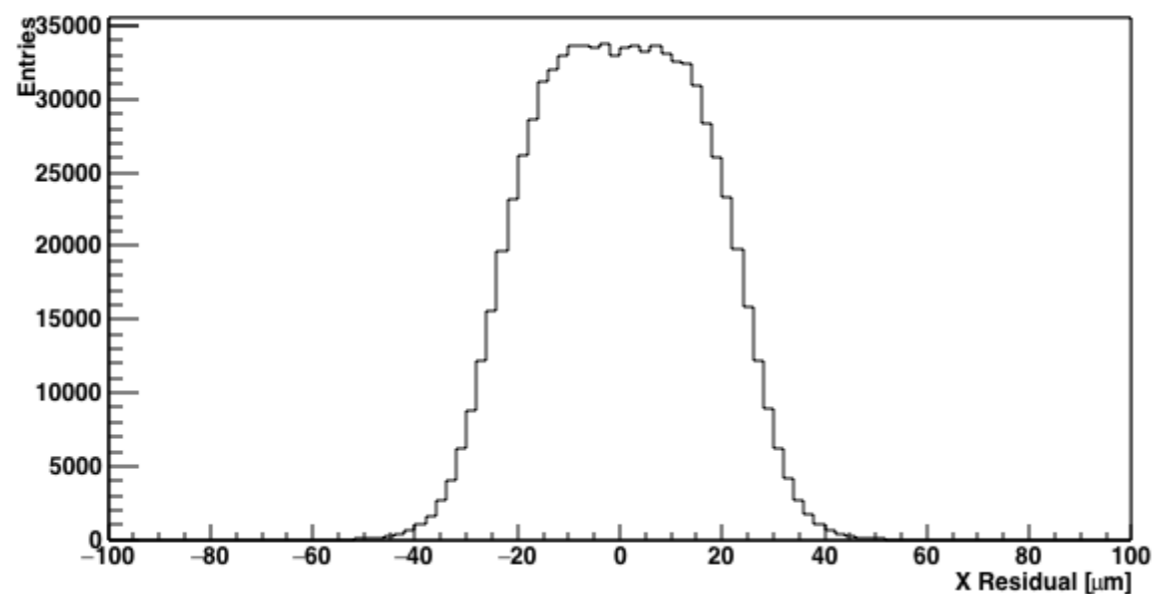
- **Next steps towards the first RD53A 3D modules:**
  - Removing of the temporary metal (done)
  - Electroless UBM at CNM -> **done for Wafer 4, ongoing on 2 more wafers**
  - Flip-chip to RD53A chips at IFAE
  - Assembly and wire-bonding to SCC
  - Characterisation with the YARR system

## Cluster Size 1

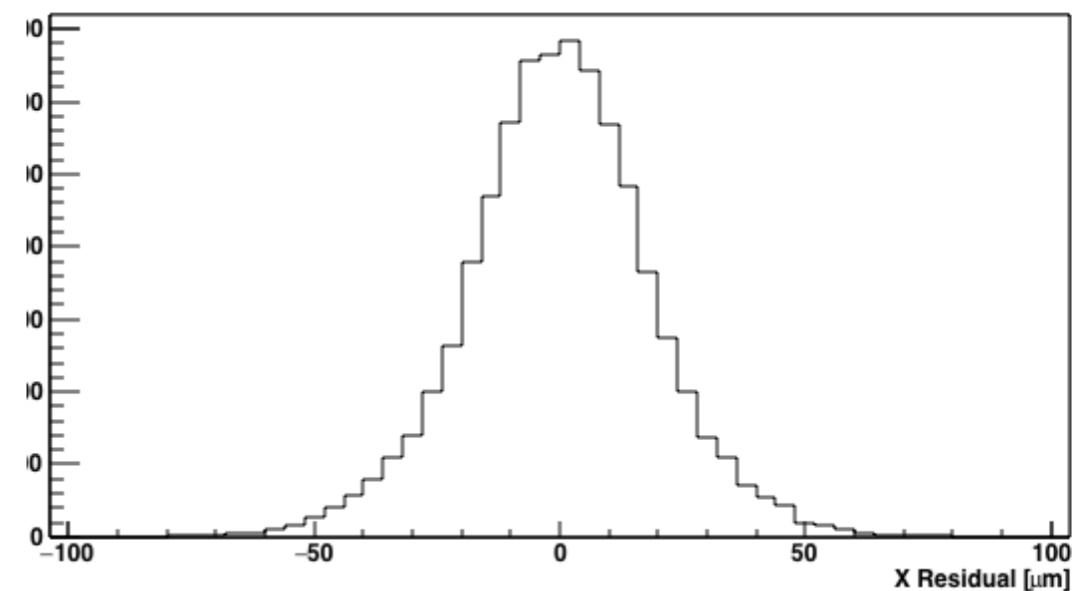
## Cluster Size 2

X

Cluster Size X 1 qmean Algorithm DUT 30

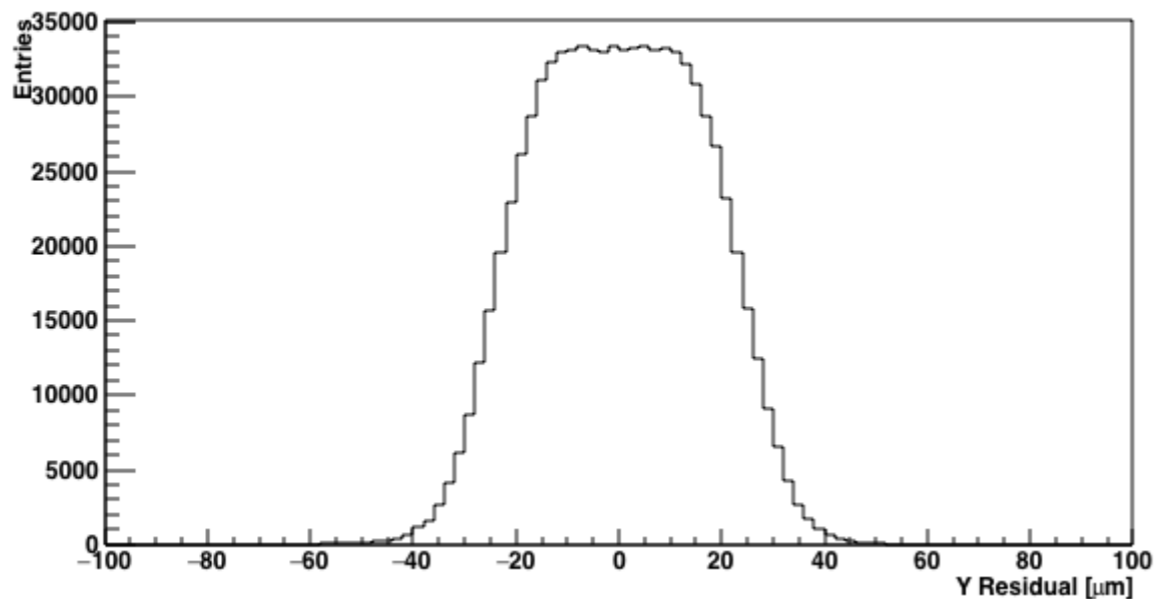


Cluster Size X 2 qmean Algorithm DUT 30

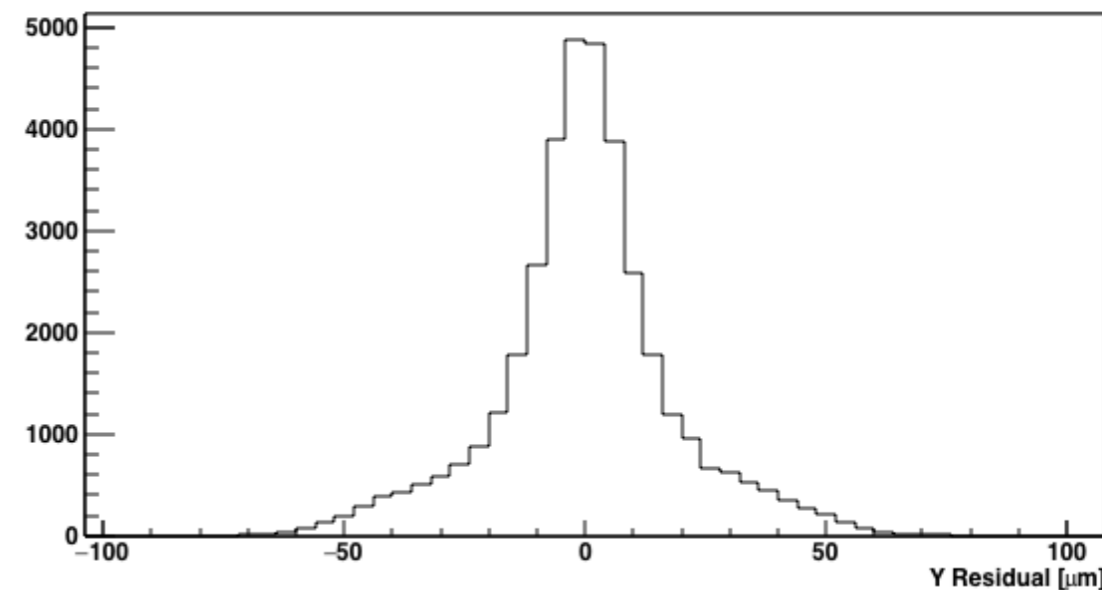


Y

Cluster Size Y 1 qmean Algorithm DUT 30

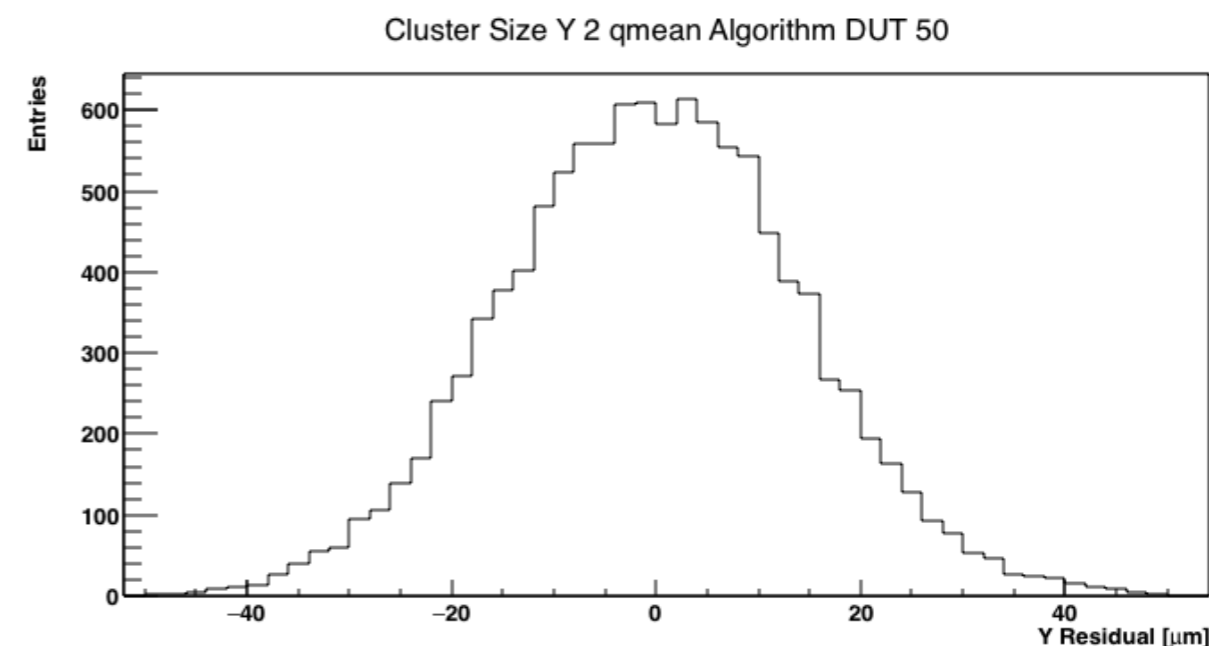
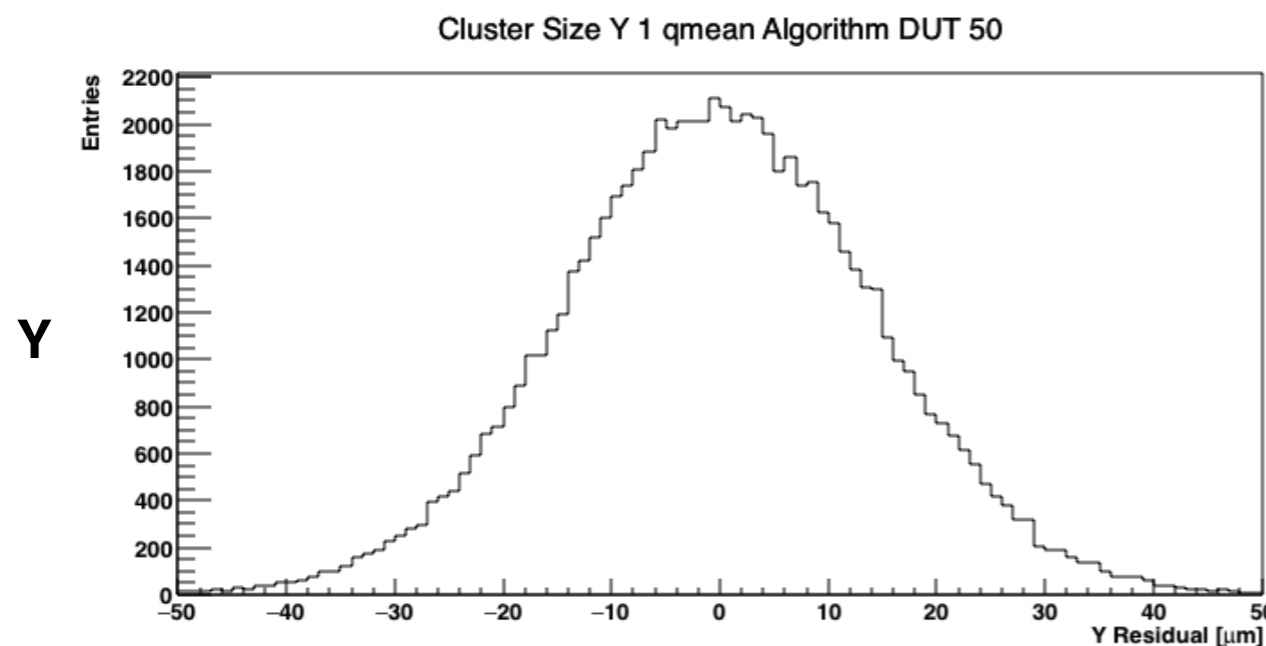
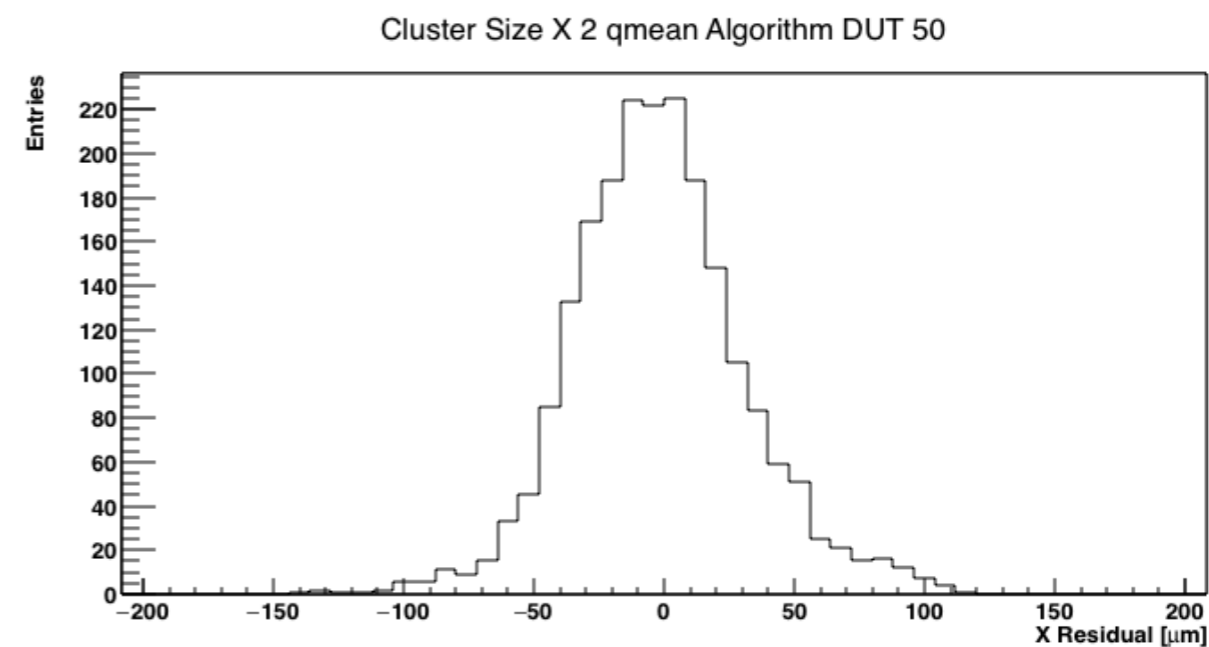
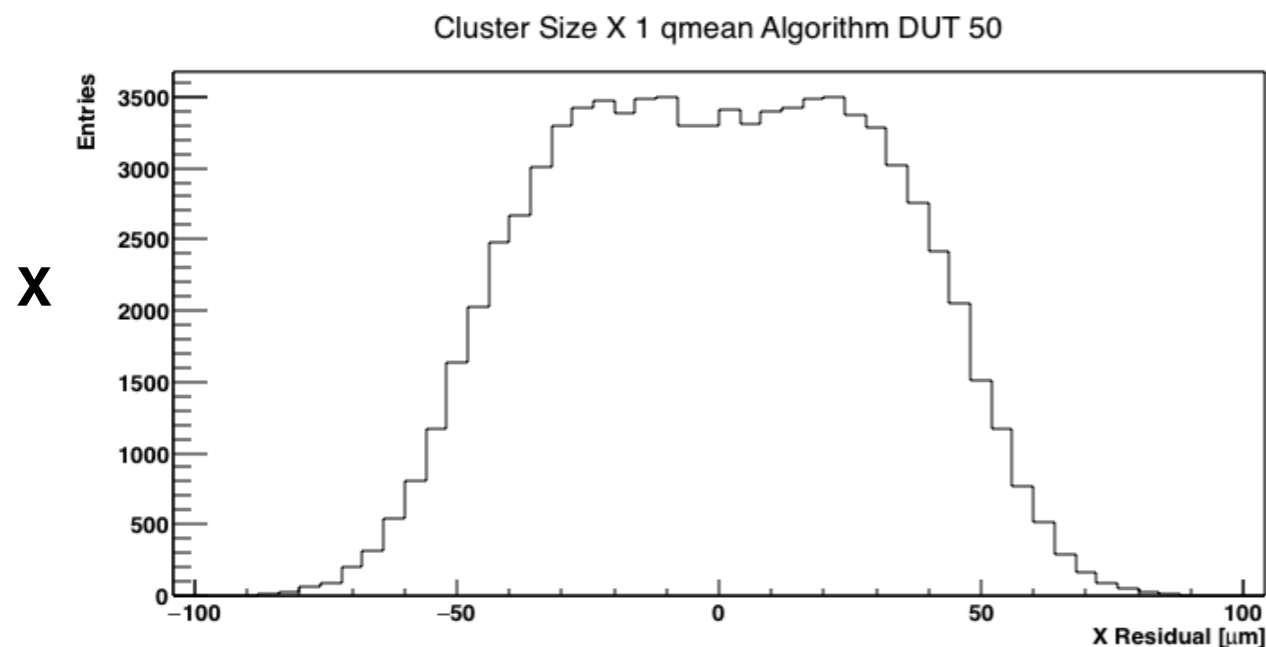


Cluster Size Y 2 geomean Algorithm DUT 30



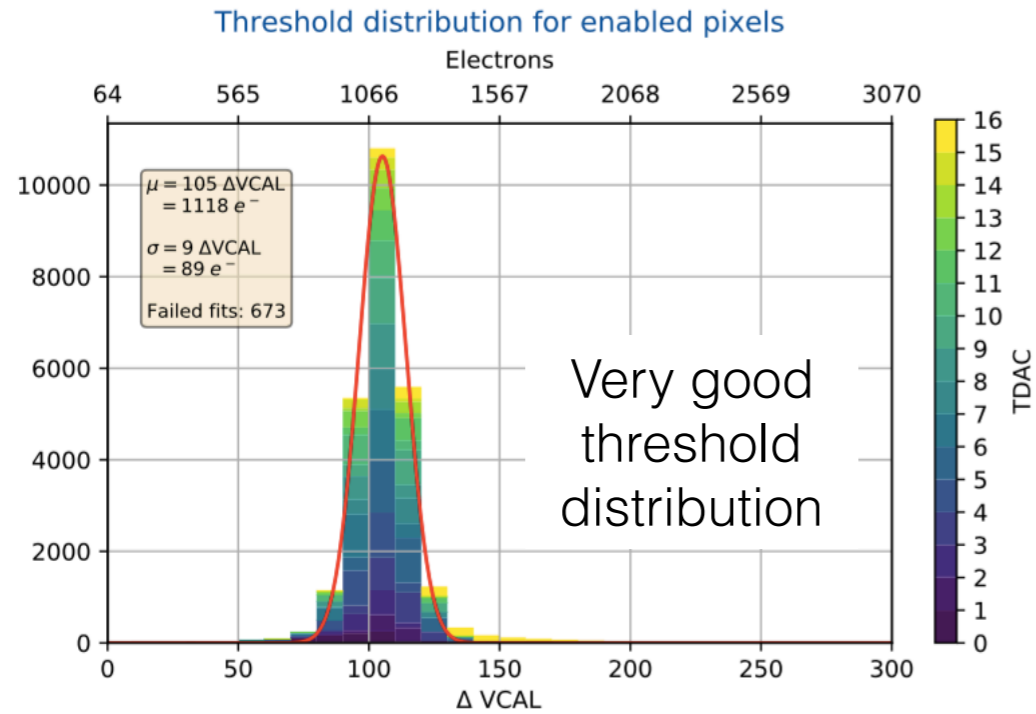
## Cluster Size 1

## Cluster Size 2



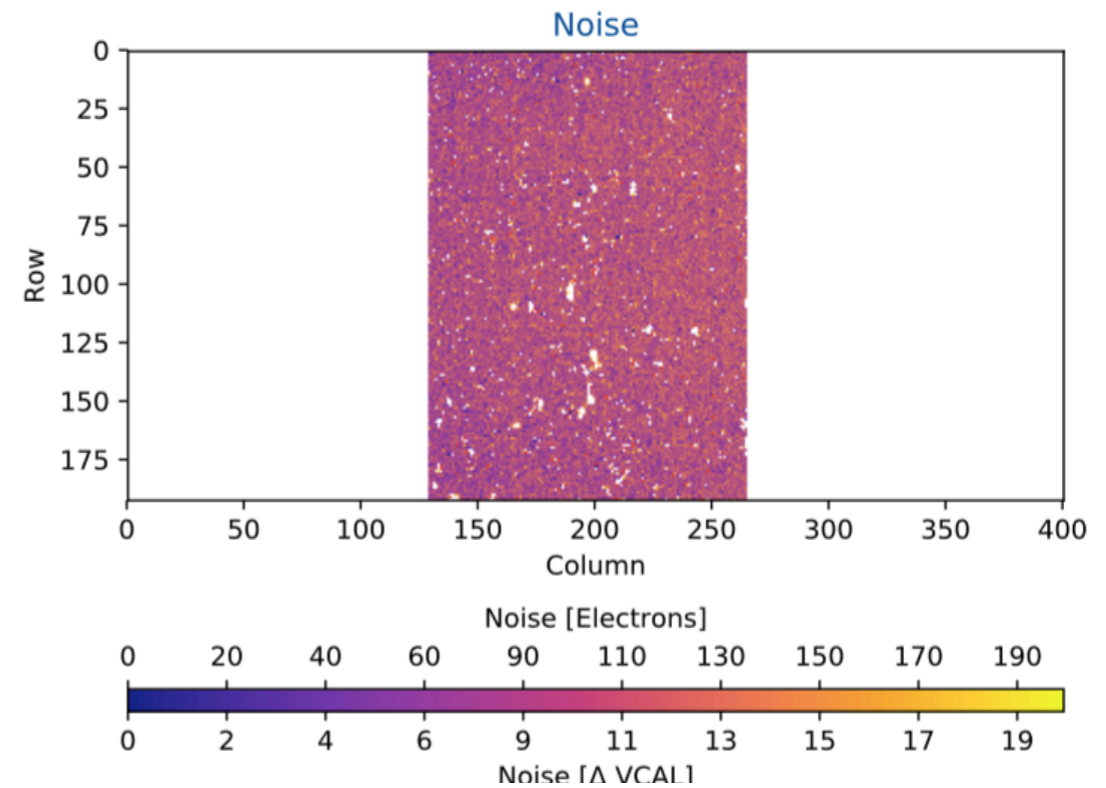
# 50x50 $\mu\text{m}^2$ - 1e16 PS - Tuning linear FE

RD53A preliminary Chip S/N: W3\_1-9\_1e16

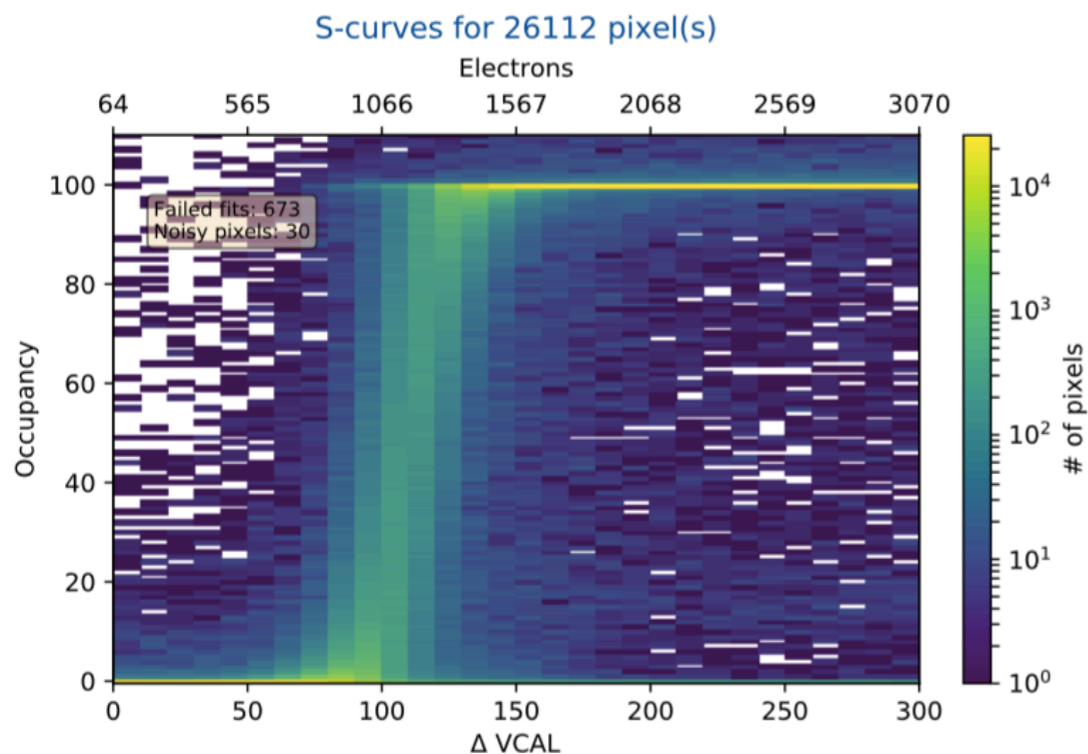


RD53A preliminary

Chip S/N: W3\_1-9\_1e16

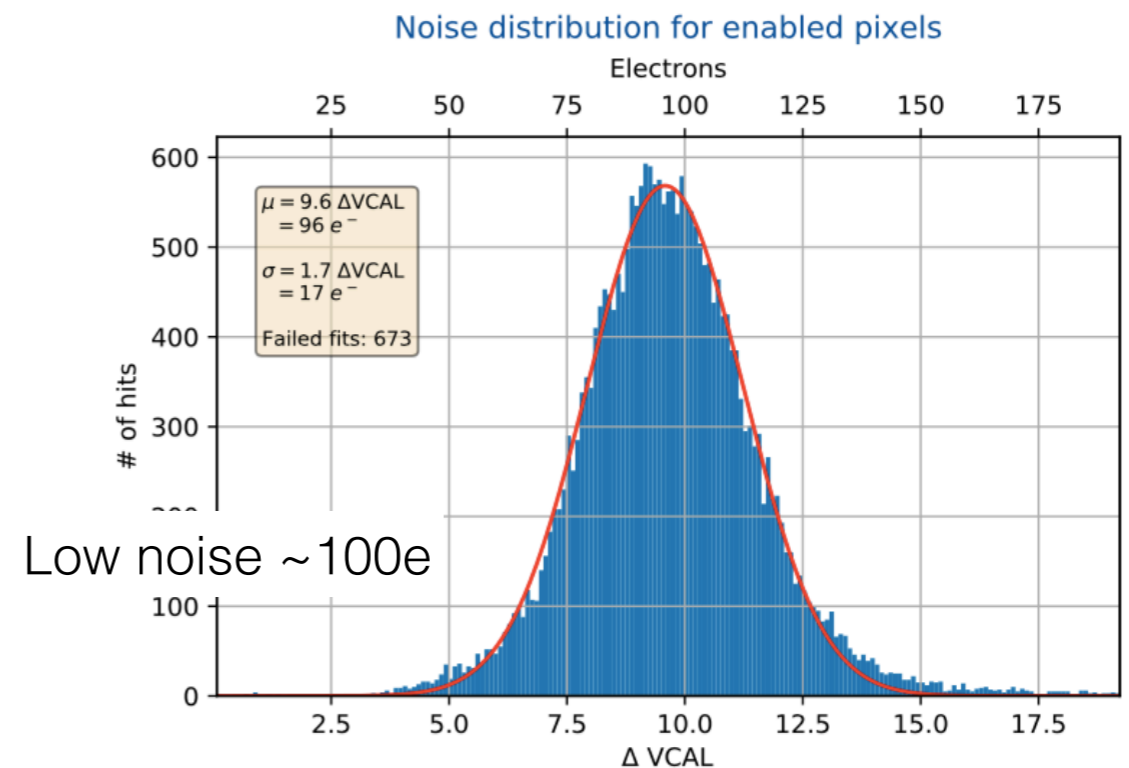


RD53A preliminary Chip S/N: W3\_1-9\_1e16



RD53A preliminary

Chip S/N: W3\_1-9\_1e16



# 50x50 $\mu\text{m}^2$ - 1e16 PS - Tuning differential FE

