

### **Evaluating IBM POWER Architecture for Deep Learning in High-Energy Physics**

CERN openlab Workshop 2019

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### **About Me**

- PhD Student from Delft University of Technology
- Quantum and Computer Engineering department
- Research on DL in HEP at CERN openlab in collaboration with IBM



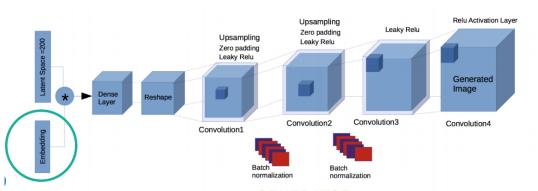




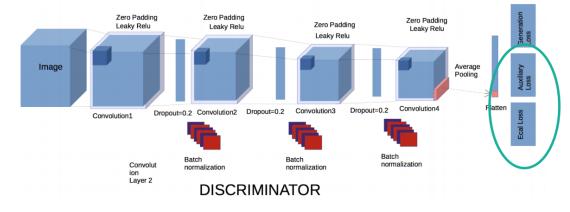


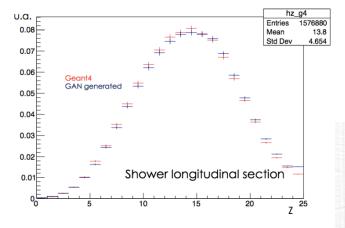
# **3D GAN Architecture**

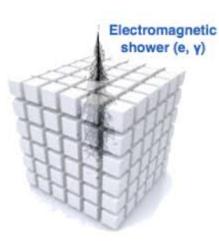
~ 25h training on single P100!

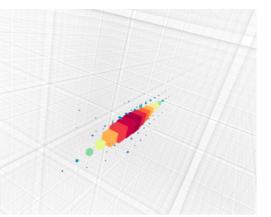


GENERATOR









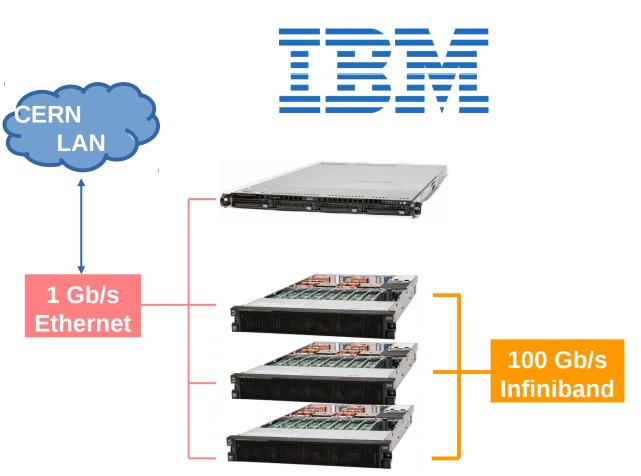
# **IBM Minsky**

#### 1x IBM SC821LC (login node)

- 1x POWER8 socket (=8 cores)
- 64 GB DDR4

#### 3x IBM SC822LC (worker nodes)

- 2x POWER8 sockets (=16 cores)
- 4x NVIDIA P100
- CPU ↔ GPU NVLink
- 256GB DDR4





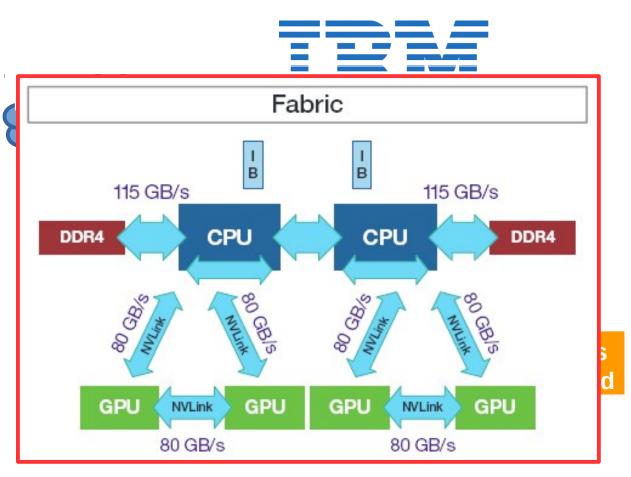
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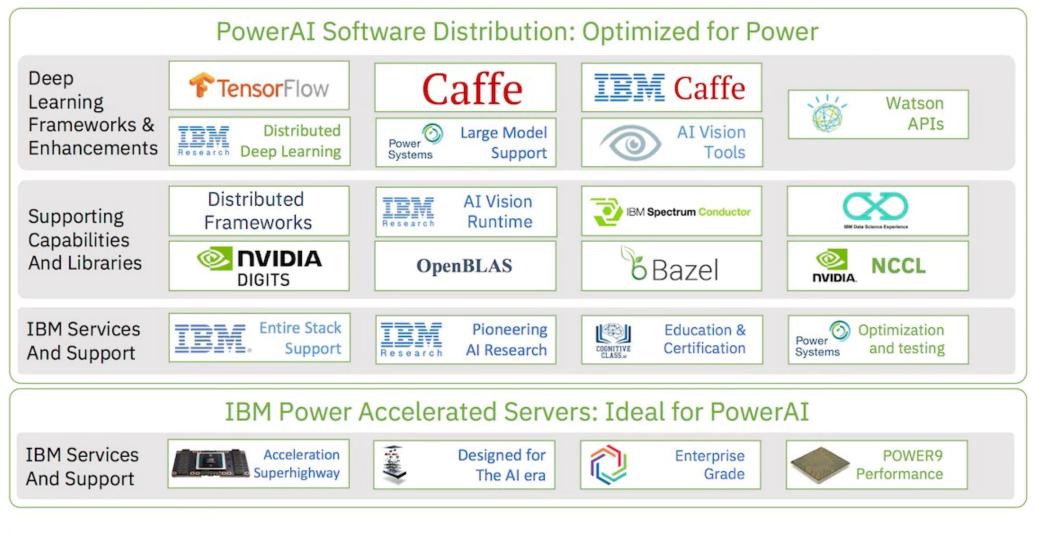
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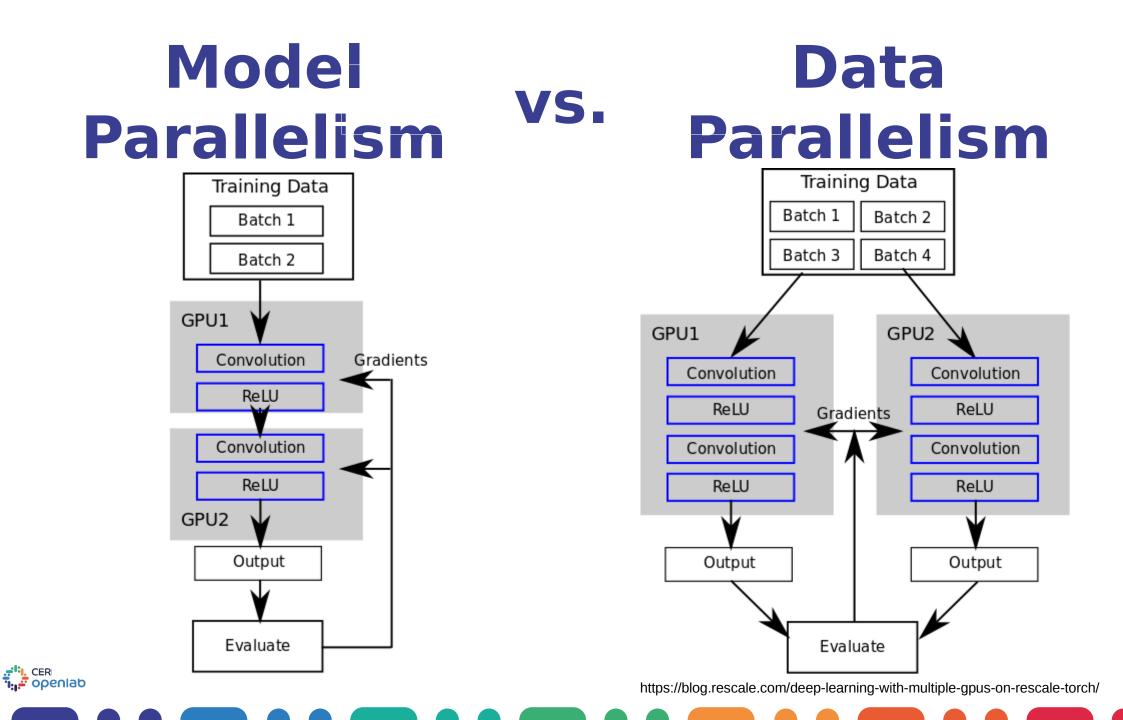
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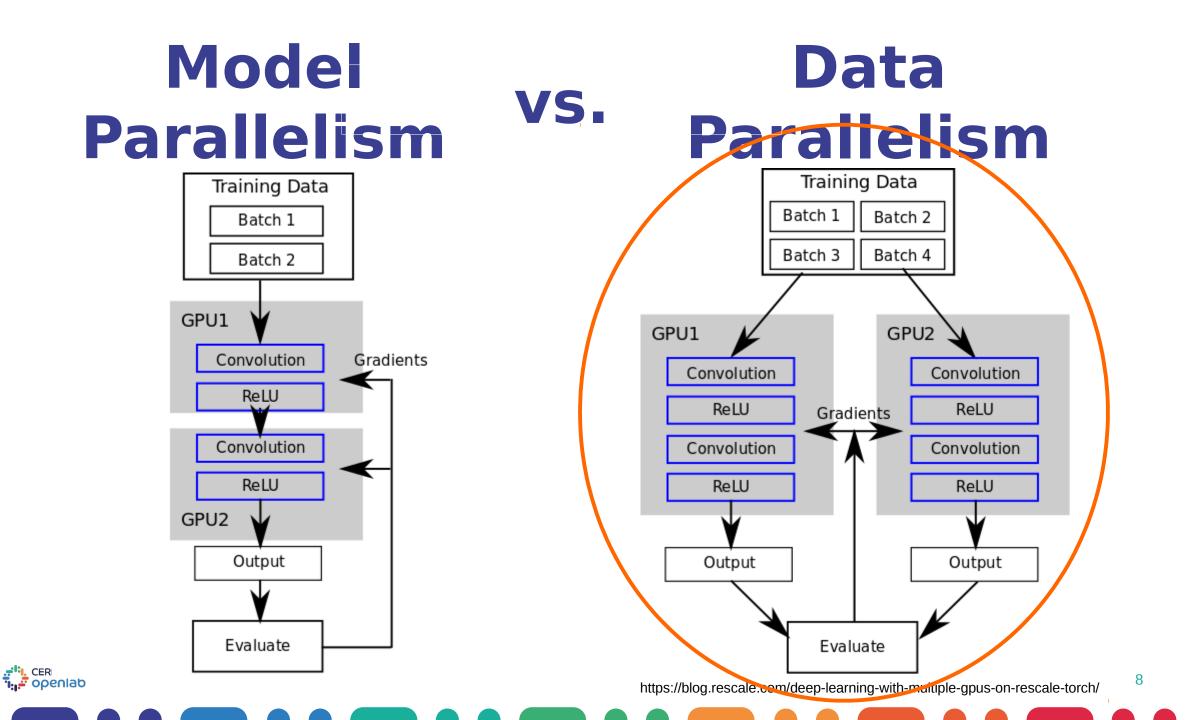


http://developercongress2017.openpowerfoundation.org/wp-content/uploads/2017/05/Porting-Applications-to-OpenMP4.5.pdf

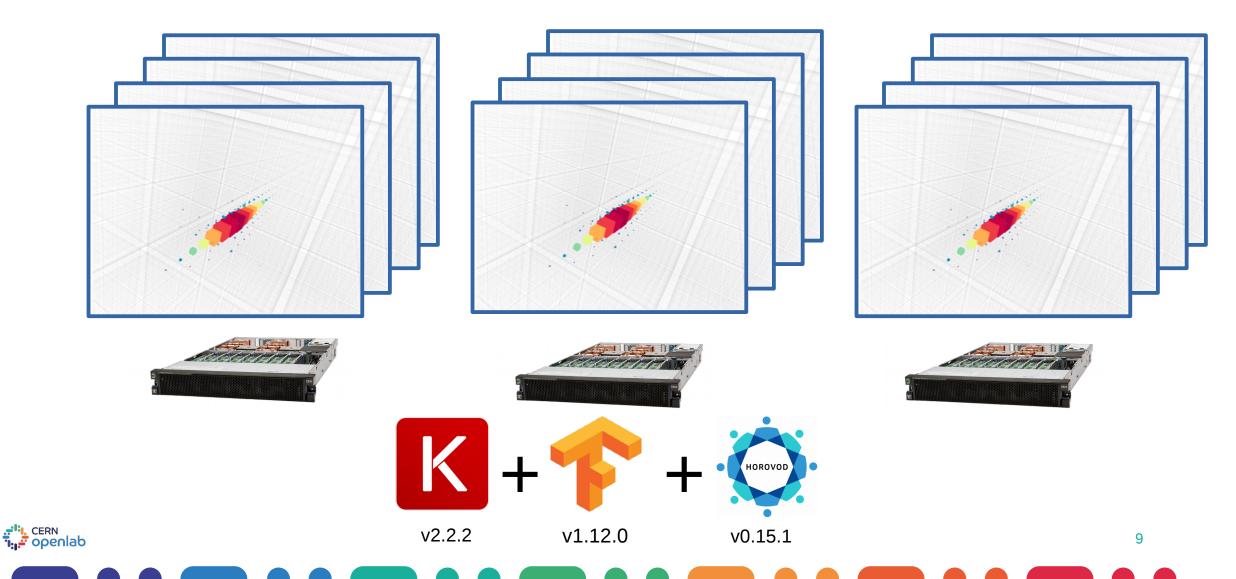
# **IBM Deep Learning Stack**





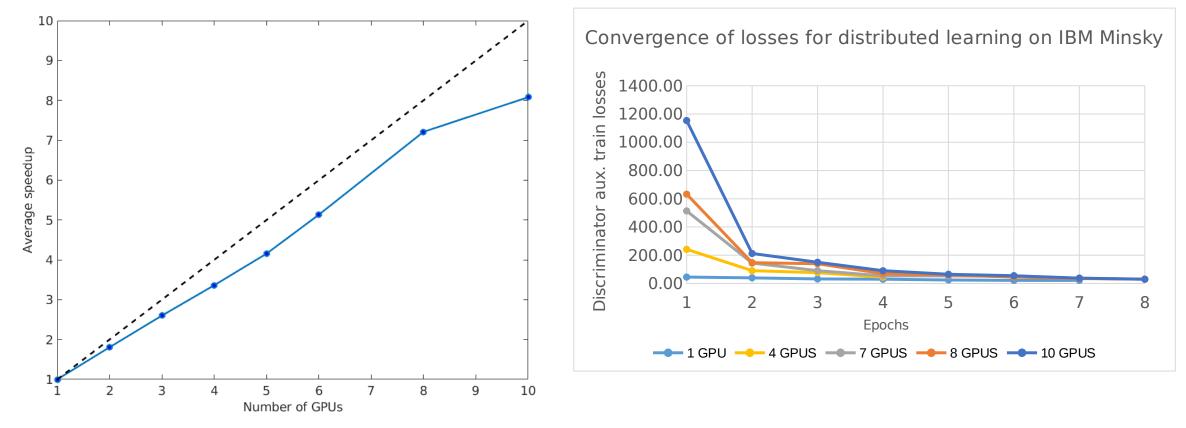


# **Distributed Deep Learning**



# **Performance Results: Training**

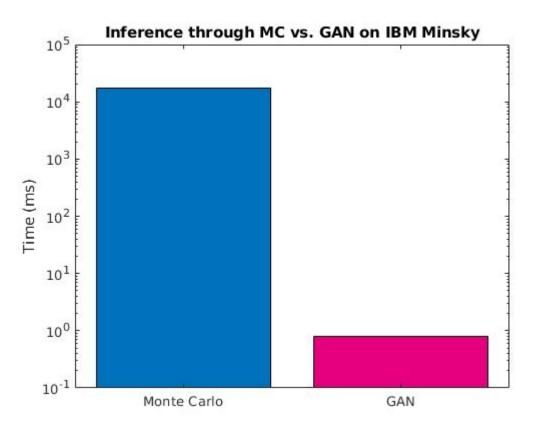
Scaling behavior over multiple nodes / GPUs on IBM Minsky



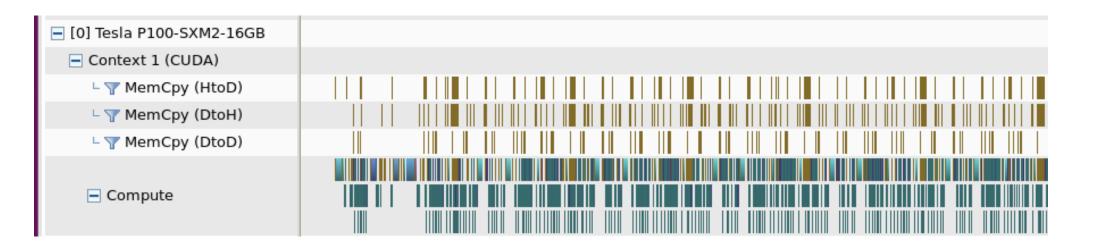
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### **Performance Results: Inference**

- Inference on single P100 GPU
- 17 sec → 0.8 ms
- 4 orders of magnitude improvement



# **Performance Results: NVLink**



High bandwidth interconnects alleviates stalls for memcpy operations  $\rightarrow$  higher throughput





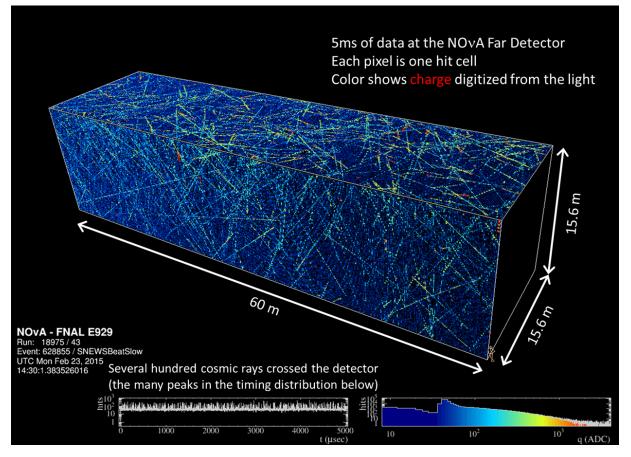
# **Conclusion: POWER for DL**

- 25 hours of training  $\rightarrow$  3 hours of training on 10 GPUs over 3 nodes
- Convergence of losses for varying number of GPUs
- NVLink between CPU ↔ GPU enables high throughput

# **Upcoming PhD Research**



- Deep Learning approach for offline event reconstruction in DUNE neutrino experiments
- Find novel methods of harnessing the cutting-edge in HPC
- Large data sets → strain on memory bandwidth → IBM is adapting its architecture towards this trend



Example of a neutrino event data display (NovA Experiment)



# **QUESTIONS?**

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