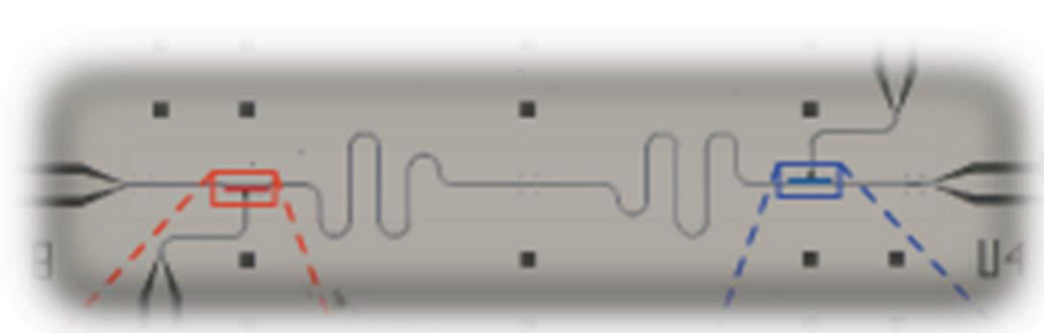


# Superconducting Cavities for Modular Quantum Information Processing



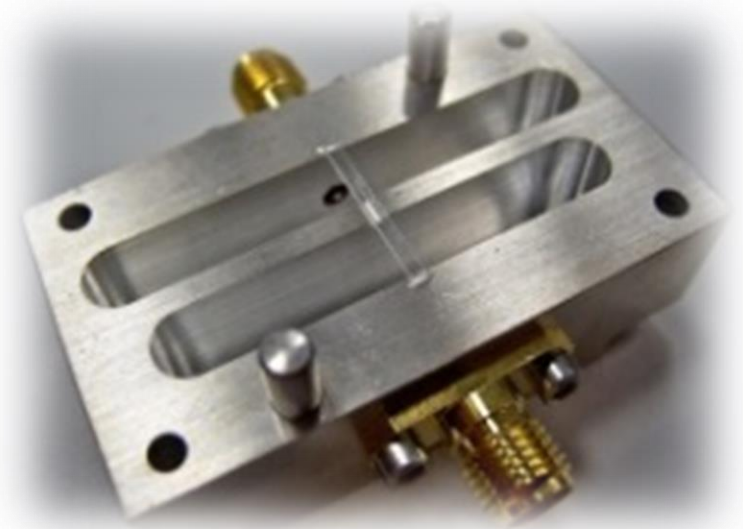
CEC-ICMC

July 24, 2019

**Luke Burkhardt, Yale University**

Chan U Lei, Lev Krayzman, Teresa Brecht,

**Robert Schoelkopf (PI)**



Yale University



# Leveraging modularity and integration

- Modularity for complex quantum machines
- Superconducting cavities as quantum circuitry
- What makes a good (or bad) cavity?
- Looking forward: high-Q cavities and integrated quantum circuits

# A very incomplete TODO for quantum information

## Long-term goals:

- Quantum computers which can do everything (universal QC)
  - Algorithms for cryptography, search, optimization, classification (c. 1990-present)
- Continental/global scale quantum networks
  - Quantum internet for secure communication

## # of qubits required:

**$\sim 10^5 - 10^6$**

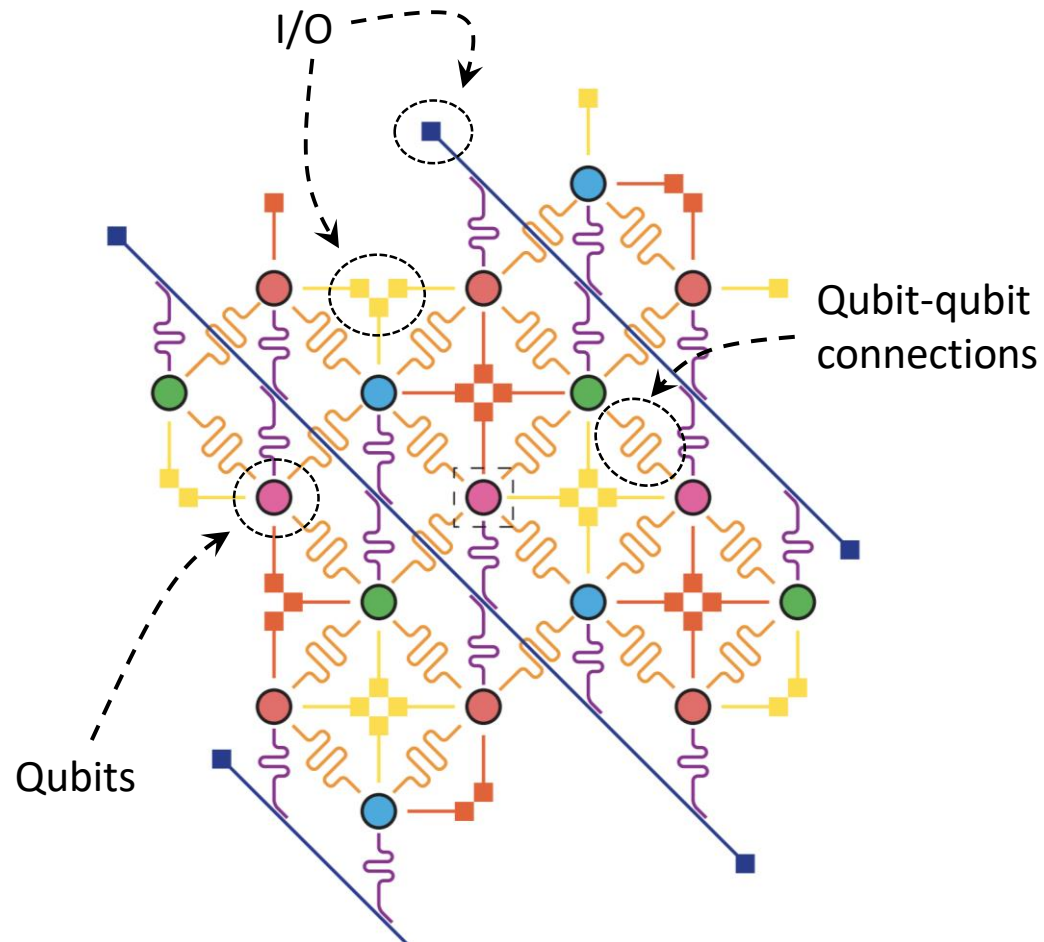
Fewer if quality  
is higher

## Near-term goals:

- Quantum computers which can do *anything at all*
- Quantum simulation – chemistry and solid-state physics
- Small-scale quantum networks
  - Quantum *intranet*

**$\sim 10 - 100$**

# Monolithic approach to complexity



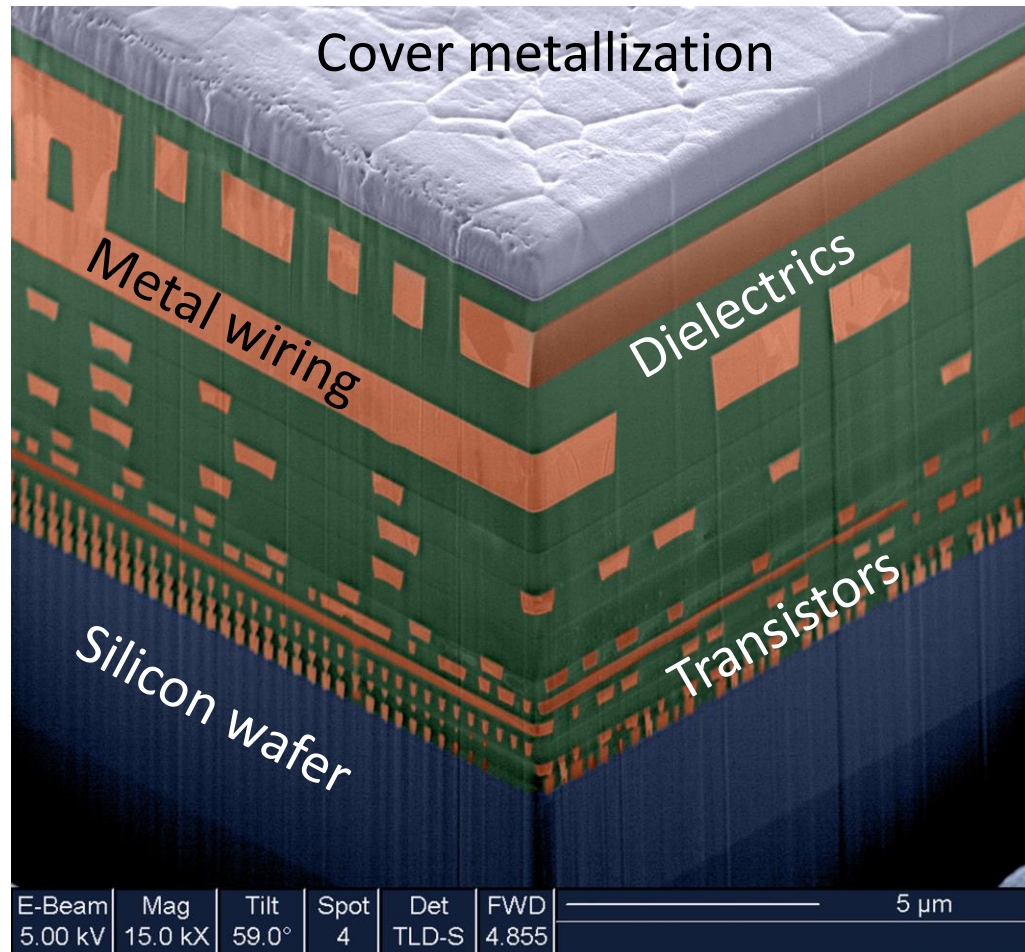
## Advantages:

- Integrated circuits on single chip or stack of chips
- Dense, connected grid for multi-qubit operations

## Challenges:

- 100% of the complexity in one place

# Monolithic approach to complexity



Cross-section, IBM 90 nm microprocessor ca. 2005  
>6 materials, >13 layers

## Advantages:

- Integrated circuits on single chip or stack of chips
- Dense, connected grid for multi-qubit operations

## Challenges:

- 100% of the complexity in one place
- Requires industrial-scale design and fabrication
- **And** all metals superconducting, with pristine dielectrics



# Modular approach to complexity



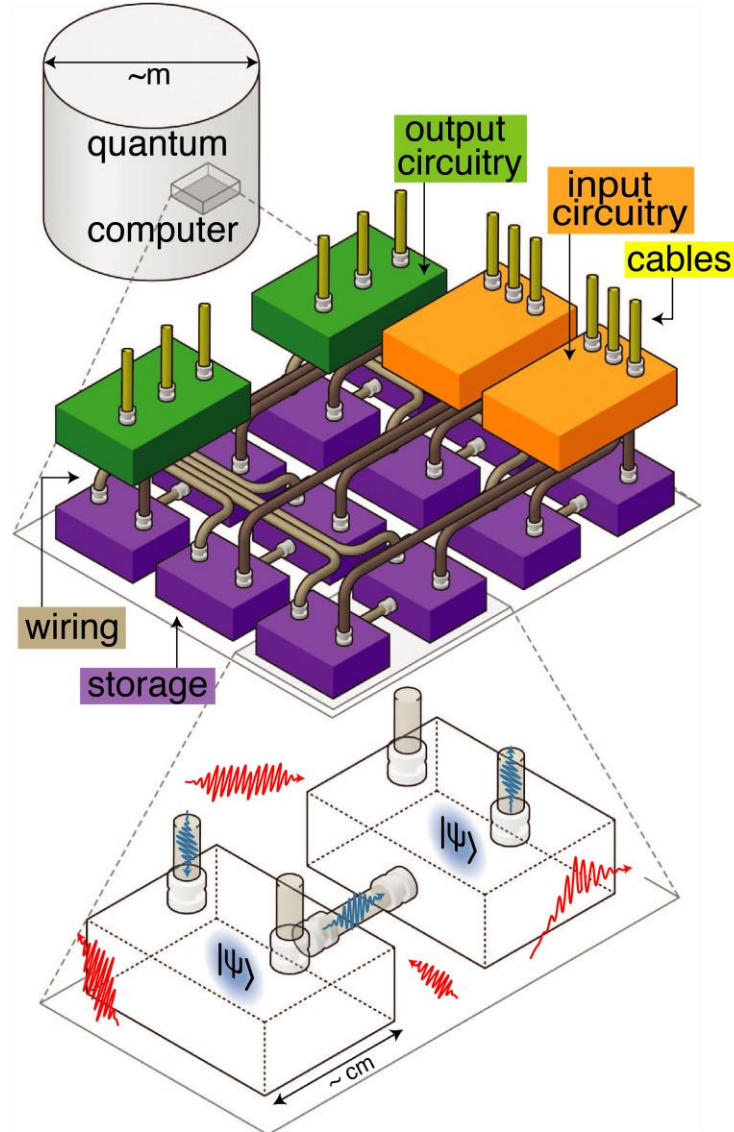
IBM Summit supercomputer @ Oak Ridge NL

Break the computer into testable, replaceable, reconfigurable chunks – the most complex thing you can make **well**

Separate external functions (communication, networking) from computation

Keeping the modules simple means academic labs can share in the innovation

# Modular approach to complexity



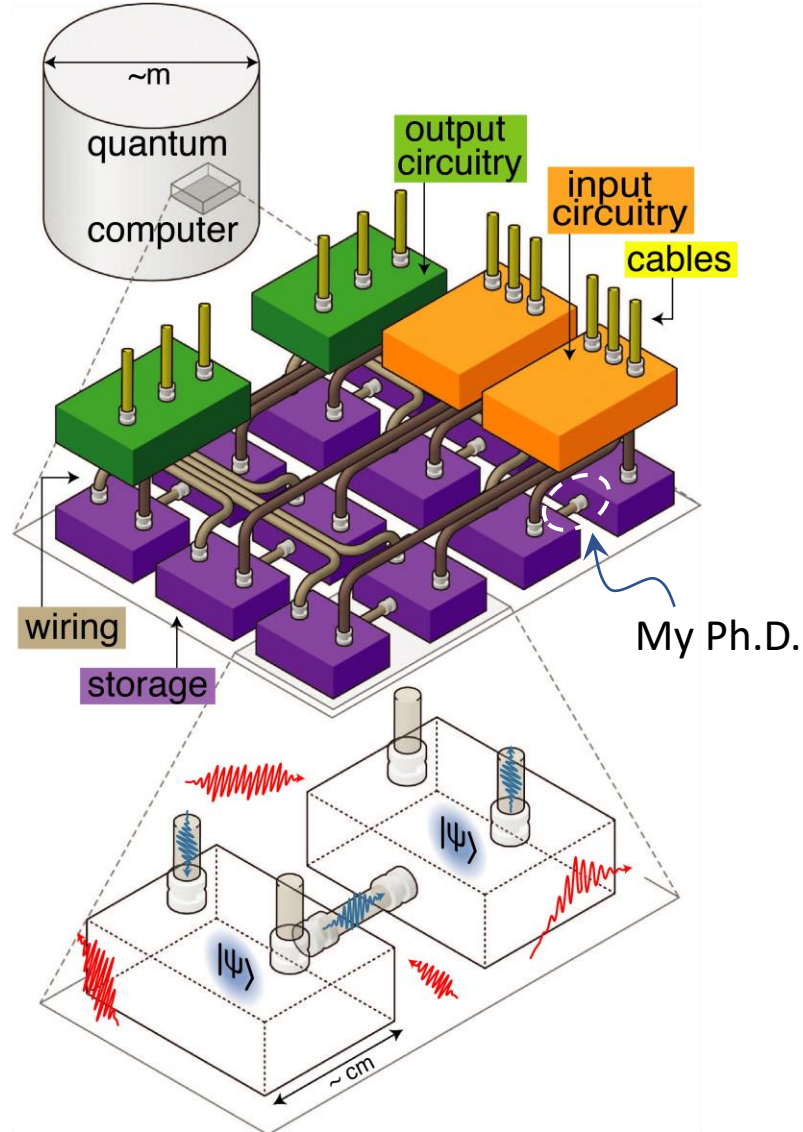
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**What are the challenges within a single module?**

# Modular approach to complexity



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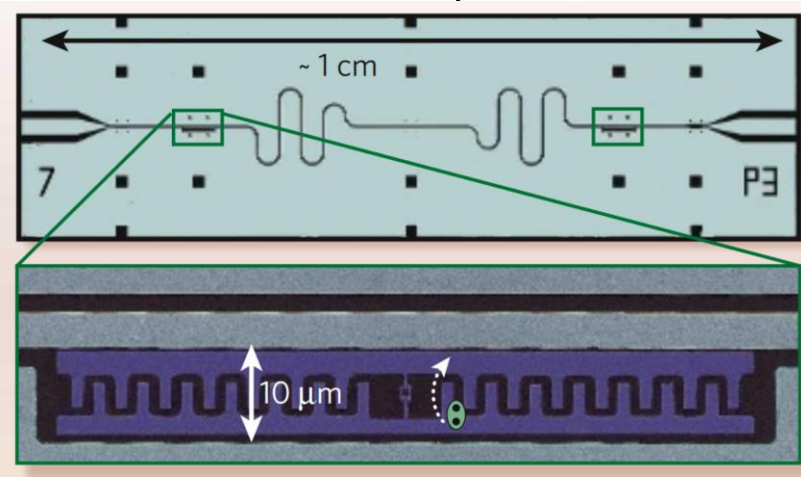
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**What are the challenges within a single module?**

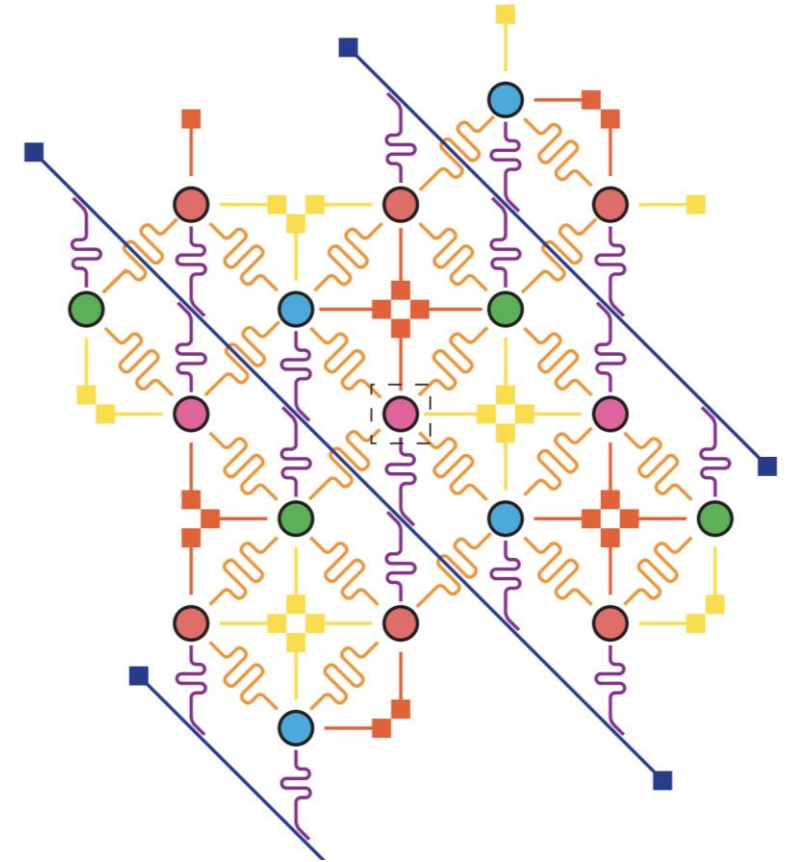
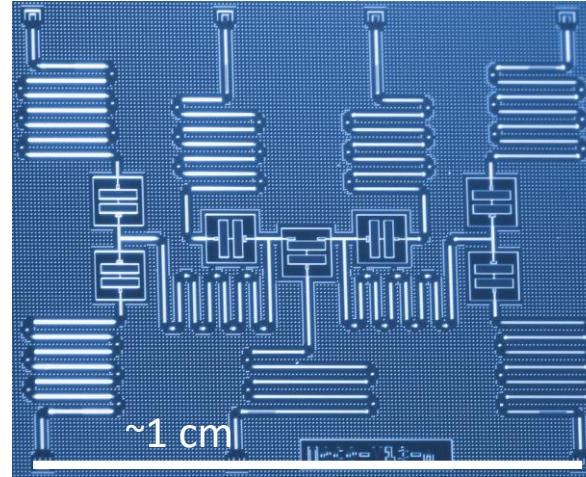


# (Some) challenges in designing a module

ca. 2007, Yale

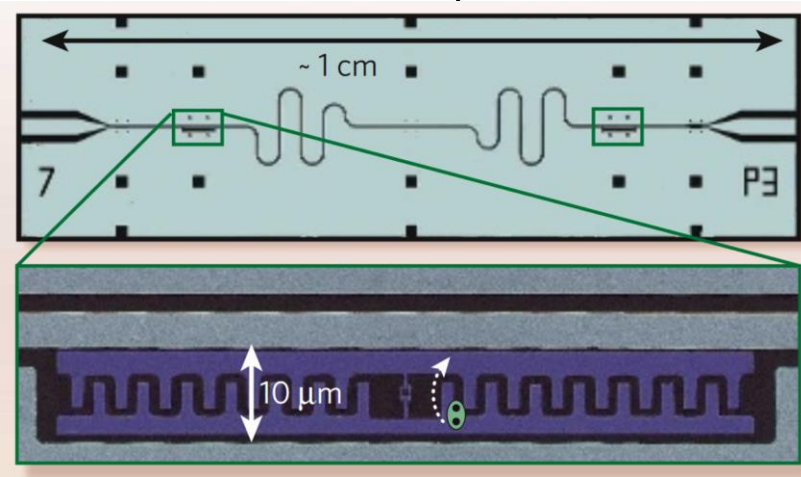


ca. 2017, IBM

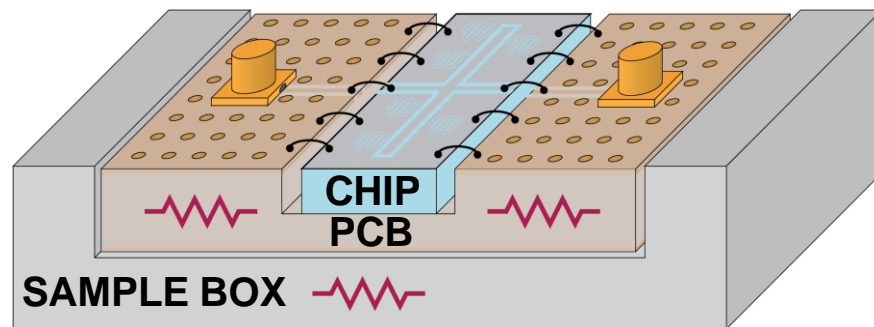
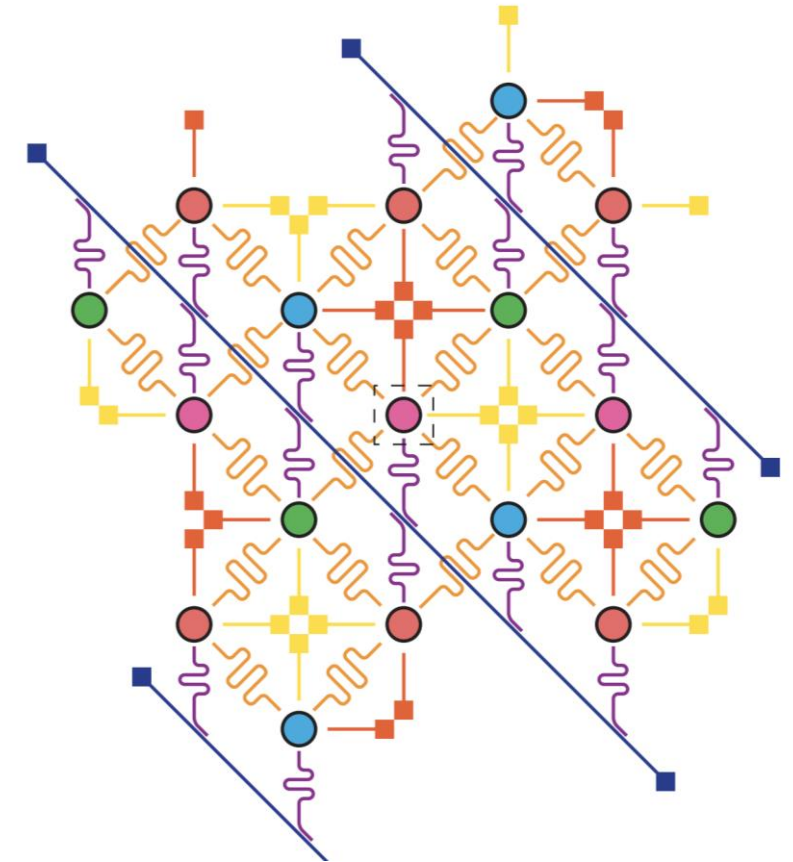
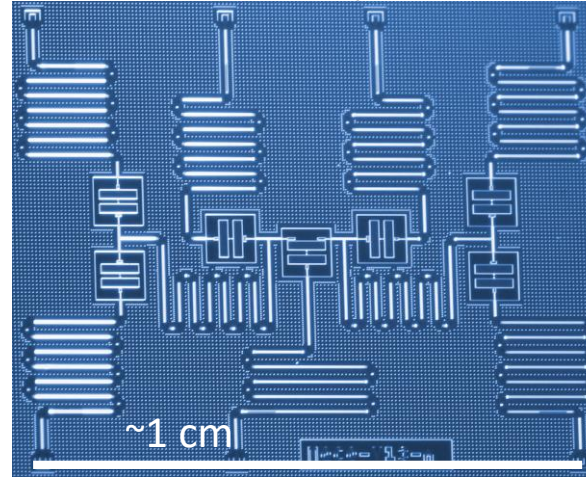


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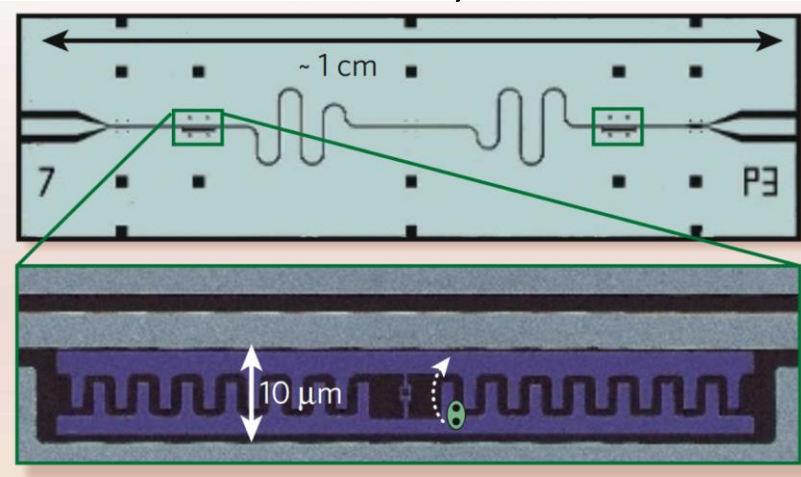
ca. 2017, IBM



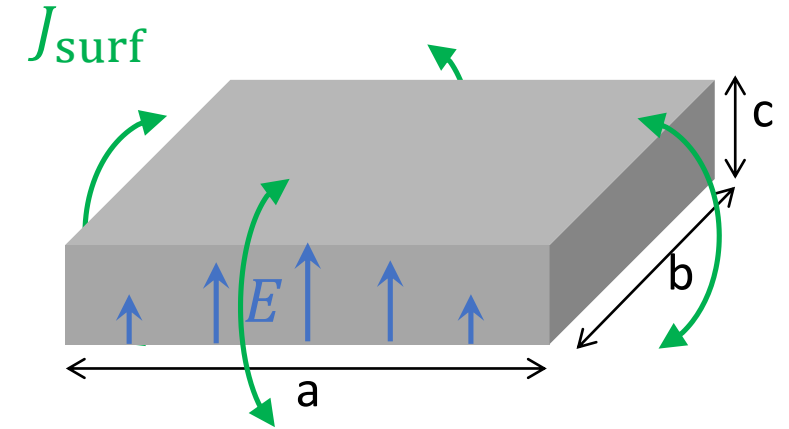
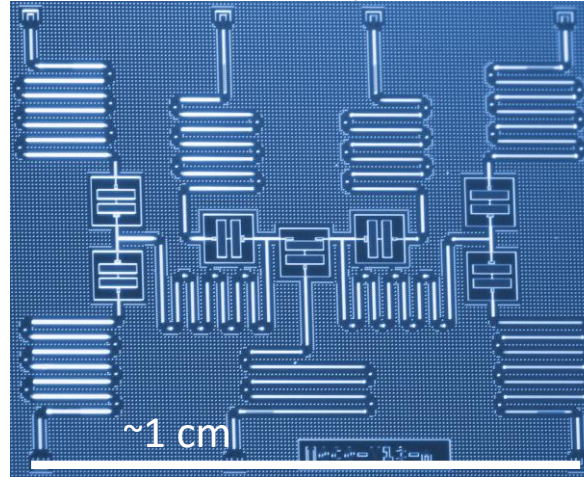


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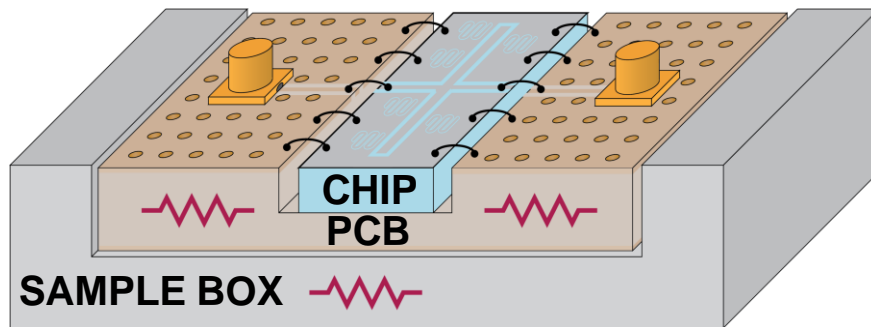
ca. 2017, IBM



Lowest frequency mode:

$$\omega_{110} = \frac{1}{\sqrt{\epsilon\mu}} \sqrt{\left(\frac{\pi}{a}\right)^2 + \left(\frac{\pi}{b}\right)^2}$$

~10 GHz for 2x2 cm chip,  
or lower w/ dielectric



# (Some) challenges in designing a module

ca. 2007, Yale

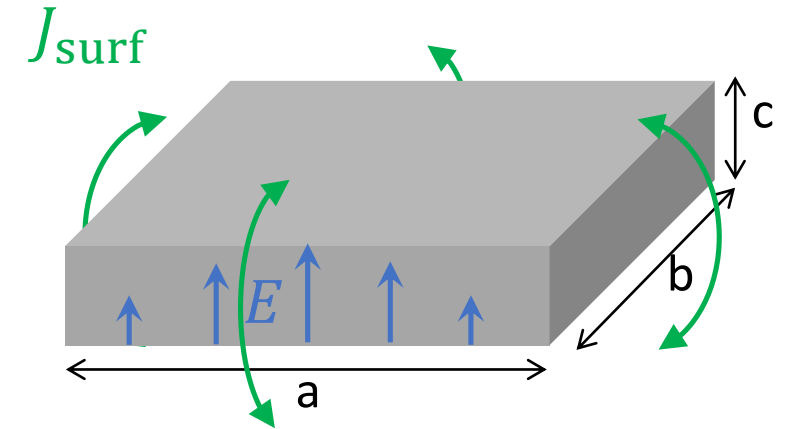
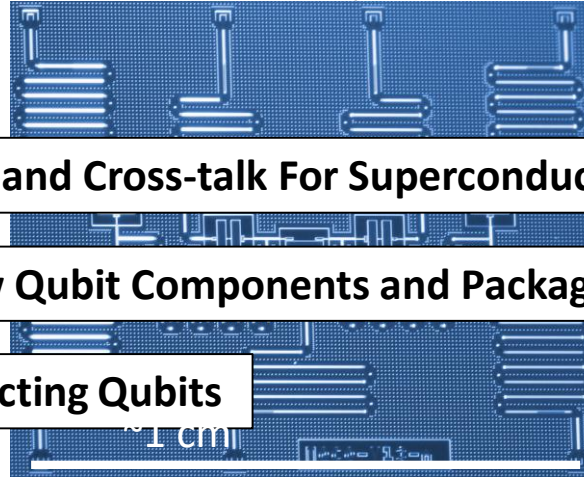
**APS March Meeting 2019:**

**Session A42: Multi-Qubit Characterizations and Cross-talk For Superconducting Qubits**

**Session P26: Superconducting Circuits: New Qubit Components and Packaging**

**Session S35: 3D Integration for Superconducting Qubits**

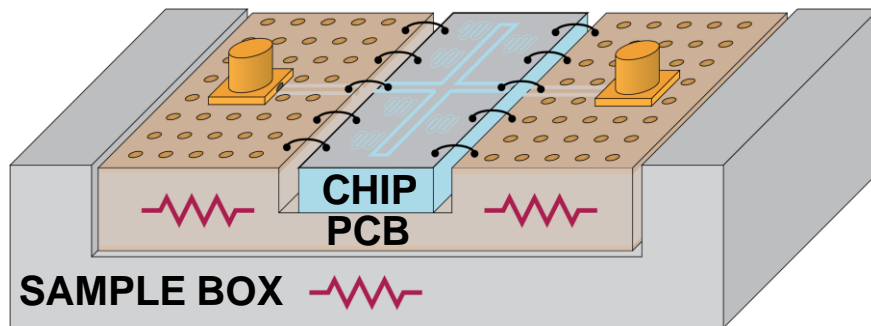
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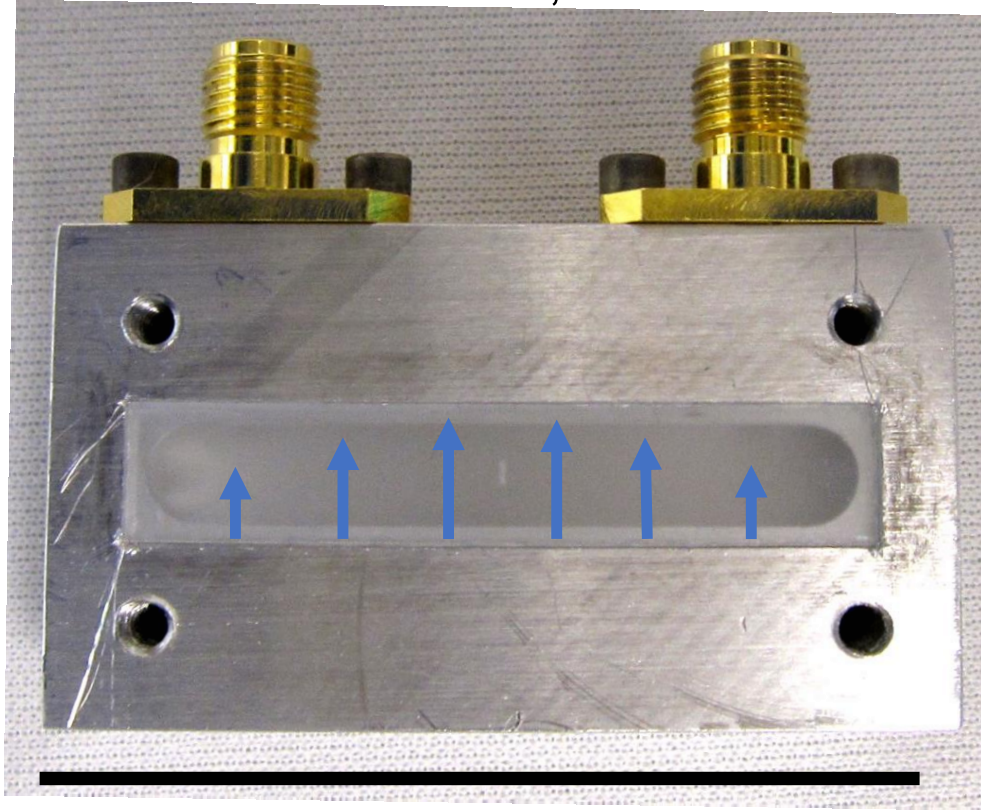


Crossovers, thru-Si vias, PCBs...  
All of which need to preserve the quality  
of the qubits!

**Why fight against the  
enclosure?**

# Alternative approach: using the box as a resource

Paik et al., 2011



5 cm

Aluminum waveguide  
cavity

$$V \sim 1 \text{ cm}^3$$

$$Q \sim 60,000,000^*$$

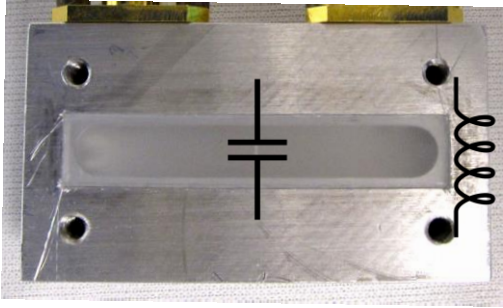
$$Q = \omega_0 \tau$$

**~10x longer lived than  
typical transmon qubits  
and on-chip resonators**

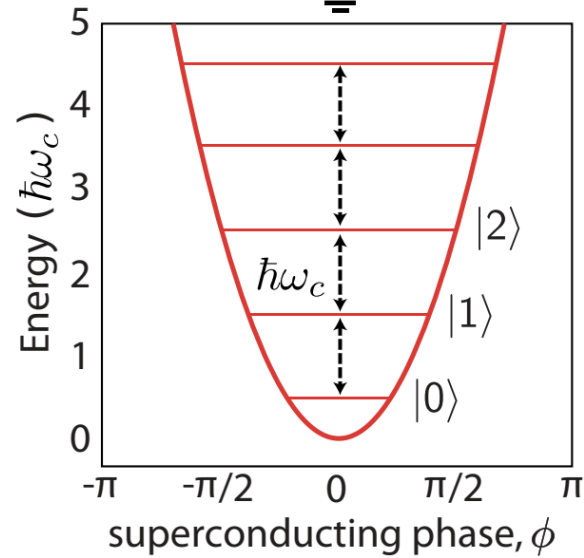
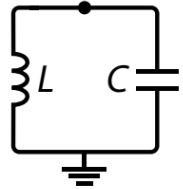
\*99.99% pure Al w/ chemical etching



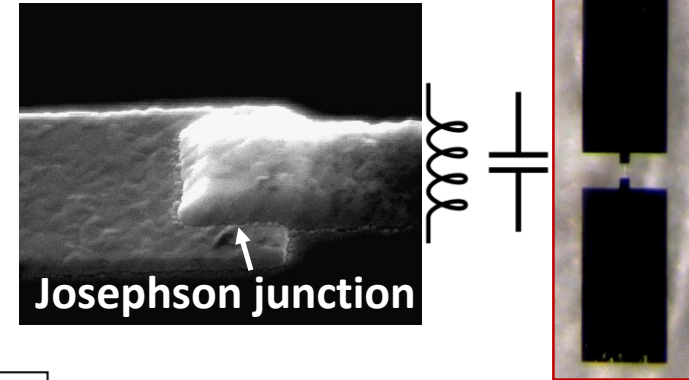
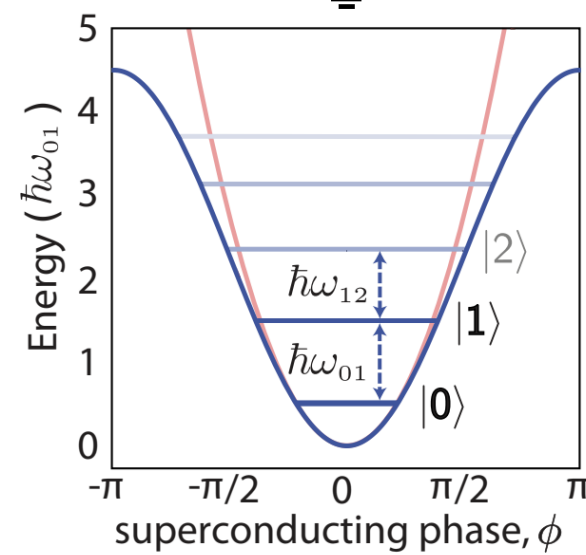
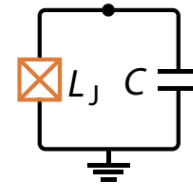
# Why cavities make interesting elements



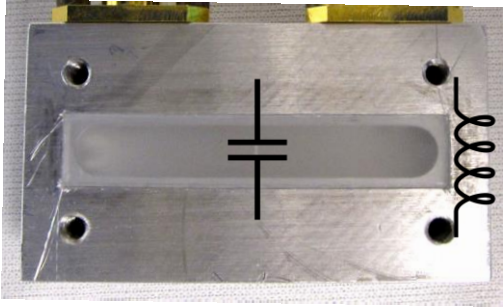
**Cavity resonator:  
(harmonic)**



**Transmon:  
(anharmonic)**



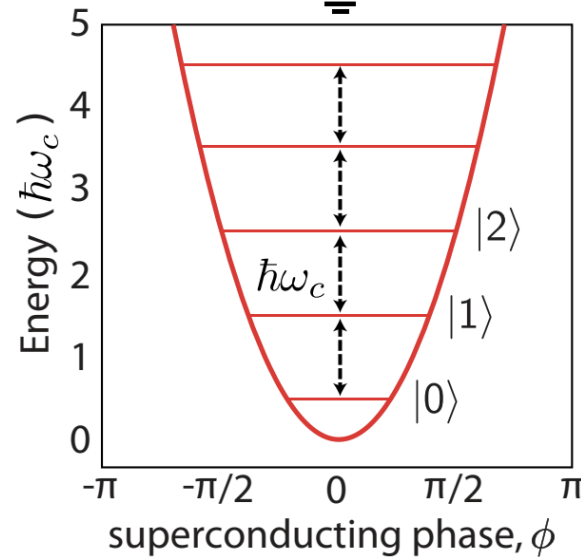
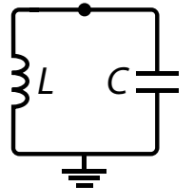
# Why cavities make interesting elements



Larger information carrying capacity

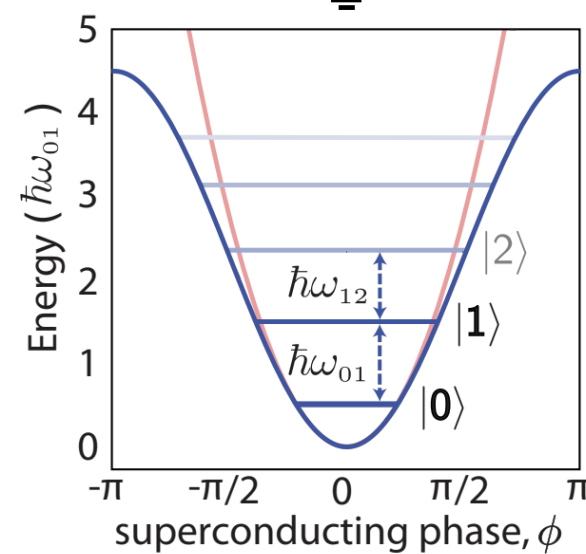
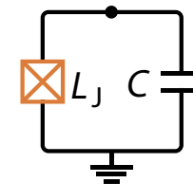
Operations are more complex and slower (~1 us)

Cavity resonator:  
(harmonic)

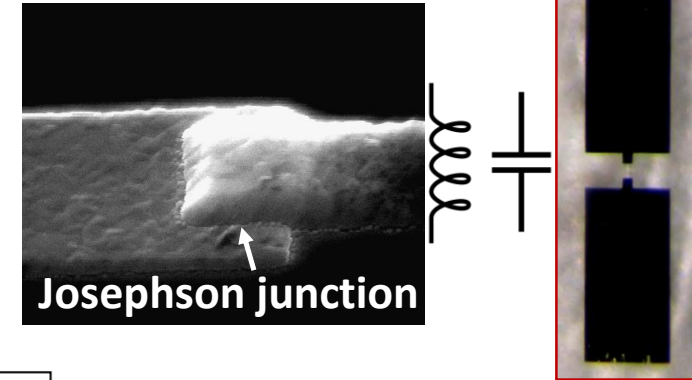


1 mode (+1 ancilla)\*

Transmon:  
(anharmonic)



At least 5 modes



Simple, robust control (~10 ns)

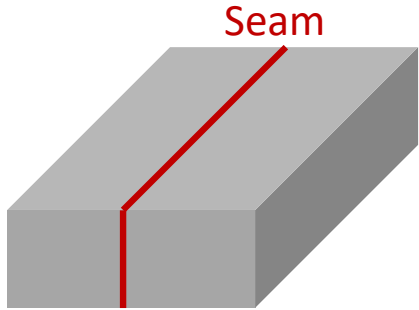
Multiple modes required to correct errors

Requirements for  
Quantum Error Correction (QEC):

Figures: "Superconducting Qubits: Current State of Play," M. Kjaergaard et al., 2019

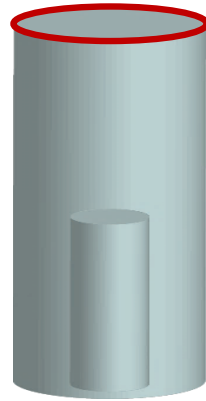
\*QEC Demonstration:  
N. Ofek et al., Nature, 2016  
Multi-mode cavity memories:  
Schuster group, U Chicago

# Some 3D cavity realizations



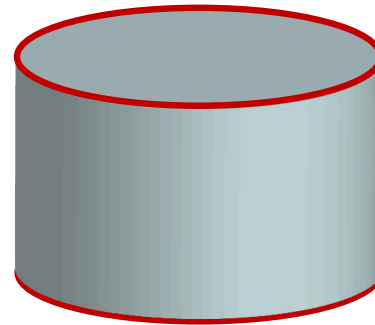
**Al rectangular**

$$Q = 60 \times 10^6$$
$$\tau = 1 \text{ ms}$$



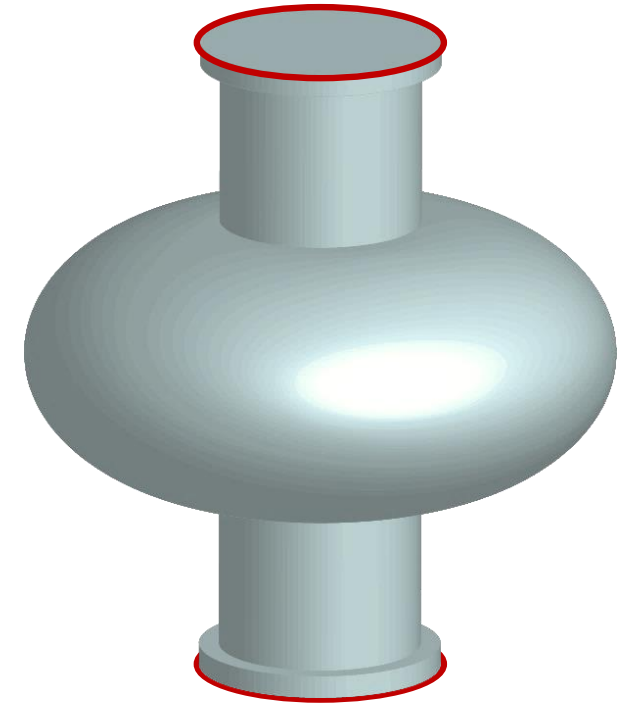
**Al stub**

$$Q = 70 \times 10^6$$
$$\tau = 3 \text{ ms}$$



**Al cylindrical**

$$Q = 740 \times 10^6$$
$$\tau = 10 \text{ ms}$$



**Nb TESLA\***

**heat treated**

$$Q = 1 \times 10^9 \quad Q = 17 \times 10^9$$
$$\tau = 30 \text{ ms} \quad \tau = 2 \text{ s}$$

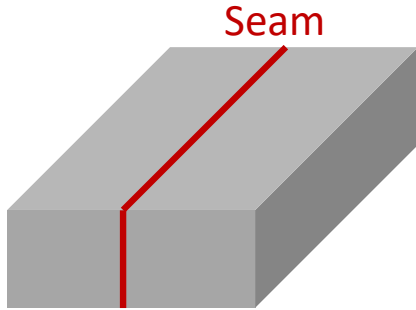
Reagor *et al.* Appl. Phys. Lett. **102**, 192604 (2013)

T. Brecht *et al.* Appl. Phys. Lett. **107**, 192603 (2015)

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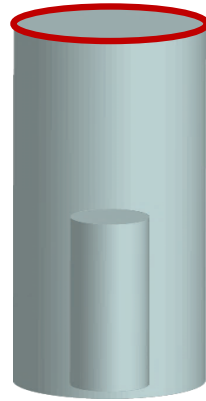
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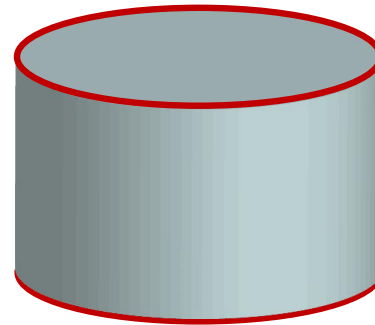
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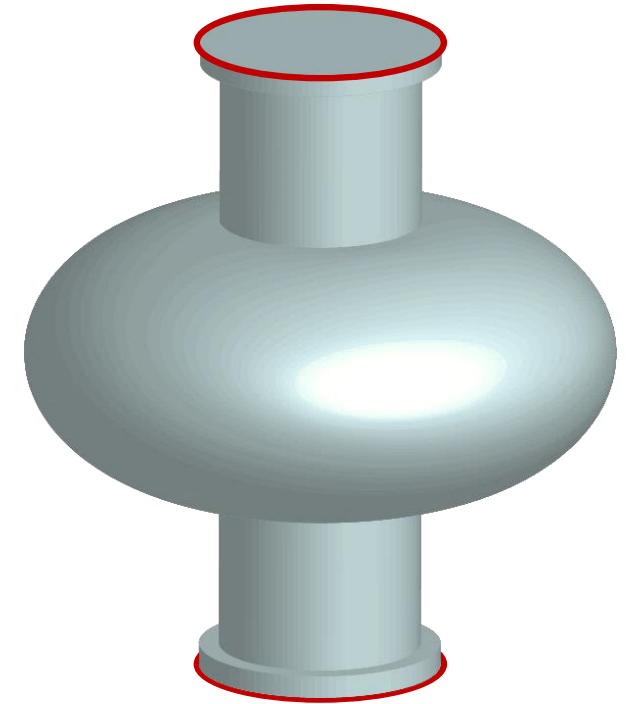
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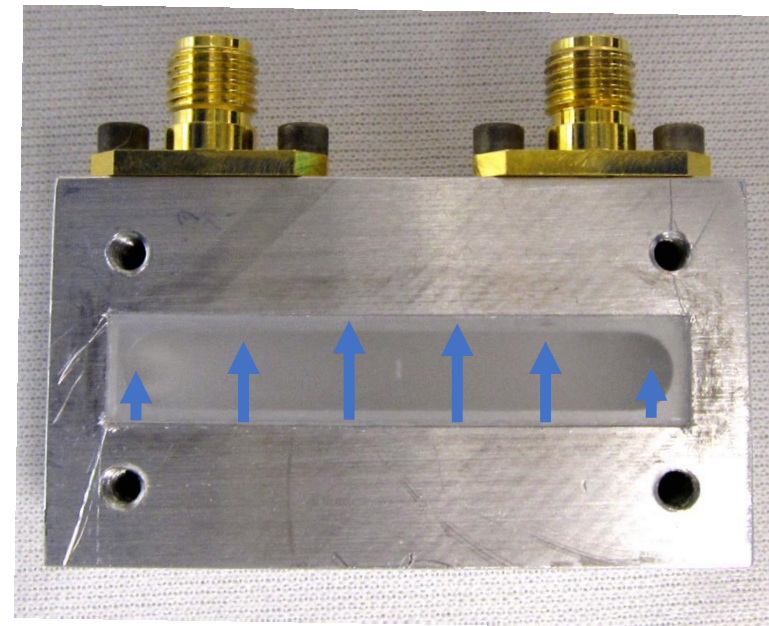
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# Cramming more components onto integrated circuits

With unit cost falling as the number of components per circuit rises, by 1975 economics may dictate squeezing as many as 65,000 components on a single silicon chip

By Gordon E. Moore

Director, Research and Development Laboratories, Fairchild Semiconductor division of Fairchild Camera and Instrument Corp.





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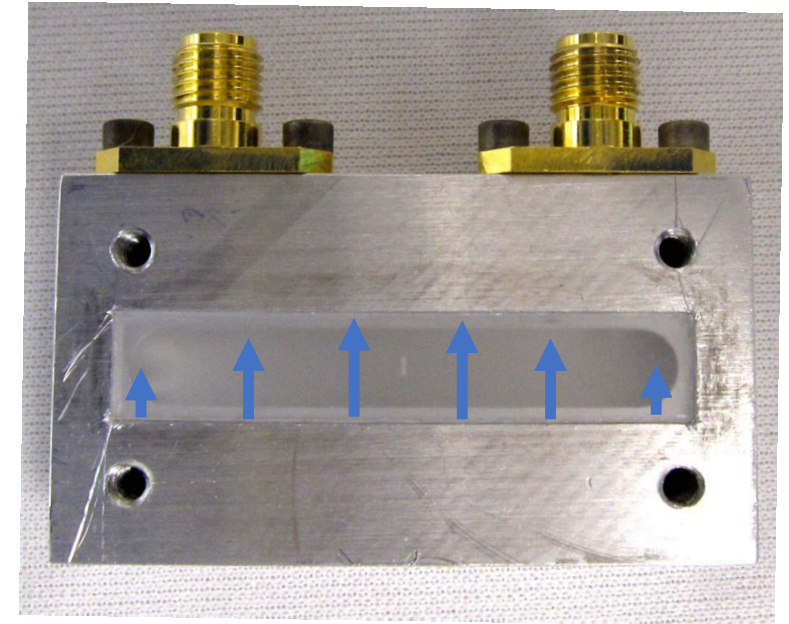
Director, Research and Development Laboratories, Fairchild Semiconductor division of Fairchild Camera and Instrument Corp.



## Linear circuitry

Integration will not change linear systems as radically as digital systems. Still, a considerable degree of integration will be achieved with linear circuits. The lack of large-value capacitors and inductors is the greatest fundamental limitations to integrated electronics in the linear area.

By their very nature, such elements require the storage of energy in a volume. For high  $Q$  it is necessary that the volume be large. The incompatibility of large volume and integrated electronics is obvious from the terms themselves.



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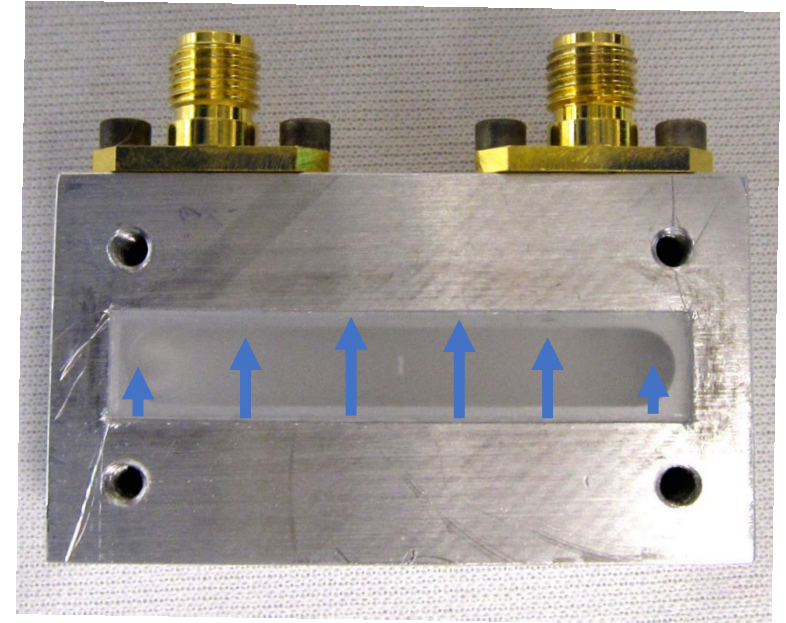
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## Linear circuitry

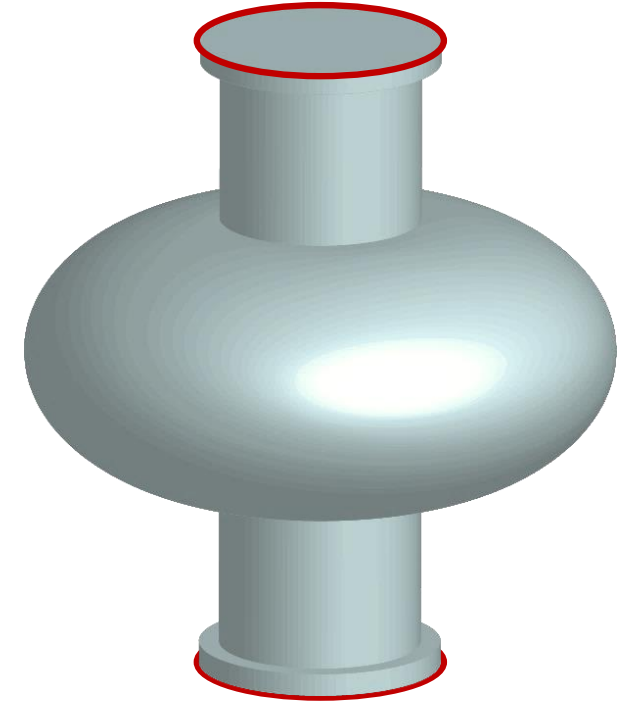
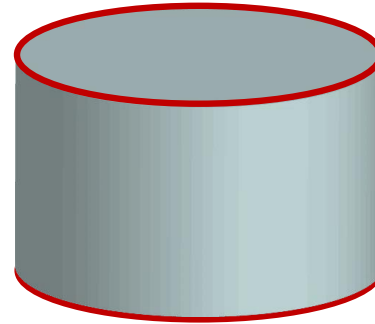
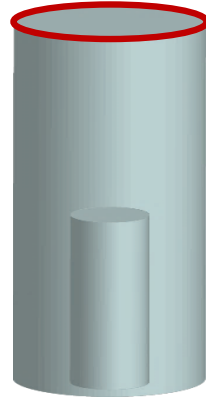
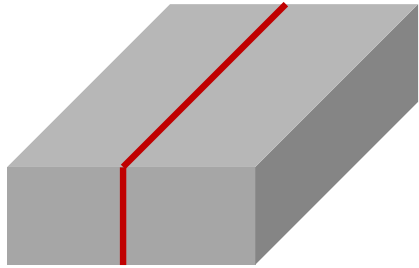
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By their very nature, such elements require the storage of energy in a volume. For high Q it is necessary that the volume be large. The incompatibility of large volume and integrated electronics is obvious from the terms themselves.



Miniaturization is **not** the name of the game. Instead, a careful understanding of what imperfections matter

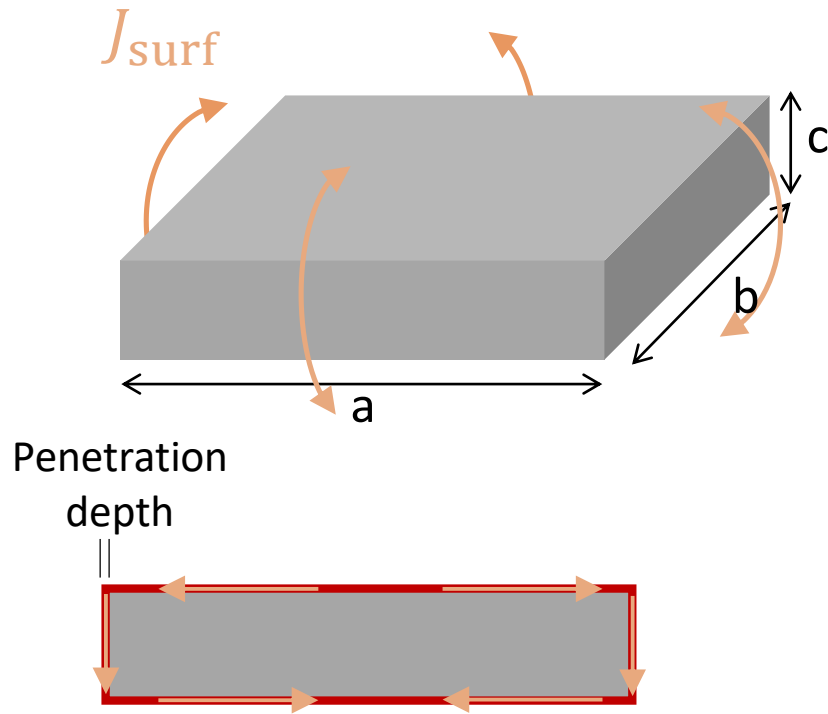
# What can go wrong in an empty box?



$$\frac{1}{Q_{\text{int}}} = \frac{P_{\text{diss}}}{\omega U_{\text{tot}}} = \sum_i \frac{P_{\text{diss},i}}{\omega U_{\text{tot}}} = \sum_i \overset{\substack{\text{geometry} \\ \swarrow}}{\underset{\nwarrow}{\text{material}}} \frac{p_i}{q_i}$$
$$p_i = \frac{\text{energy stored in element } i}{\text{total energy}}$$

For systematic studies, see for example:  
C. Wang et al., Appl. Phys. Lett. (2015)  
W. Woods et al., Phys. Rev. Applied (2019)

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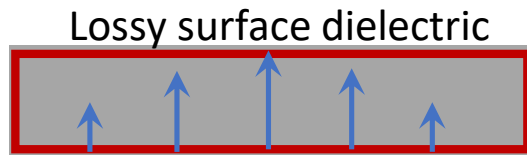
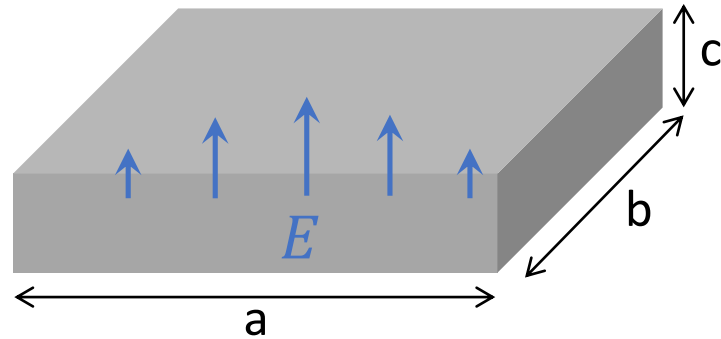


**Conductor loss:** magnetic losses in w/in penetration depth (quasiparticles, vortices, magnetic impurities,...)

$$\frac{1}{Q_{\text{cond}}} = \frac{R_{\text{sq}}}{\mathcal{G}} = \frac{p_{\text{cond}}}{q_{\text{cond}}} \leftarrow \begin{array}{l} \text{Kinetic inductance fraction} \\ \text{(Surface resistance)}^{-1} \end{array}$$

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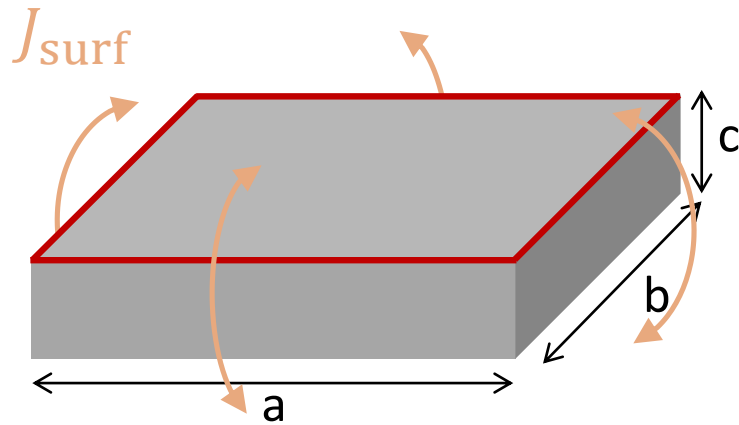
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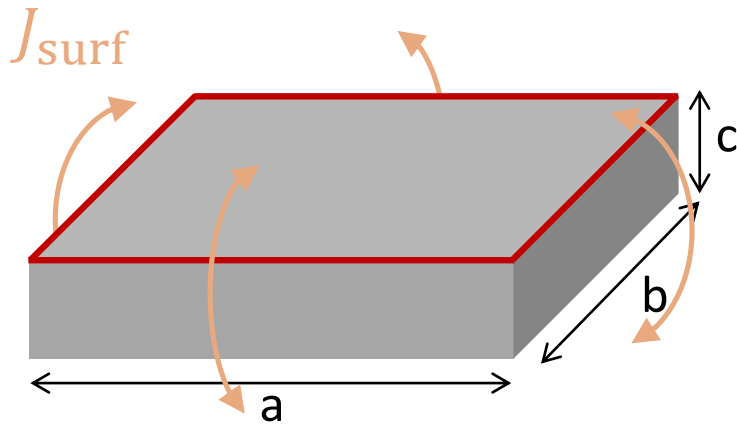
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**Seam loss:** resistance at joint between two halves of cavity (unbroken oxides, contaminants)

$$\frac{1}{Q_{\text{seam}}} = \frac{y_{\text{seam}}}{g_{\text{seam}}} \leftarrow \begin{array}{l} \text{Current across seam} \\ \text{Seam conductance} \end{array}$$

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**Dielectric loss:** electrical losses in thin surface dielectric (lossy oxides, contaminants)

$$\frac{1}{Q_{\text{int}}} = \frac{p_{\text{cond}}}{q_{\text{cond}}} + \frac{p_{\text{surf}}}{q_{\text{surf}}} + \frac{y_{\text{seam}}}{g_{\text{seam}}} + \dots$$

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**Typically, all participations drop with increasing volume**

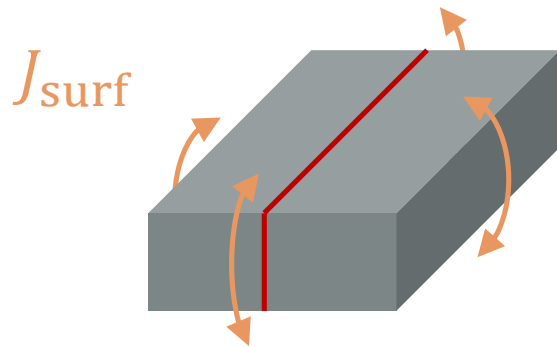
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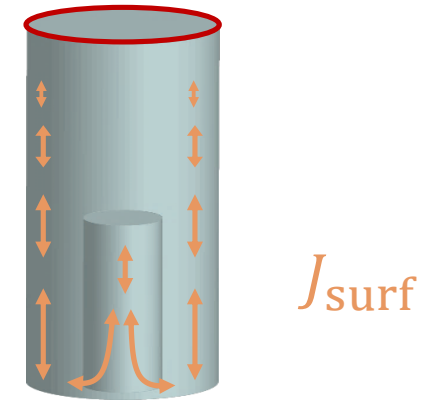
# Avoiding seam loss through clever design

Design zero current across lossy seam (by symmetry)



**Al rectangular**  
 $Q = 77 \times 10^6$   
 $\tau = 1 \text{ ms}$

Design exponentially small current across lossy seam



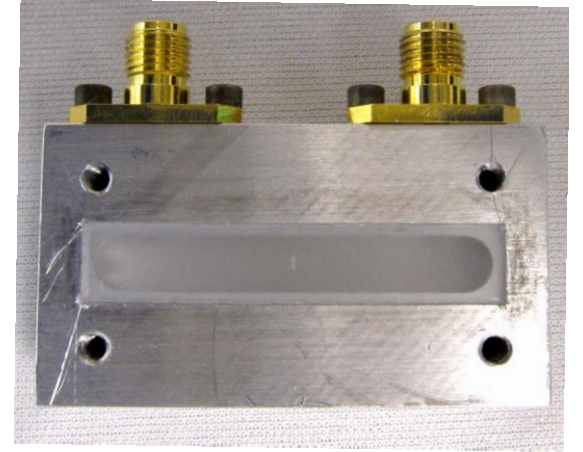
**Al stub**  
 $Q = 70 \times 10^6$   
 $\tau = 3 \text{ ms}$

**What about a use case  
where this is impossible?**

# Moving forward: complex circuits with 3D cavities?

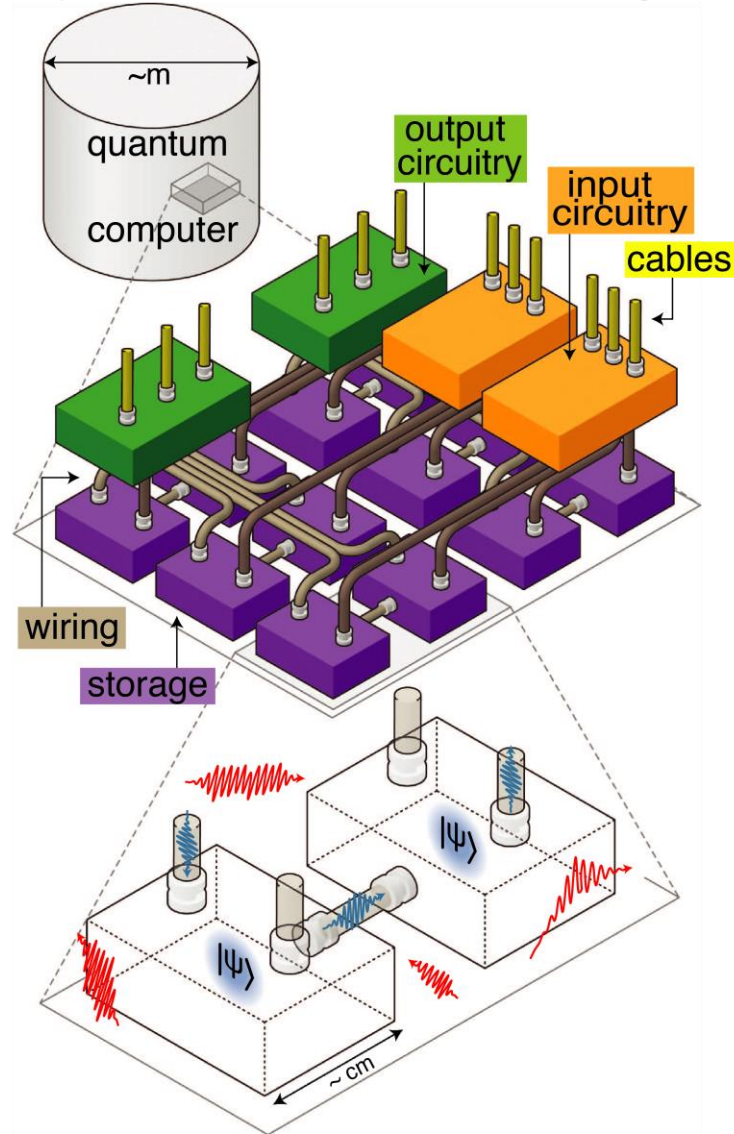
Bulk machining is a “necessary evil”

- Huge (10x) variability in surface quality
- Far less precision compared to lithography
- Milling machines have a particular set of design constraints
- **Difficult to integrate with certain useful circuitry**



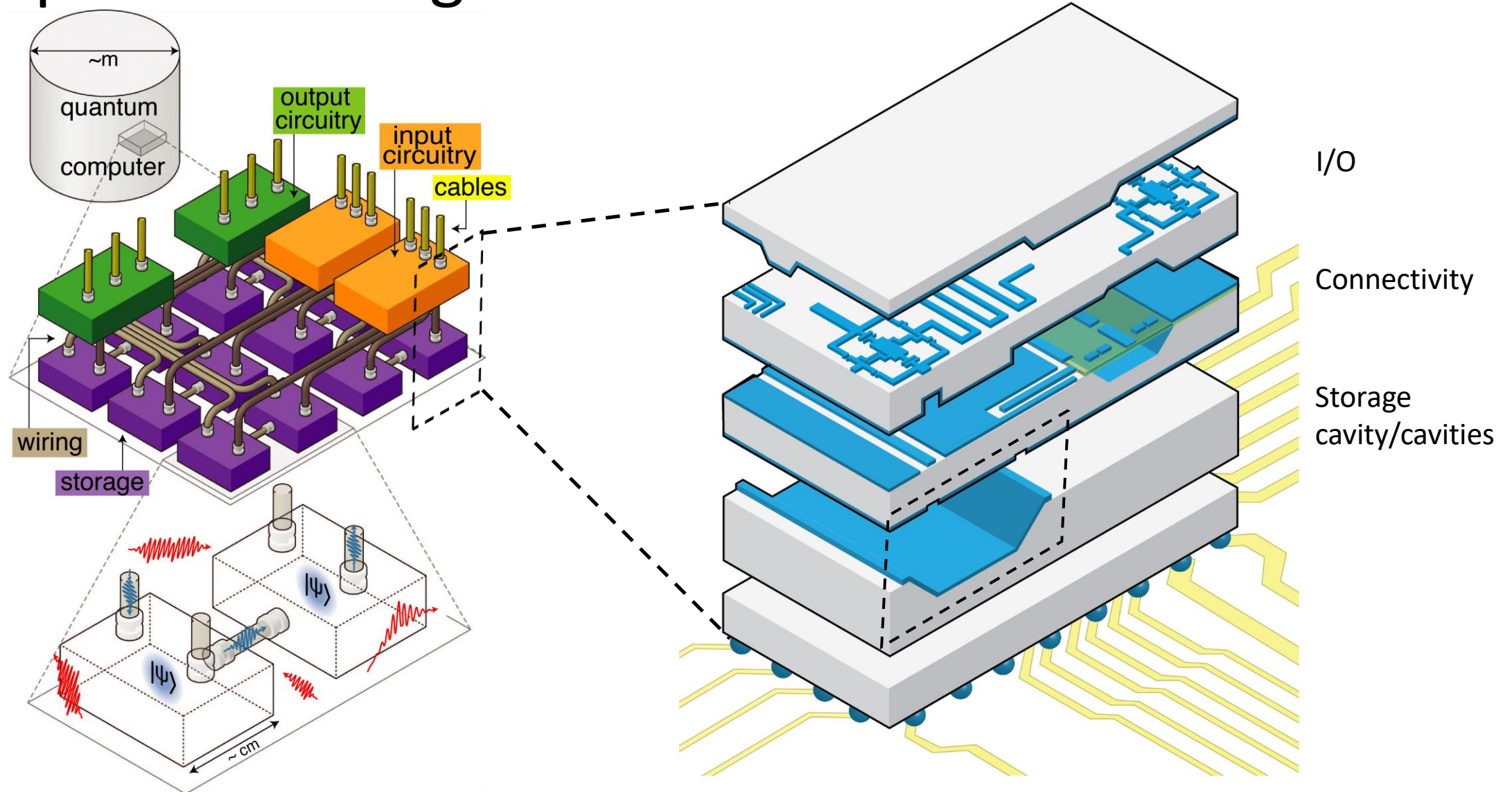
**Can we make a cavity lithographically with the same (or higher) quality?**

# Prospects for integration within a module





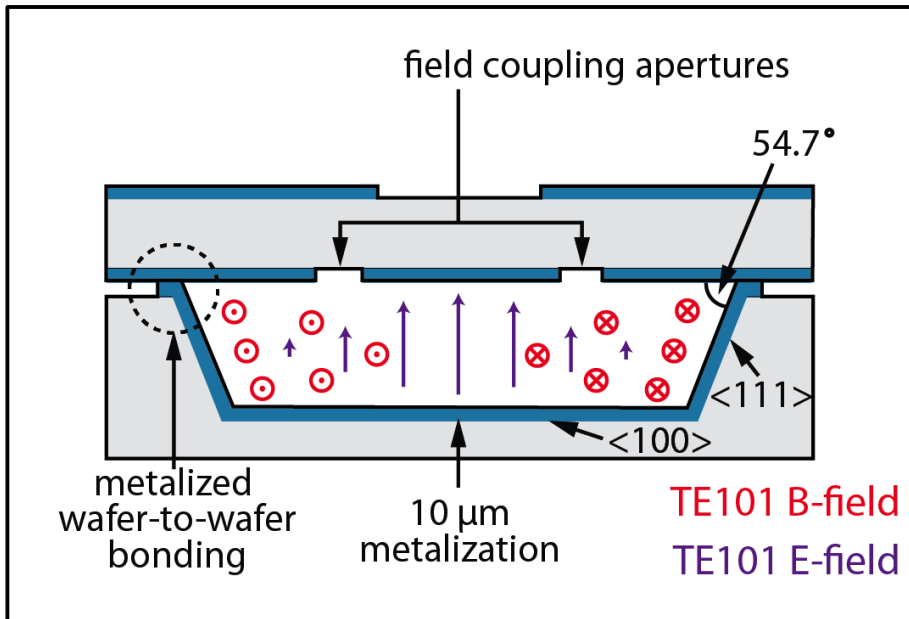
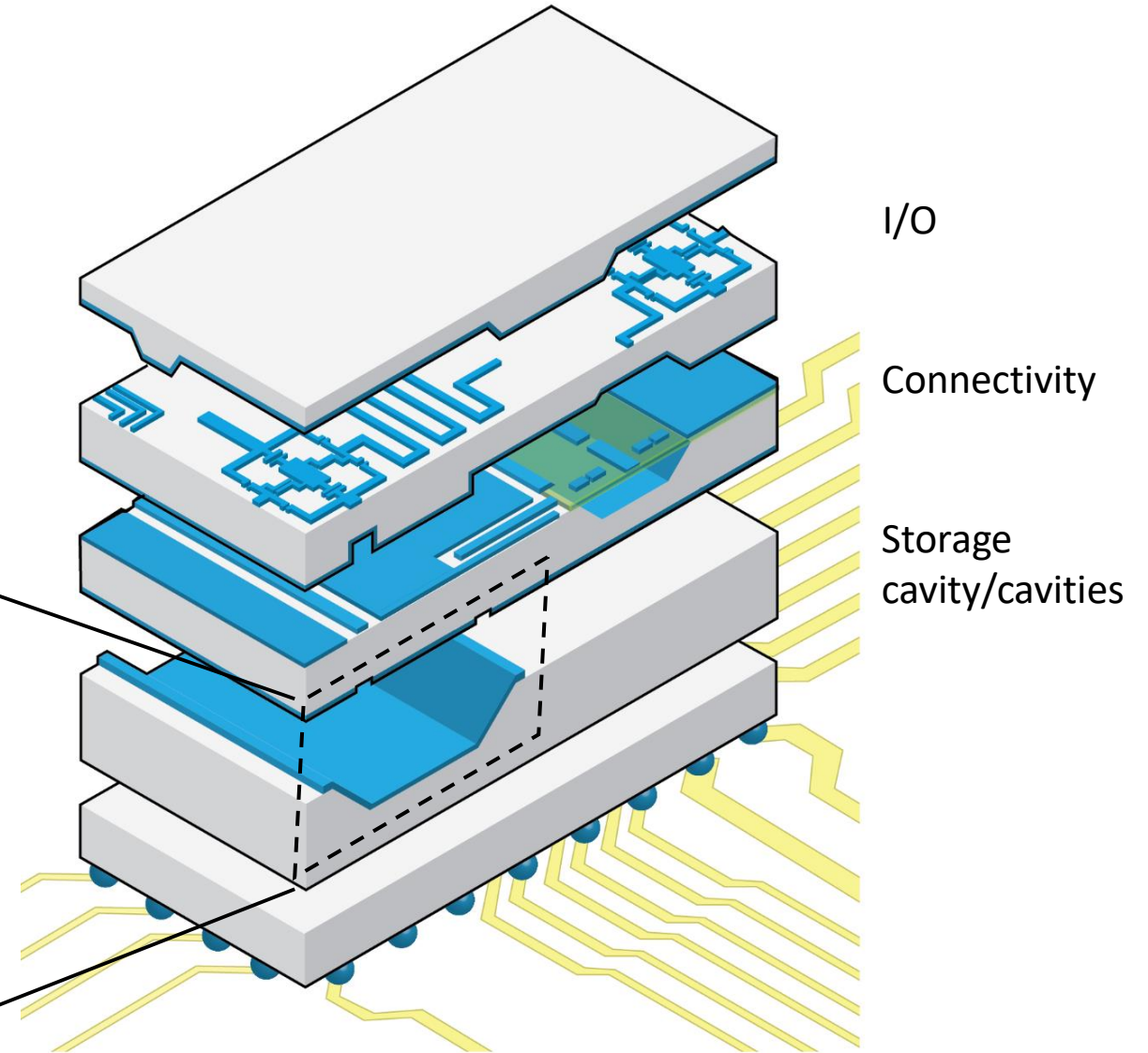
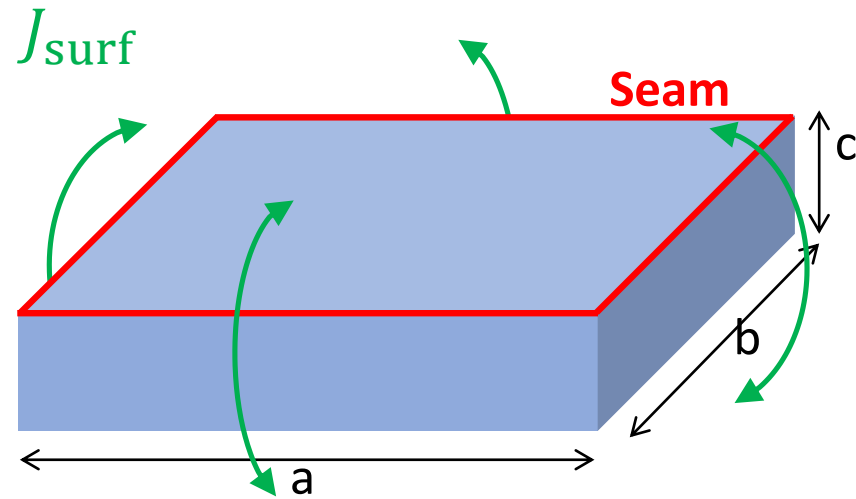
# Prospects for integration within a module



T. Brecht *et al.*, Nature Quantum Info, 2016

D. Rosenberg *et al.*, Nature Quantum Info, 2017

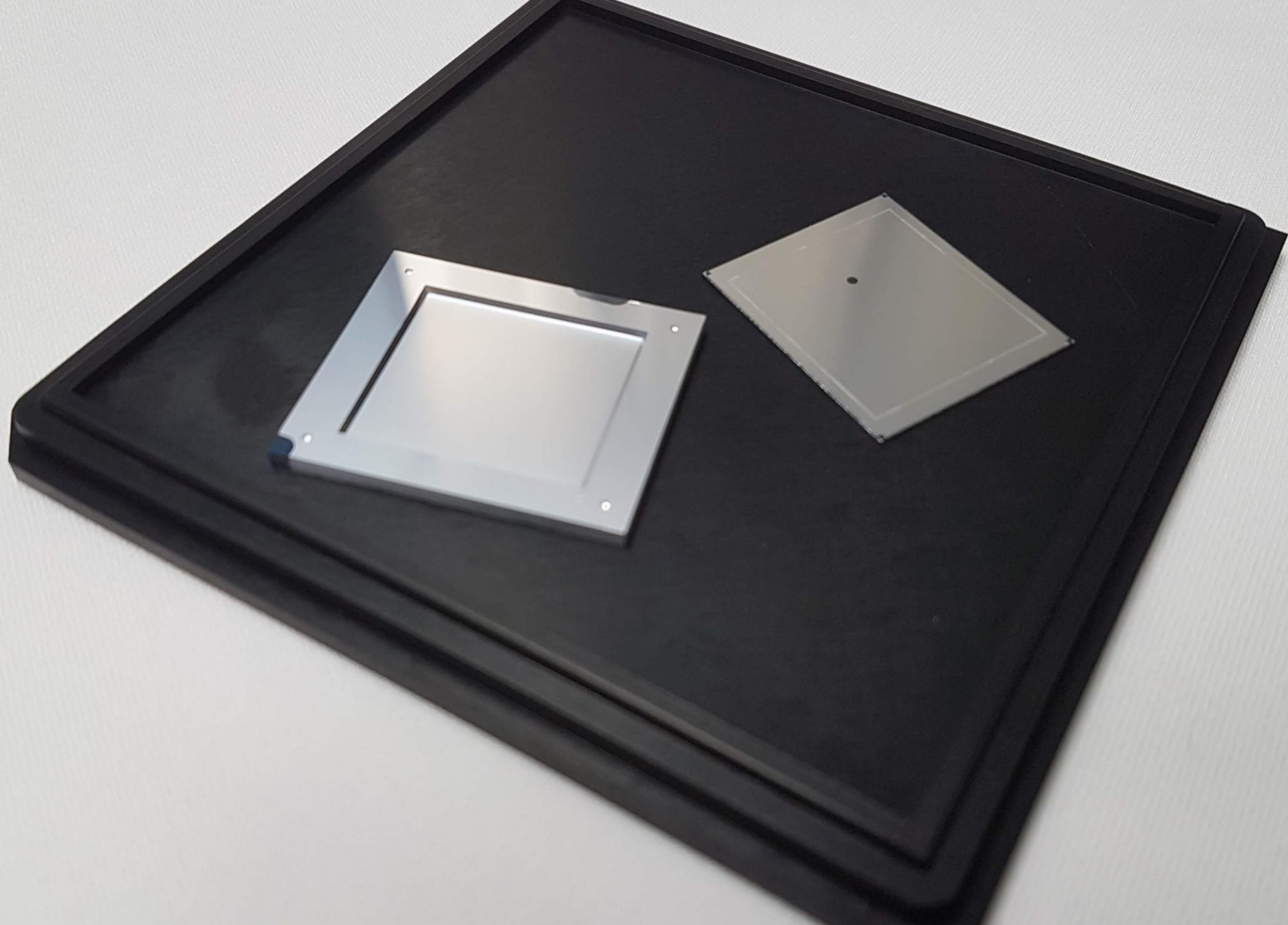
# Prospects for integration within a module

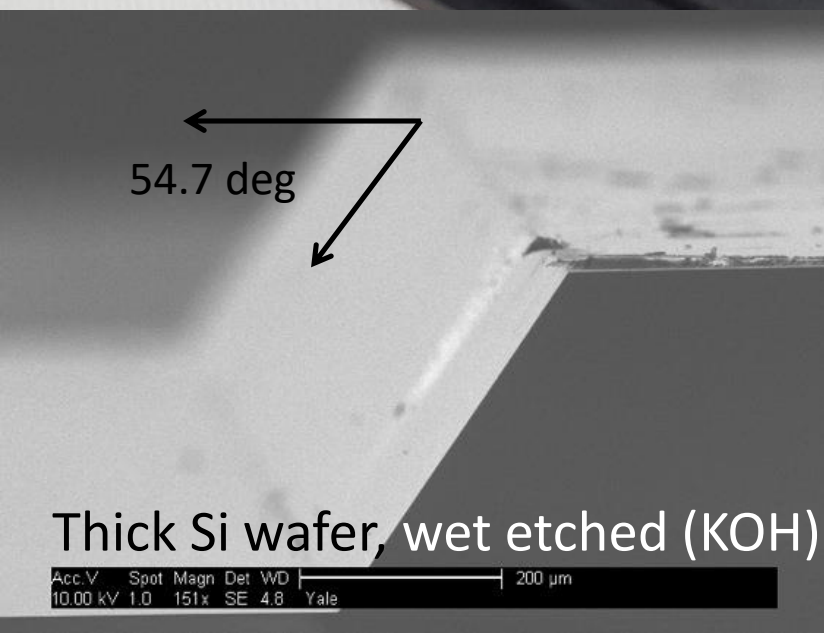
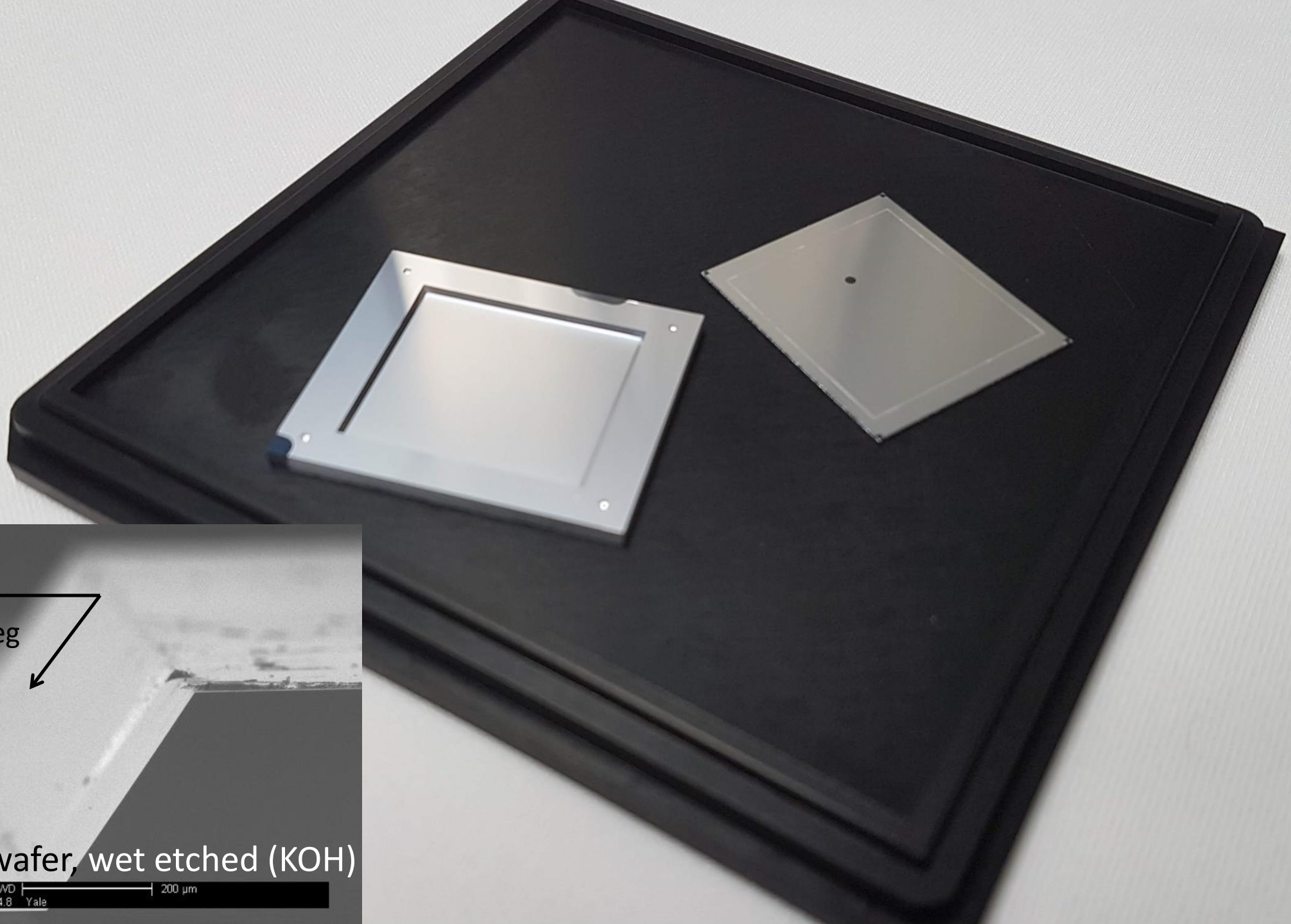


T. Brecht *et al.*, Nature Quantum Info, 2016

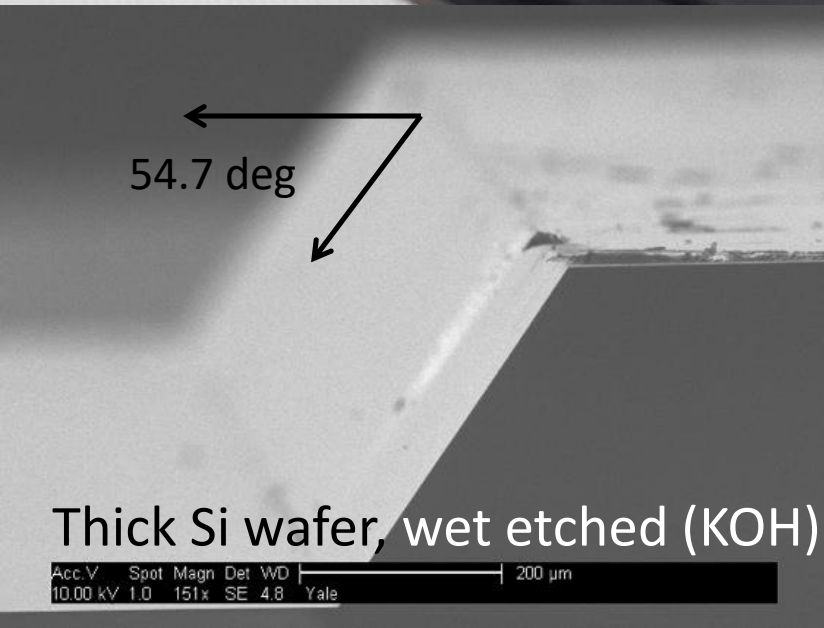
D. Rosenberg *et al.*, Nature Quantum Info, 2017





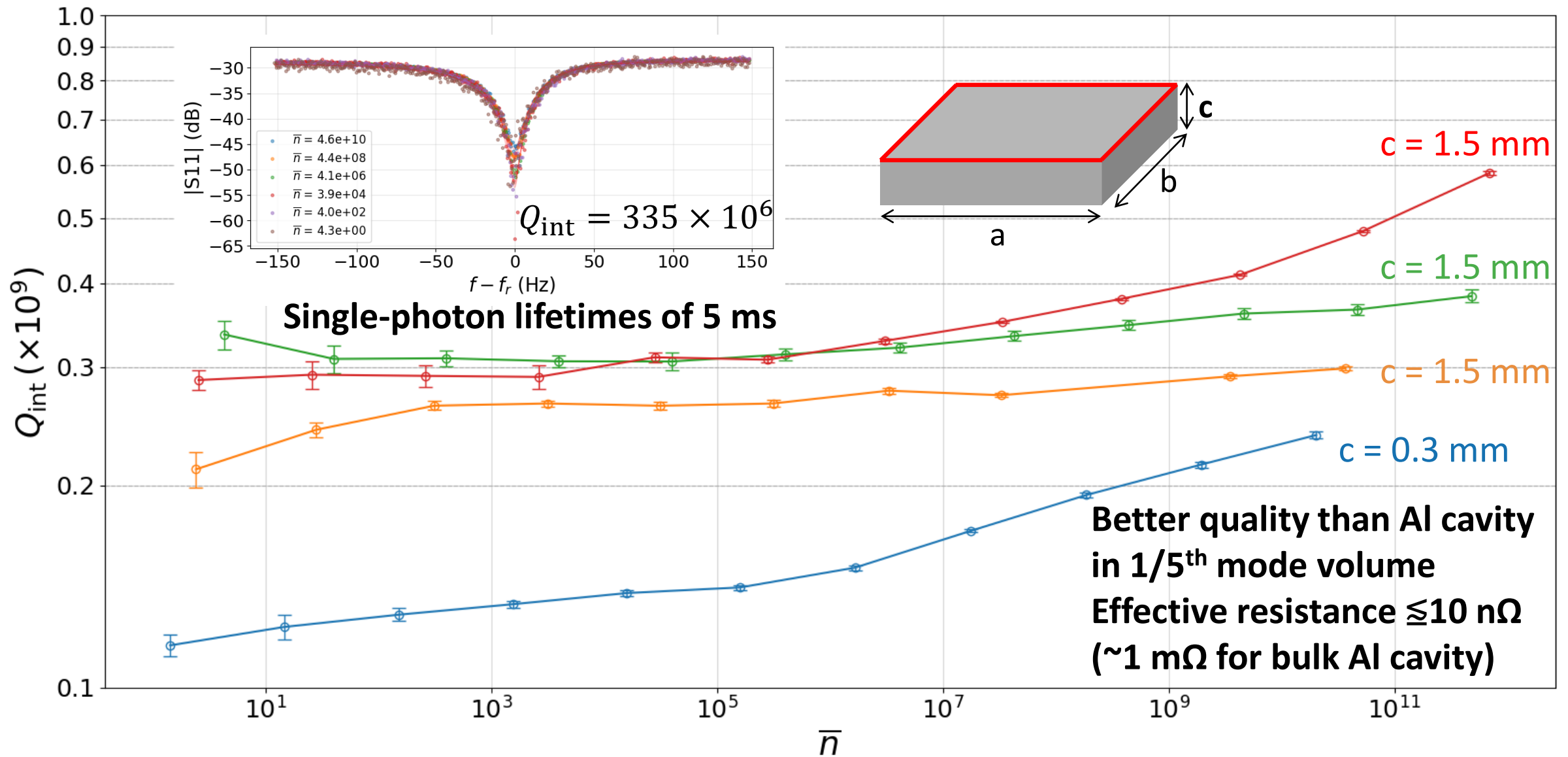








# A (very) high-Q cavity in a wafer



# Outlook: high-Q cavities in integrated quantum circuits

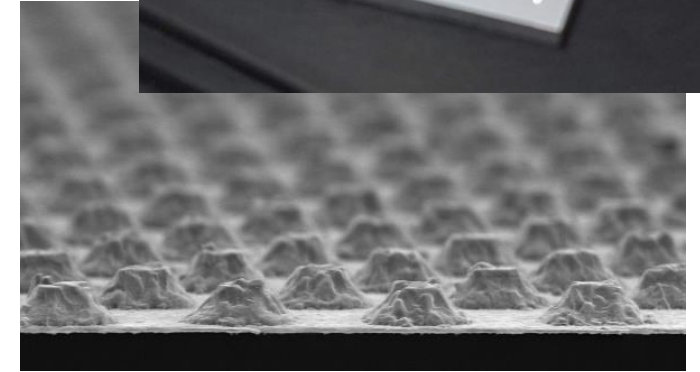
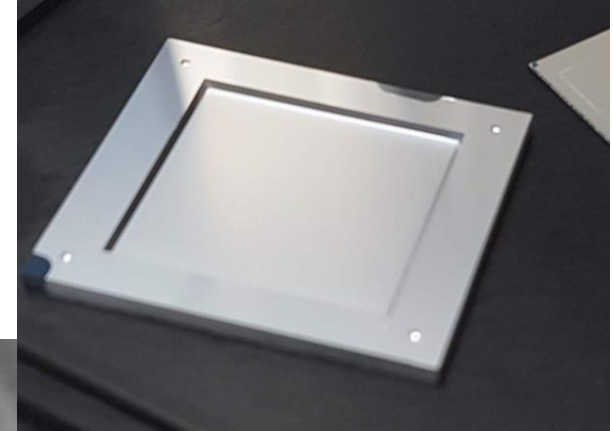
As technology develops, complexity within modules can increase, and connecting modules gets us to the next level of scale

Cavities make excellent circuit elements for modular quantum computers

3D integration of microwave circuits in a very novel regime

- nano-ohm RF resistance
- extremely low-loss dielectrics
- thin and rare contaminants

**Thank you!**



**Special thanks to:**

Chan U Lei  
Lev Krayzman  
Rob Schoelkopf (PI)



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