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BIS - BIC - SMP

The Beam Interlock System (BIS) and Safe Machine Parameters (SMP) system are central to the protection of the Large Hadron Collider (LHC) machine.

The BIS has been critical for the safe operation of LHC from the first day of operation. It has been installed and commissioned, only minor enhancements are required in order to accommodate all future LHC machine protection requirements.

At reduced intensity, the SMP system is less critical for LHC operation. As such, the current system satisfies the 2010 operational requirements. Further developments are required, both at the SMP Controller level, and at the system level, in order to accommodate the requirements of the LHC beyond 2010

Summary

Performance, problems encountered, is related software sufficient, necessary improvements, appropriate for 3.5TeV running?

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