AIDA-2020 Fourth Annual Meeting

Wednesday 3 April 2019

WP4 Micro-electronics and interconnections: WP4 Micro-electronics and interconnections - Seminar Room 11 (14:30 - 18:20)

time	[id] title	presenter
14:30	[514] Introduction	DE LA TAILLE, Christophe RE, Valerio
14:40	[545] HGCROC1 test results and HGCROC2 design	DE LA TAILLE, Christophe
15:00	[526] A high-resolution Vernier Ring Oscillator TDC in TSMC 130 nm CMOS for CMS MG-RPC	DAHOUMANE, Mokrane DAHOUMANE, Mokrane
15:15	[593] Development of blocks for FLAME and HGCROC2 chips in TSMC 130nm and for lpGBT chip in TSMC 65nm	IDZIK, Marek
15:30	Coffee Break	
16:00	[515] Recent results of the RD53A front-end chip	Mr LAURITZEN, Magne Eik
16:20	[516] From RD53A to RD53B: development of the next generation readout chips for the ATLAS and CMS experiments at the HL-LHC	GAIONI, Luigi GAIONI, Luigi
16:40	[561] Tests on FE-I4 modules with TSV, plans for TSV processing in RD53A wafers	HUEGGING, Fabian HUEGGING, Fabian
17:00	[612] TSV LETI summary	LOUNIS, Abdenour
17:20	[611] Discussion	RE, Valerio