

21st International Workshop on Radiation Imaging Detectors

Wednesday, 10 July 2019

CMOS, ASIC, chair: George Fanourakis (09:00 - 10:30)

time	[id] title	presenter
09:00	[67] High data-rate readout logic design for 1024*512 CMOS pixel array dedicated for CEPC experiment	Ms WEI, Xiaomin
09:15	[8] Predicting the radiation damage in a space-qualified high performance CMOS image sensor	Dr CREWS, Chiaki
09:30	[152] FRIC - A 50 μm pixel-pitch single photon counting ASIC with Pattern Recognition algorithm in 40 nm CMOS technology	OTFINOWSKI, Piotr
09:45	[120] Characterization of the IBEX ASIC for Electron Detection	Dr RADICCI, Valeria
10:00	[162] Single Event Upsets in the ATLAS IBL Frontend ASICs at the Large Hadron Collider at CERN	ROZANOV, Alexandre
10:15	[97] Prototype single-ended and differential charge processing circuits for micro-strip silicon and gaseous sensors read-out	ZUBRZYCKA, Weronika