

Electrical Defects in Co-cured Bus-Tapes and Quality Control Strategies

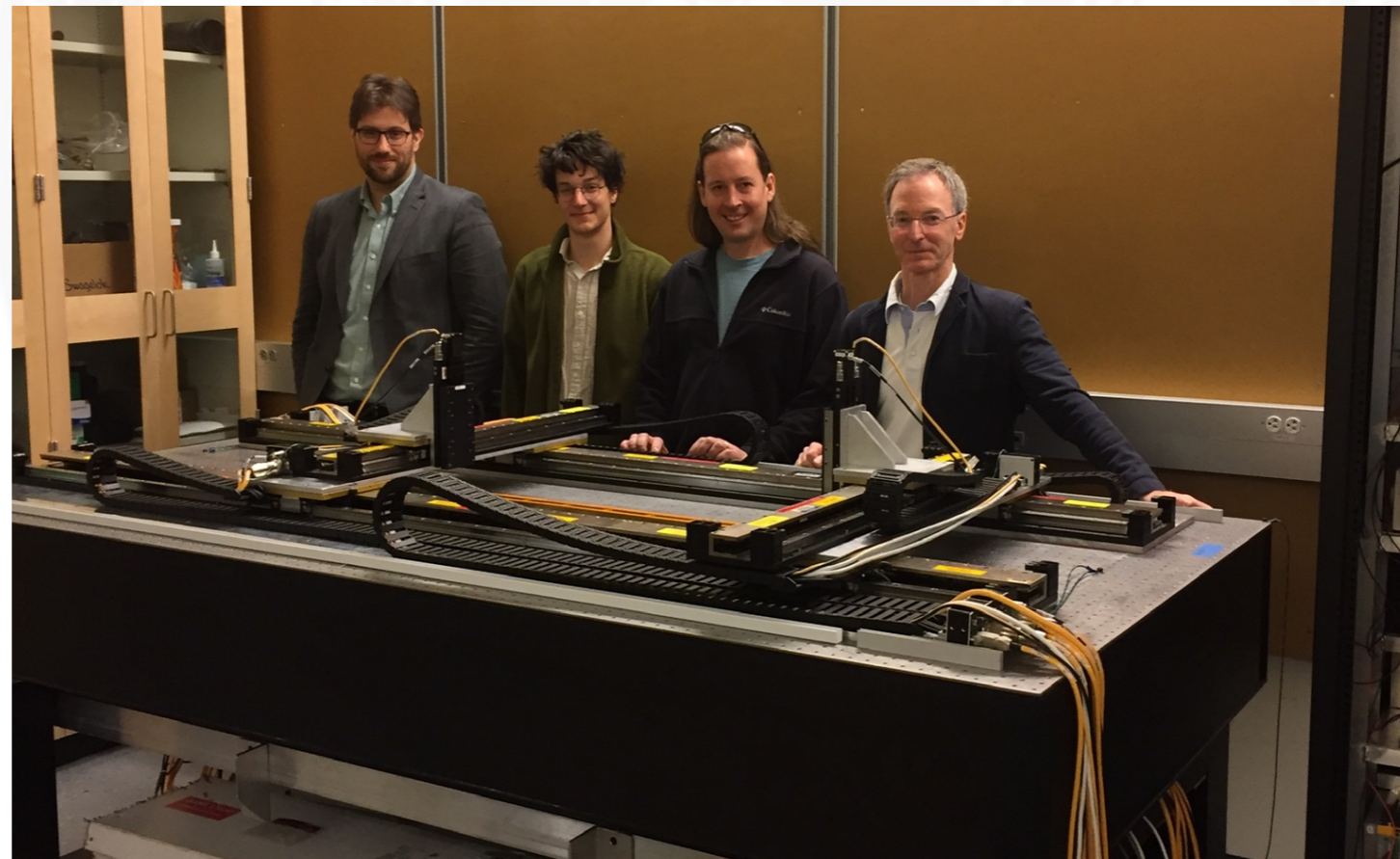
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Forum on Tracker Detector Mechanics 2019

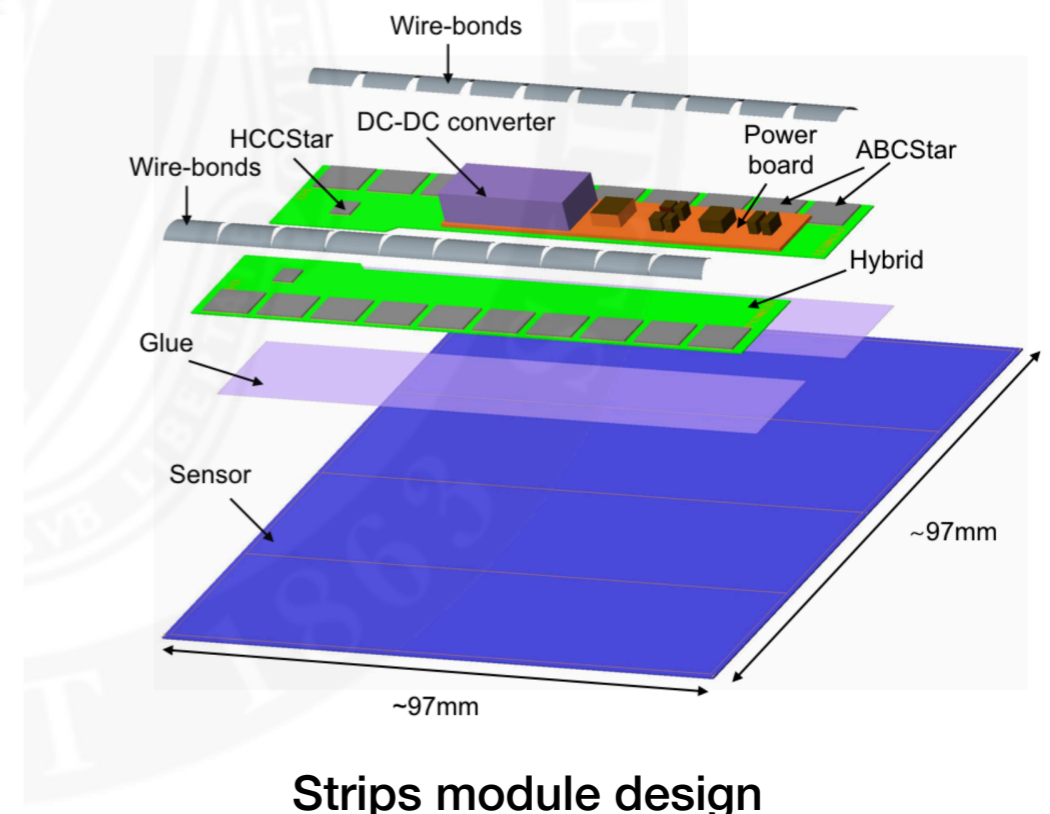
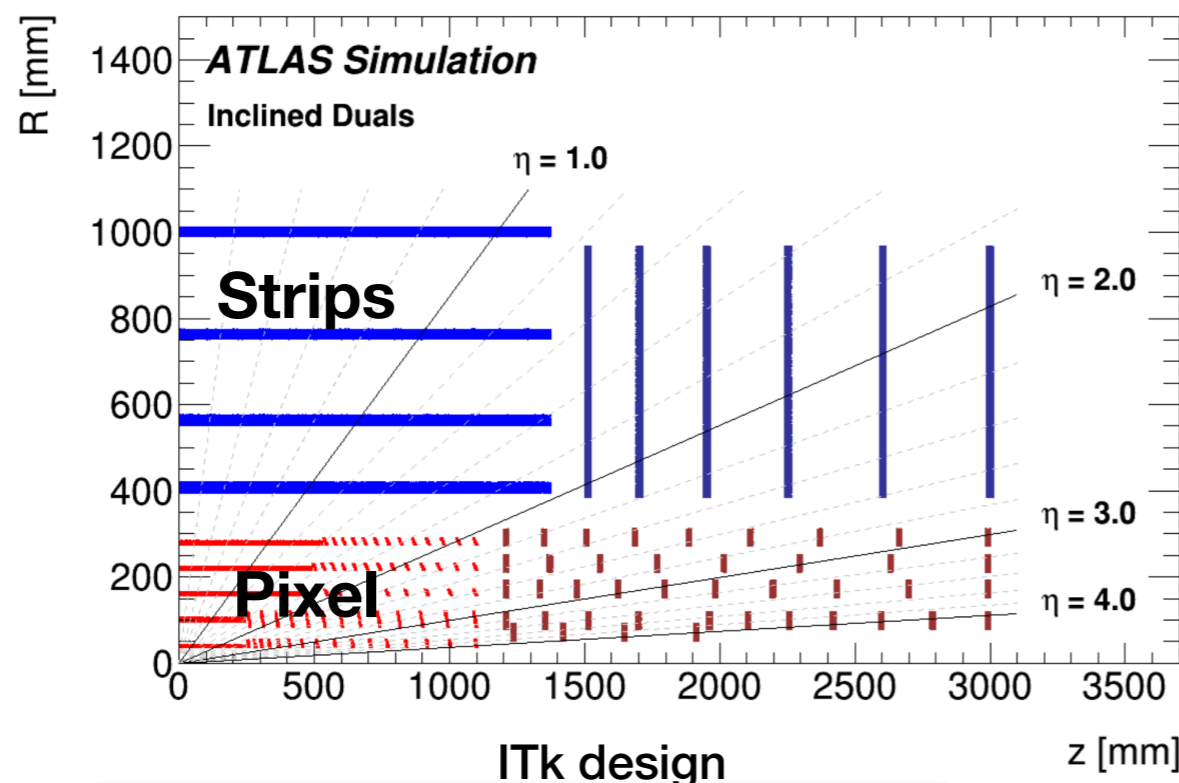
June 18, 2019

- Introduction to ATLAS ITk Strips detector
- Local support structures
- Bus-tape overview and design
- Observed bus-tape defects
- Quality control strategies

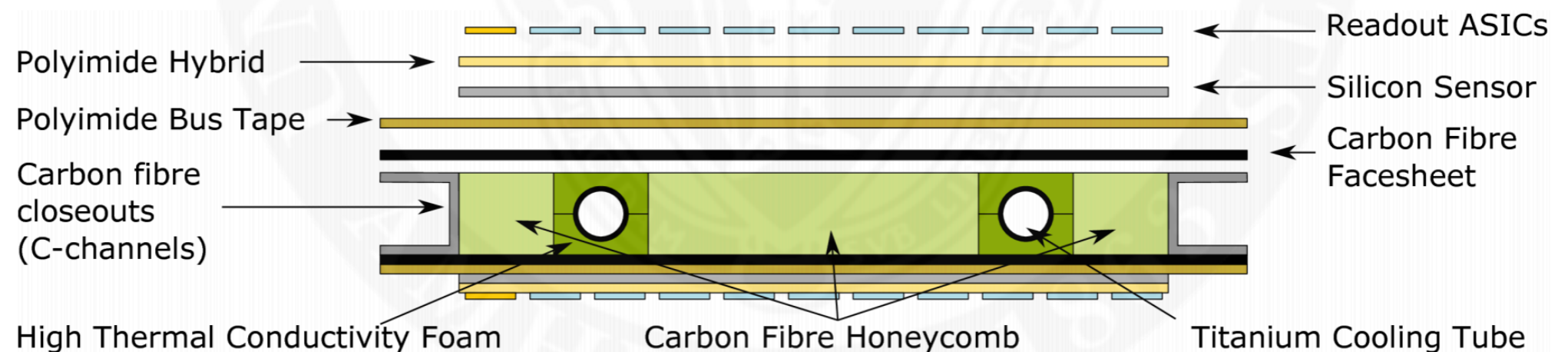


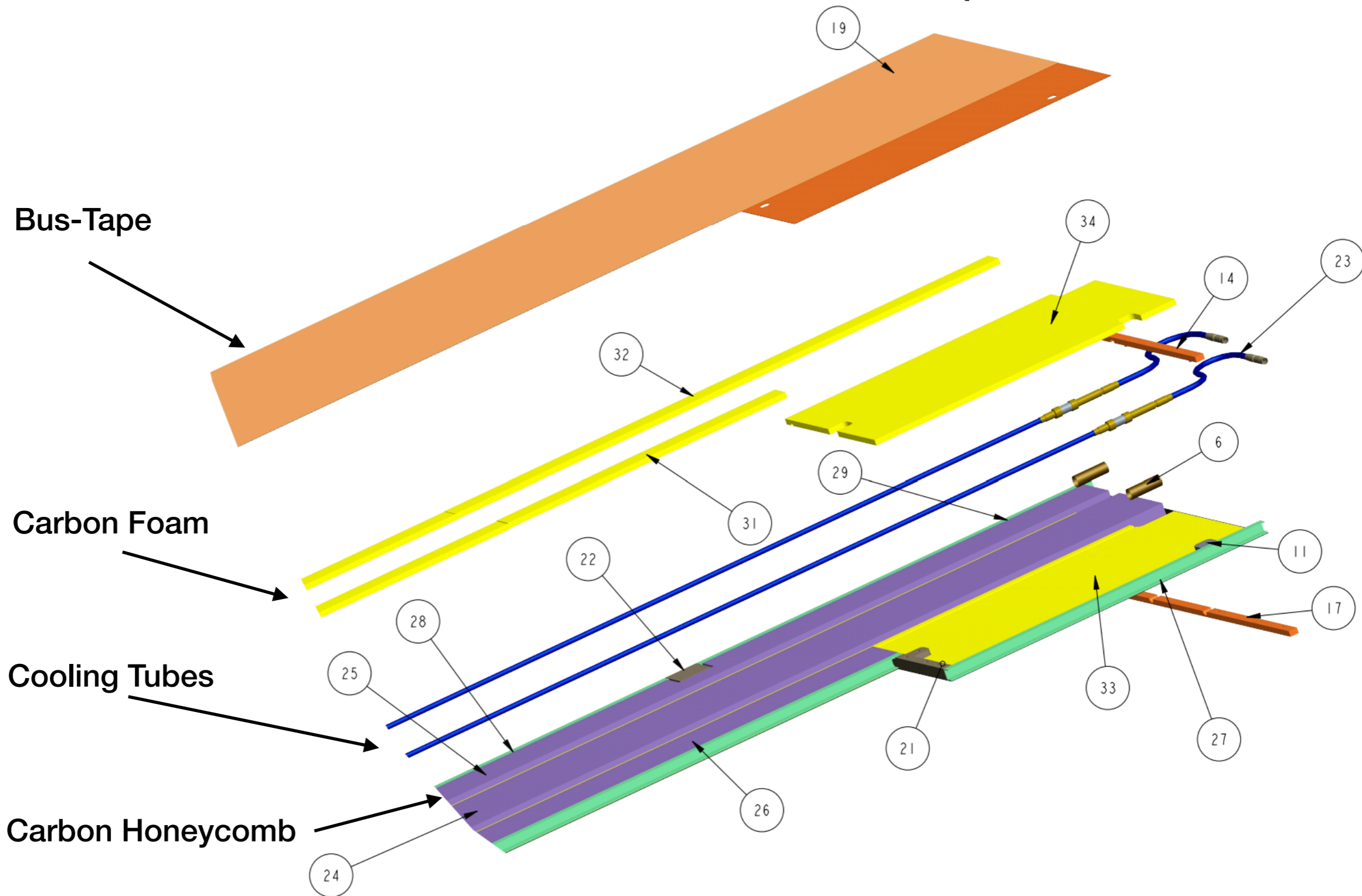
(Very) Brief Introduction to ITk Strip Detector

- Current ATLAS Inner Detector will be replaced with new all-silicon Inner Tracker (ITk) to accommodate tracking and radiation conditions at HL-LHC
- ATLAS ITk consists of pixel detector at small radius and a large-area **strip tracking detector** surrounding it
- Strips detector consists of silicon modules mounted on local support structures
- Strip modules:
 - ▶ Basic detector unit consisting of silicon sensor, a hybrid circuit hosting read-out ASICs, and a power board
 - ▶ Adhesively attached to local support structure and electrical connections made via wire-bonding

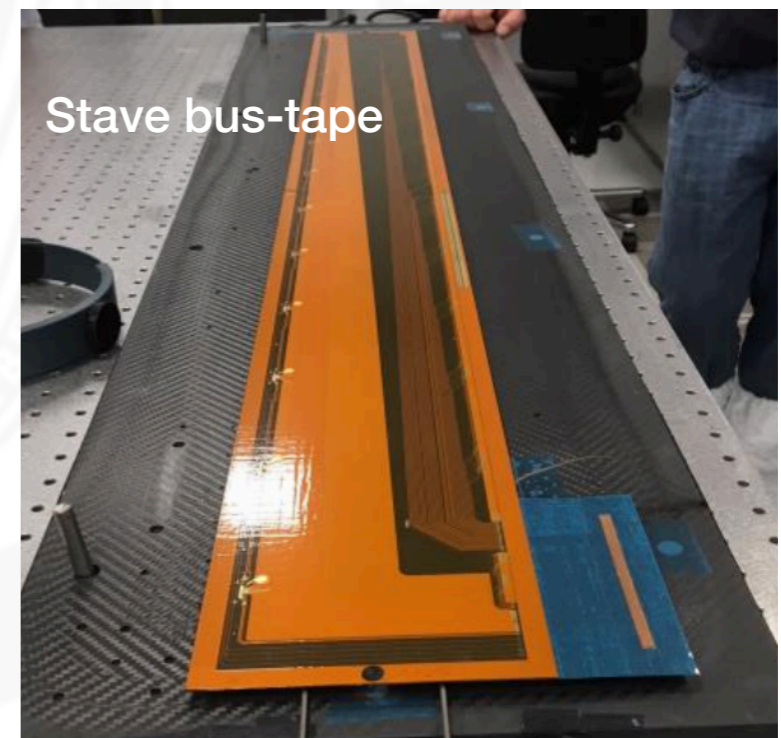


- Local support structures: stave cores (barrel) and petal cores (end cap)
 - ▶ Formed from two CF face-sheets, which sandwich a core that is made from cooling components and low-density CF honeycomb
 - ▶ Provide mechanical, thermal, and electrical support to modules
 - ▶ Electrical power and data transfer services carried by a copper/polyimide **bus-tape** mounted on both sides of structure and operated by EoS (End of Substructure) card
 - ▶ Modules adhesively attached to the two faces and electrical connections between the read-out hybrids and the bus-tape will be made via wire-bonding
 - ▶ 14 (9) modules mounted on each side of stave (petal)
- 392 stave cores and 384 petal cores to be installed in ITk

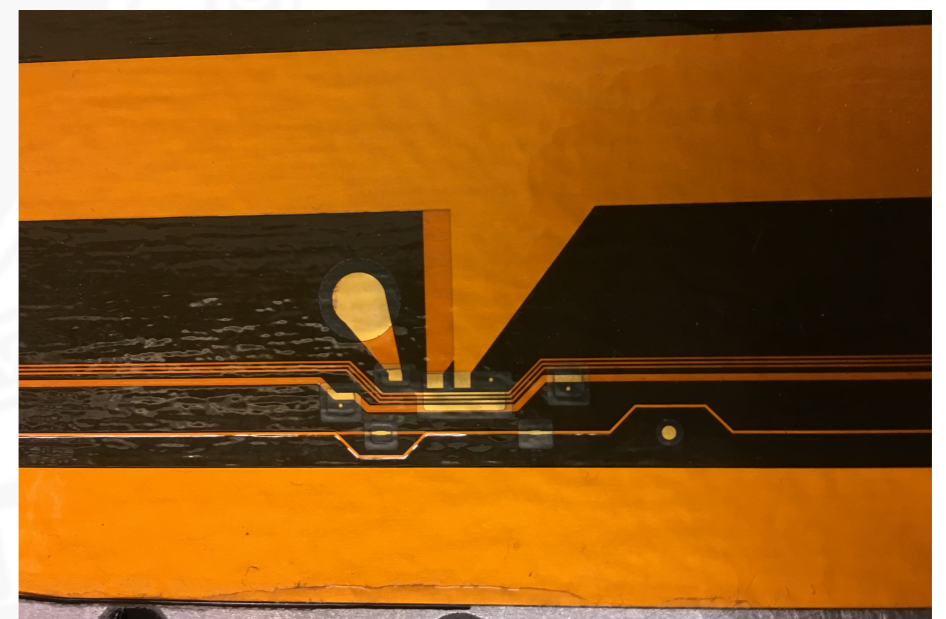
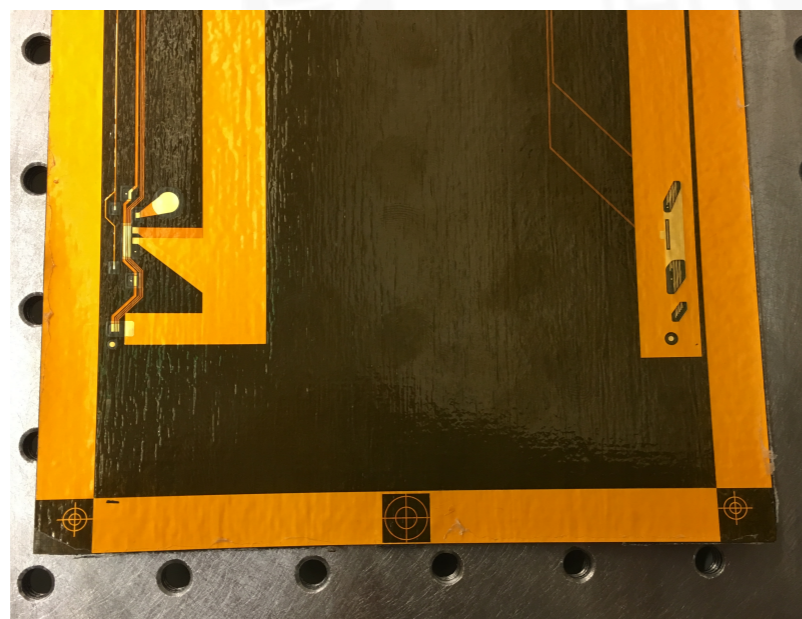
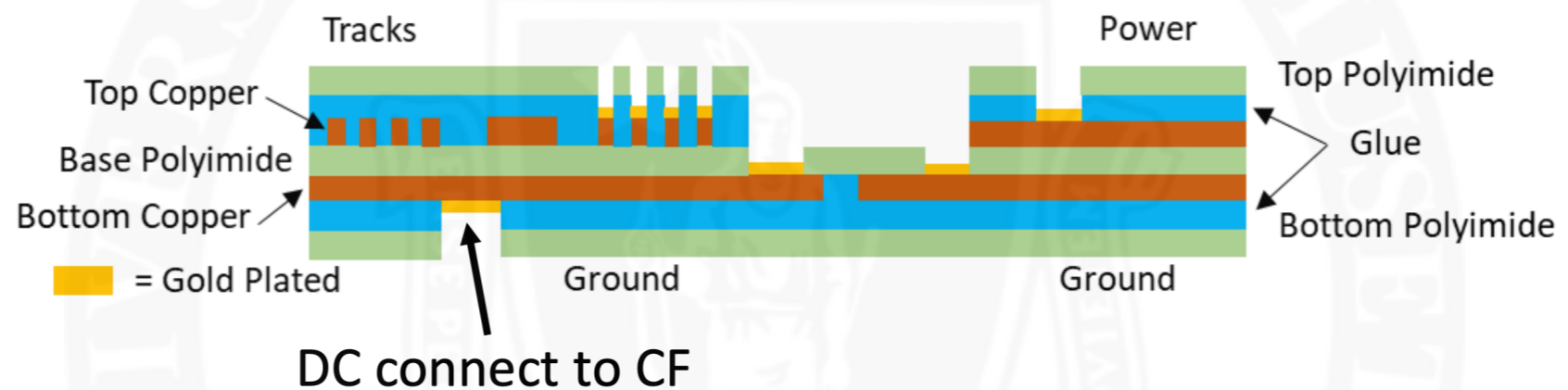


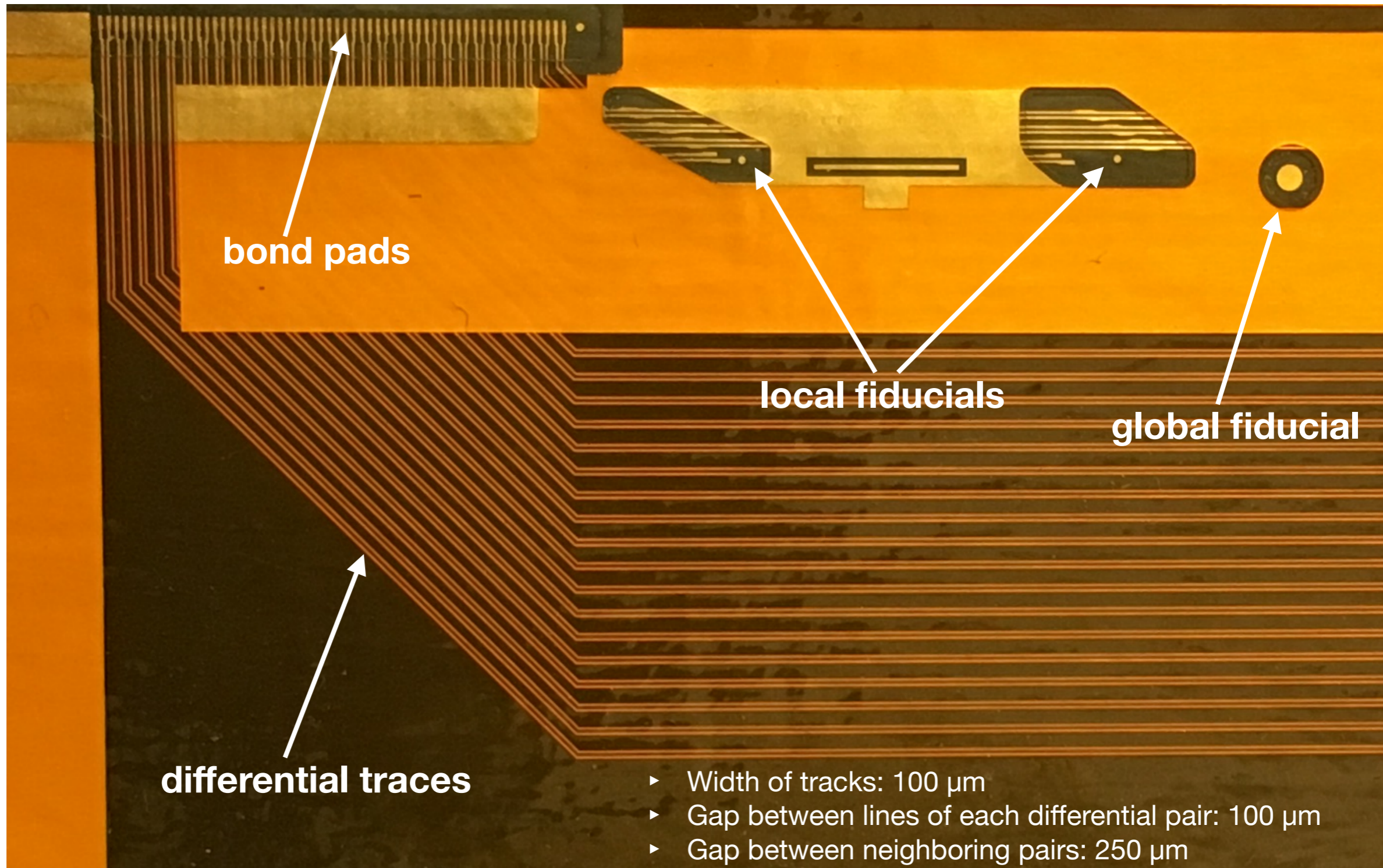


- The purpose of the bus tapes is to:
 - ▶ provide electrical power (low voltage DC (10-11V) and high voltage DC (up to 750V)) to the modules
 - ▶ transfer TTC (Timing, Trigger and Control) and measured data between the EoS and the hybrids
 - ▶ provide control data to the power boards
- Specifications:
 - ▶ Round-trip voltage drop for low-voltage lines $< 1\text{ V}$
 - ▶ High Voltage Insulation Resistance (HVIR) between neighboring HV lines $> 1\text{ G}\Omega$
- Identical technologies will be used for the barrel stave and petal bus-tapes

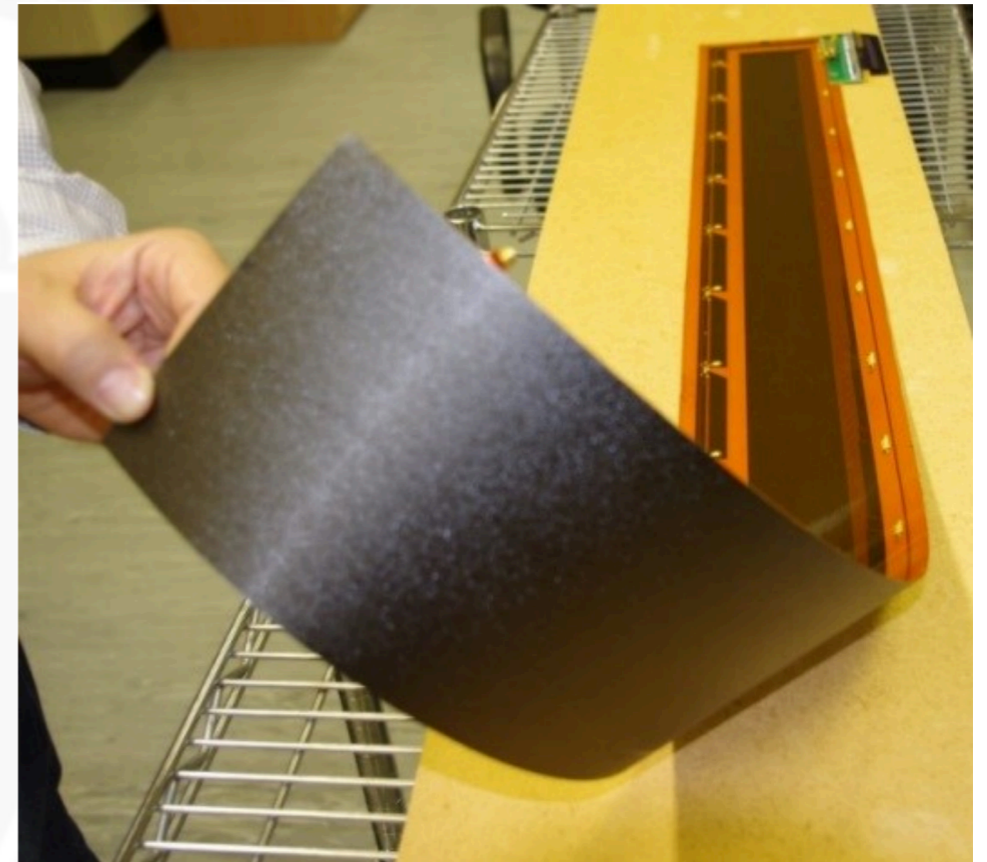


- Bus-tapes are made from two layers of adhesive-less copper/polyimide
- Differential lines to transfer high-speed (640 Mbps) data from the hybrids to the EoS
- Openings cut in cover layer around pads to allow standard nickel-gold plating for wire bonding hybrids to the tape
- Global and local fiducials included in tape design for QC testing





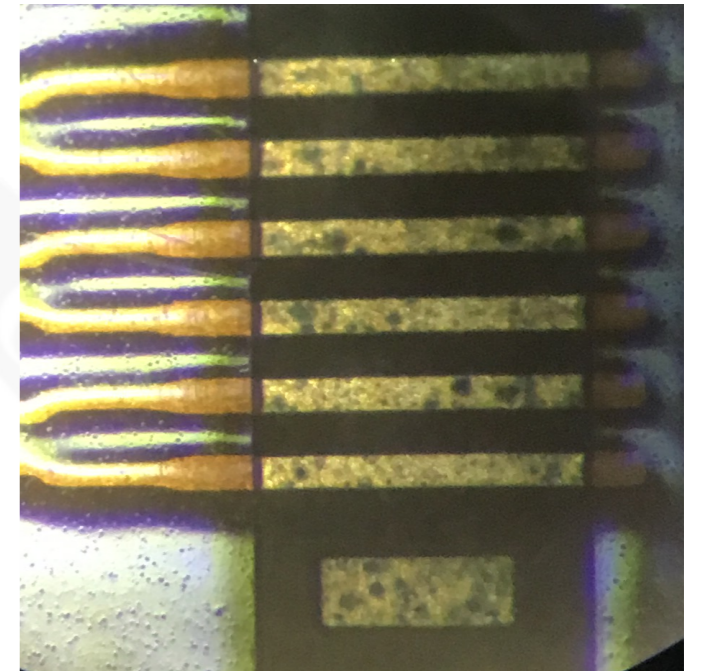
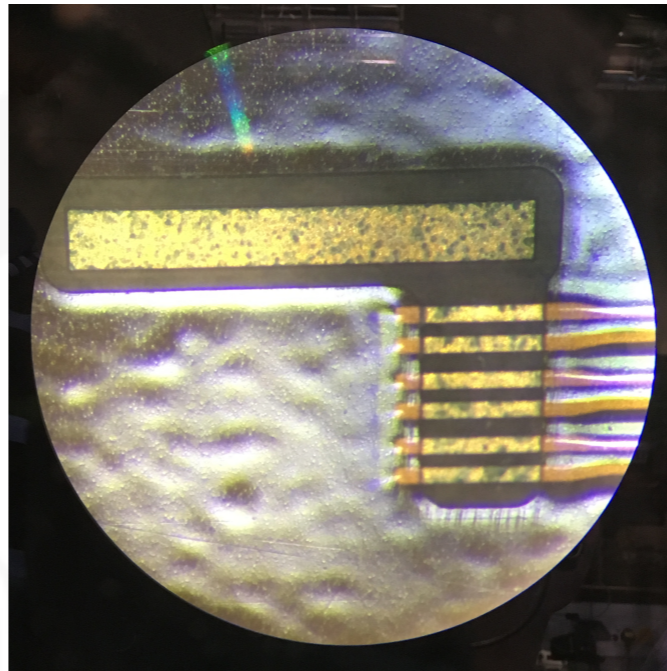
- The bus tapes will be co-cured with three layers of carbon fibre to produce the face sheets which are then mounted on both faces of the local support sandwich
- Co-curing the tape provides:
 - ▶ mechanical stability
 - ▶ thermal dissipation
 - ▶ electrical grounding
- Co-curing done in an autoclave at 120°C and 7 bar
- Bus-tape for the stave expands by as much as 0.5 mm after co-cure
- Tapes are cut with a 10 mm sacrificial rim to prevent resin from creeping around the edge of the tape and onto sensitive areas (bond pads)



Bus-Tape Defects & Consequences

- Several failure modes have been observed in the bus-tapes:

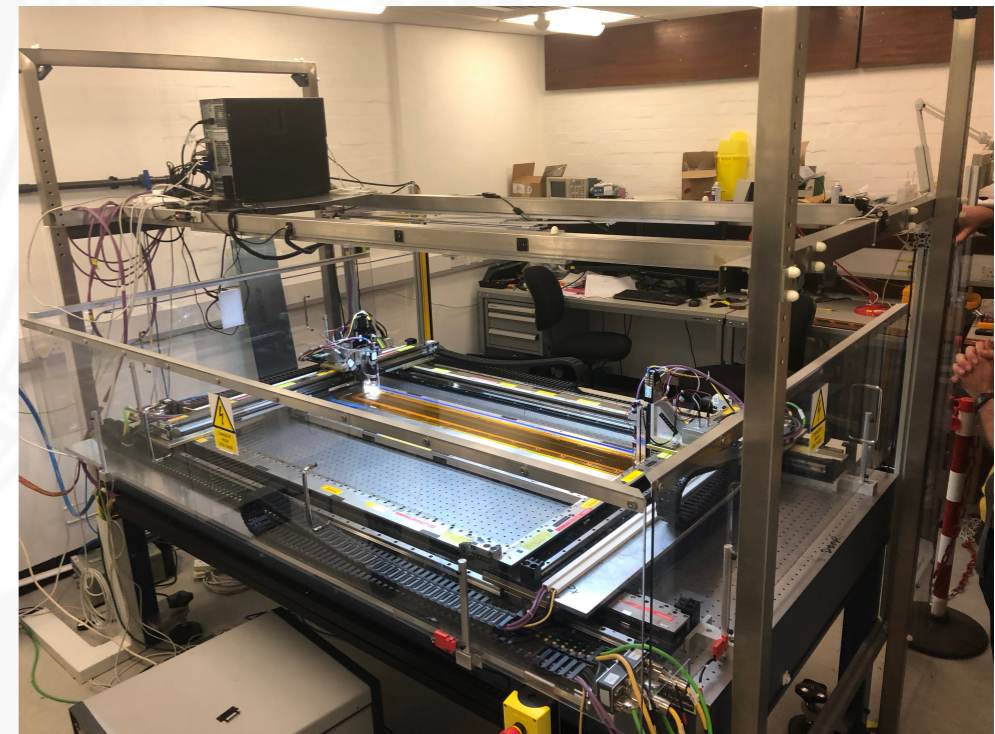
- ▶ Open circuits
- ▶ Short circuits
- ▶ Poor pad quality



- Consequences:

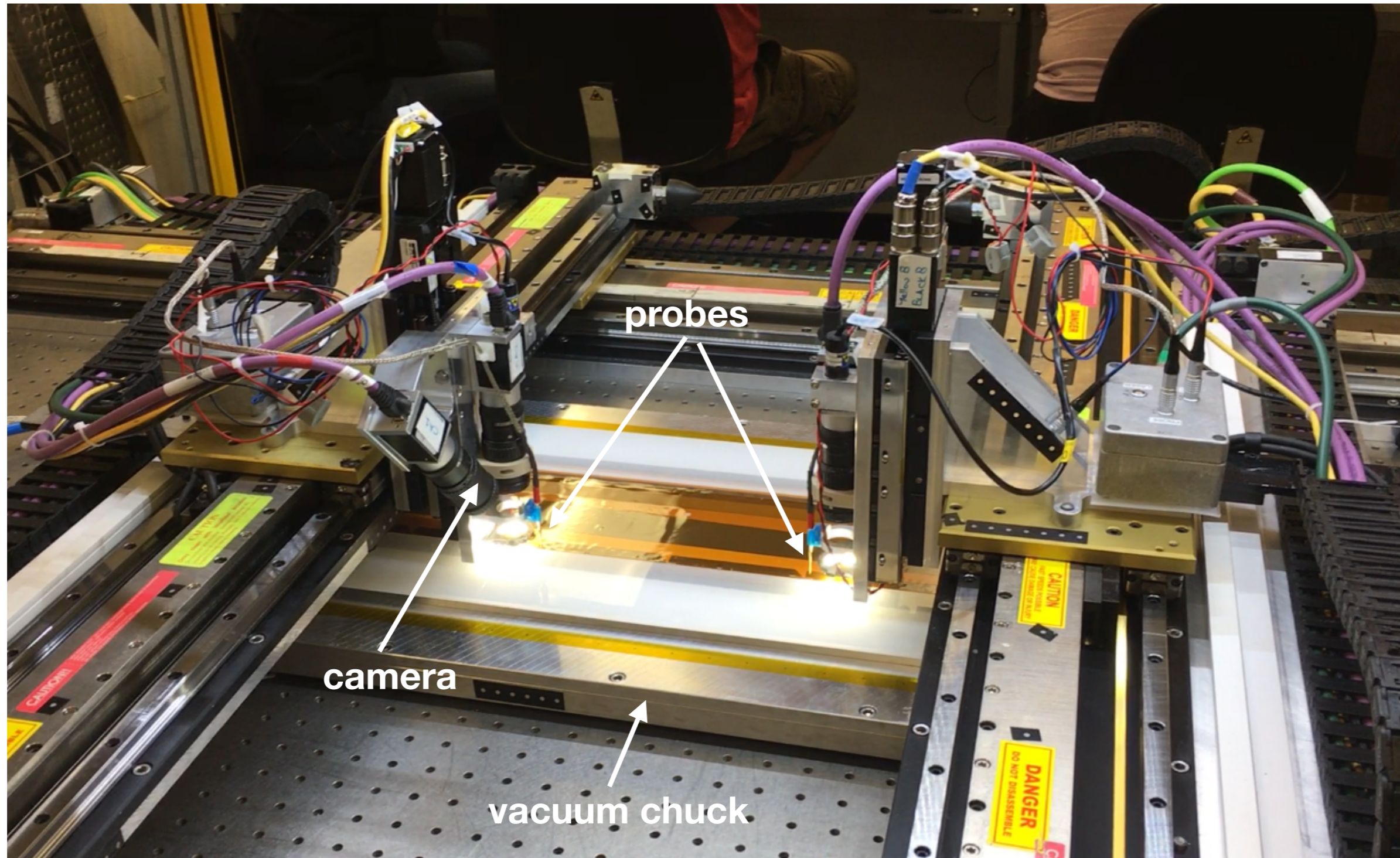
- ▶ A crack on a TTC line would result in the loss of physics data from up to 10 hybrids
 - ▶ In the worst case, this could make one side of a stave/petal inoperable
- Co-curing can introduce defects due to different thermal expansion between the tape material and the carbon fiber
 - ▶ Important to test all tapes at multiple stages during production

- A fully automated system has been developed by Roy Wastie at Oxford to perform QC testing of all bus-tapes
 - ▶ Due to distributed stave/petal core production, identical systems being built at UMass Amherst, DESY, and Ljubljana
 - ▶ Staves: Oxford/Yale, Petals: DESY/Ljubljana
- Design:
 - ▶ 2 independent probe heads on a gantry system with cameras for alignment
 - ▶ XY position control by linear stages, 5 μm resolution
 - ▶ Z position controlled by stepper motor, 1 μm resolution
 - ▶ Tape held down by vacuum chuck
- Test performed in multiple production stages:
 - ▶ Bare tape
 - ▶ After co-cure to carbon-fibre skin
 - ▶ After assembly into full mechanical stave/petal

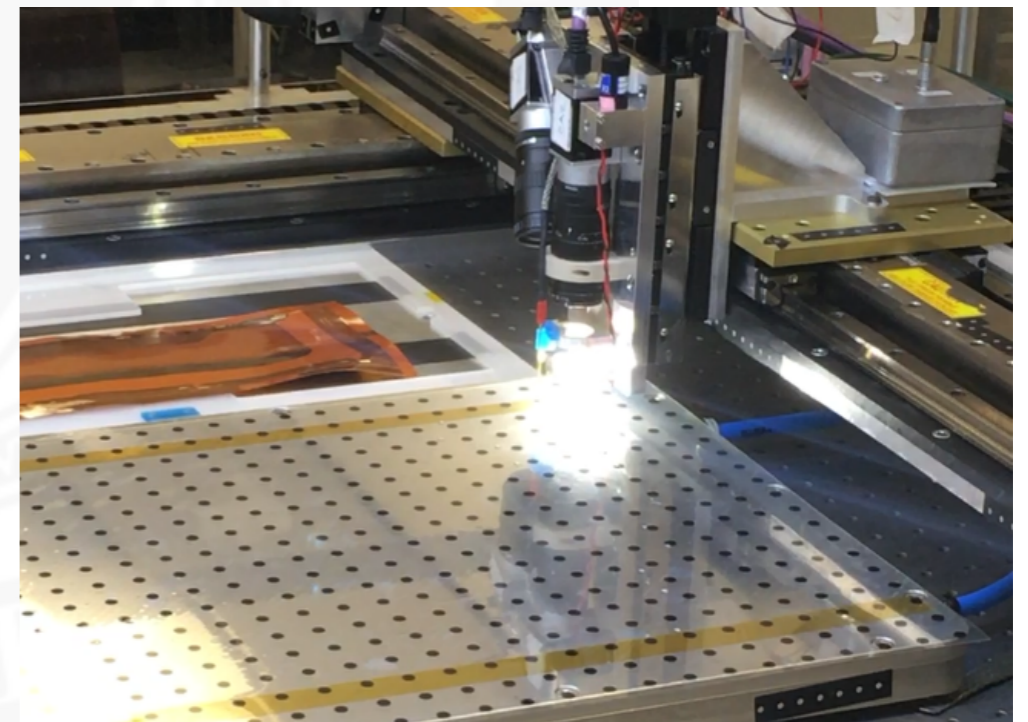
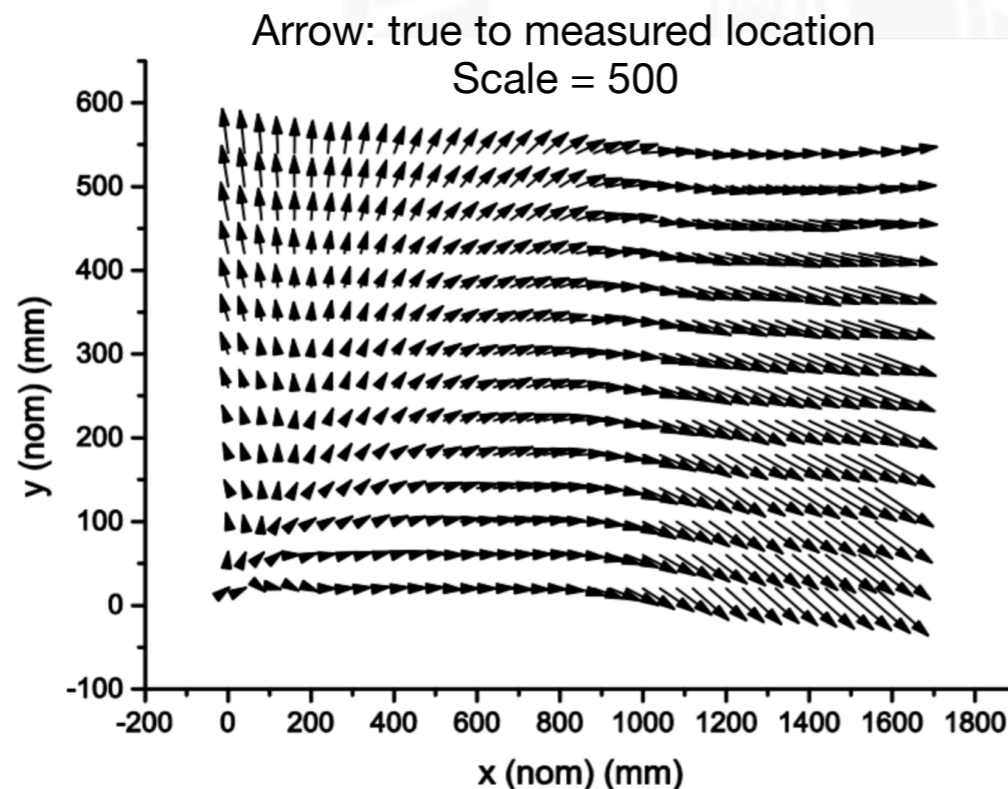


Oxford robot

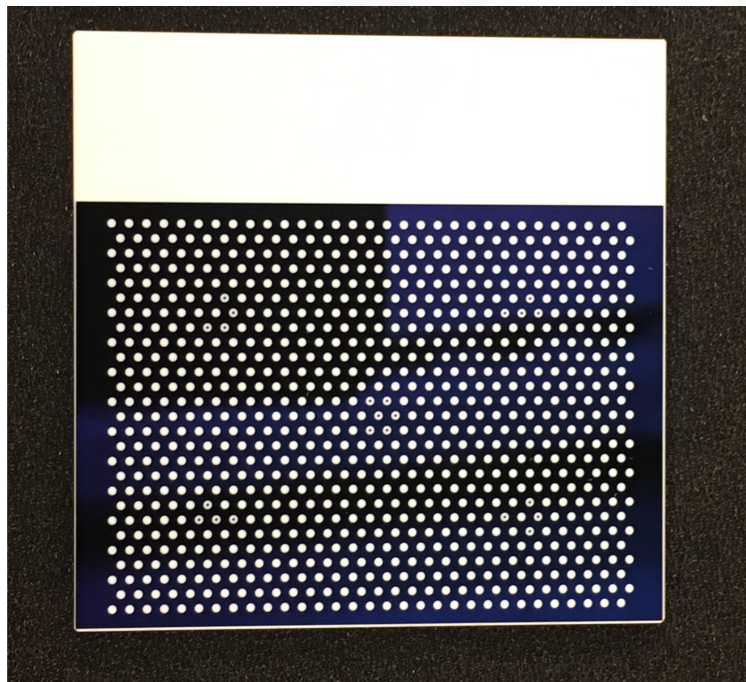
Oxford Tape Testing Robot



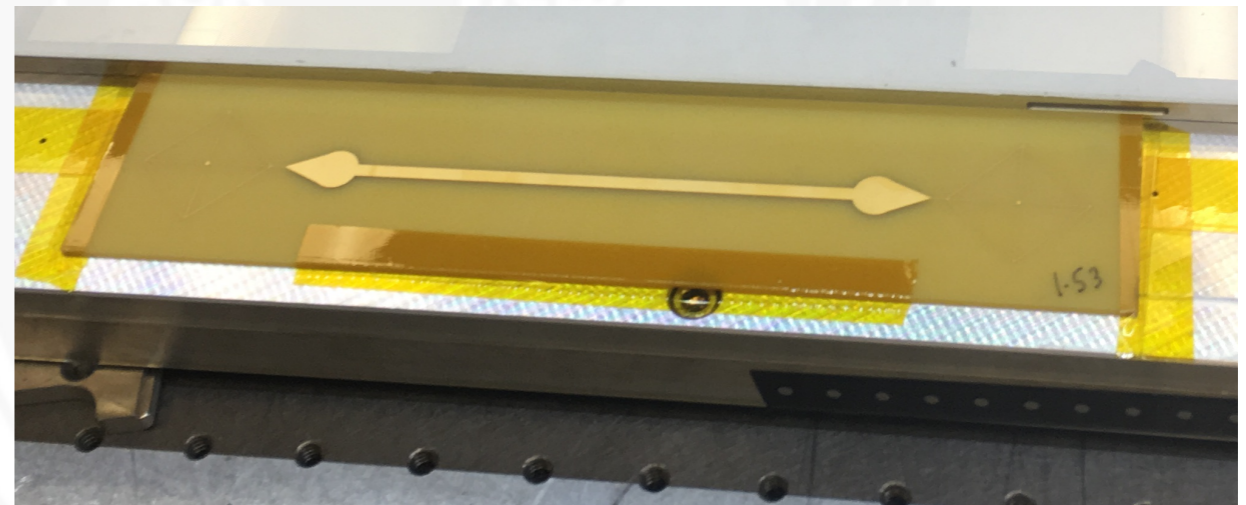
- Need to calibrate stages to achieve sufficient probe positioning precision
- Procedure:
 - ▶ Use a calibration plate with 2cm×2cm grid of black dots in regular pattern covering the full area of the vacuum chuck
 - ▶ Measure positions of circles in nominal stage coordinates using image finding software
 - ▶ Generate look up table of corrections from stage to real coordinates using linear interpolation
- Offsets between robot position and real coordinates up to ~200μm
 - ▶ larger than the size of the bond-pads, so calibration is necessary



- Need to calibrate camera to transform pixels into world coordinates
 - Uses calibration plate and image recognition software
- Need to calculate offset of the probe position from the camera image center
 - Uses a custom PCB with fiducials and copper traces



**Camera calibration
plate**



**Camera to probe
calibration PCB**

- Position of features on tape can vary from nominal location by more than 100 μ m
 - ▶ caused by the flexibility of the Kapton material during the lamination process or expansion during co-curing
 - ▶ Displacements are same size as the bond pads, would make probing impossible
- Local fiducials necessary for positioning the probes
- Probe positioning procedure:
 - ▶ Map nominal pads locations (from CAD file) to stage coordinates
 - ▶ Additional correction using local fiducials to allow for tape distortions
 - ▶ Include camera-probe offset



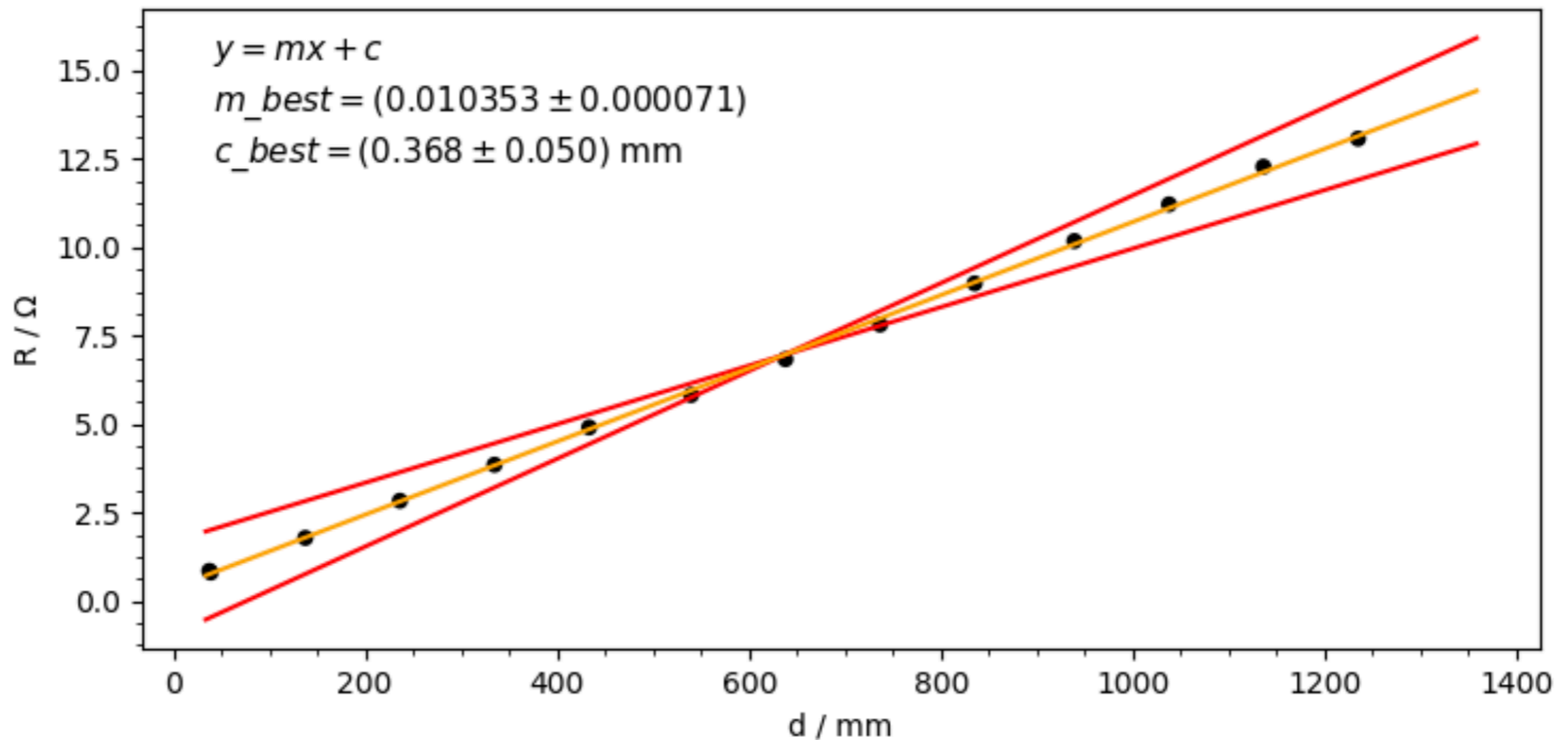
Tape Testing Robot: Probe View



1. Find tape position on table using robot cameras and global tape fiducials
2. Measure local fiducials to determine tape deformations
 - ▶ Location of bond pads with respect to bus tape center should be within 300 μm of the nominal locations
3. Position probe heads on tape
4. Perform electrical tests
 - ▶ Measures resistance between pads on all networks of the tape
 - ▶ Checks for open/short circuits
 - ▶ HV insulation resistance testing $>1\text{G}\Omega$
 - * voltage equal to twice the maximum voltage required in operation
5. Repeat from step 3 for all networks

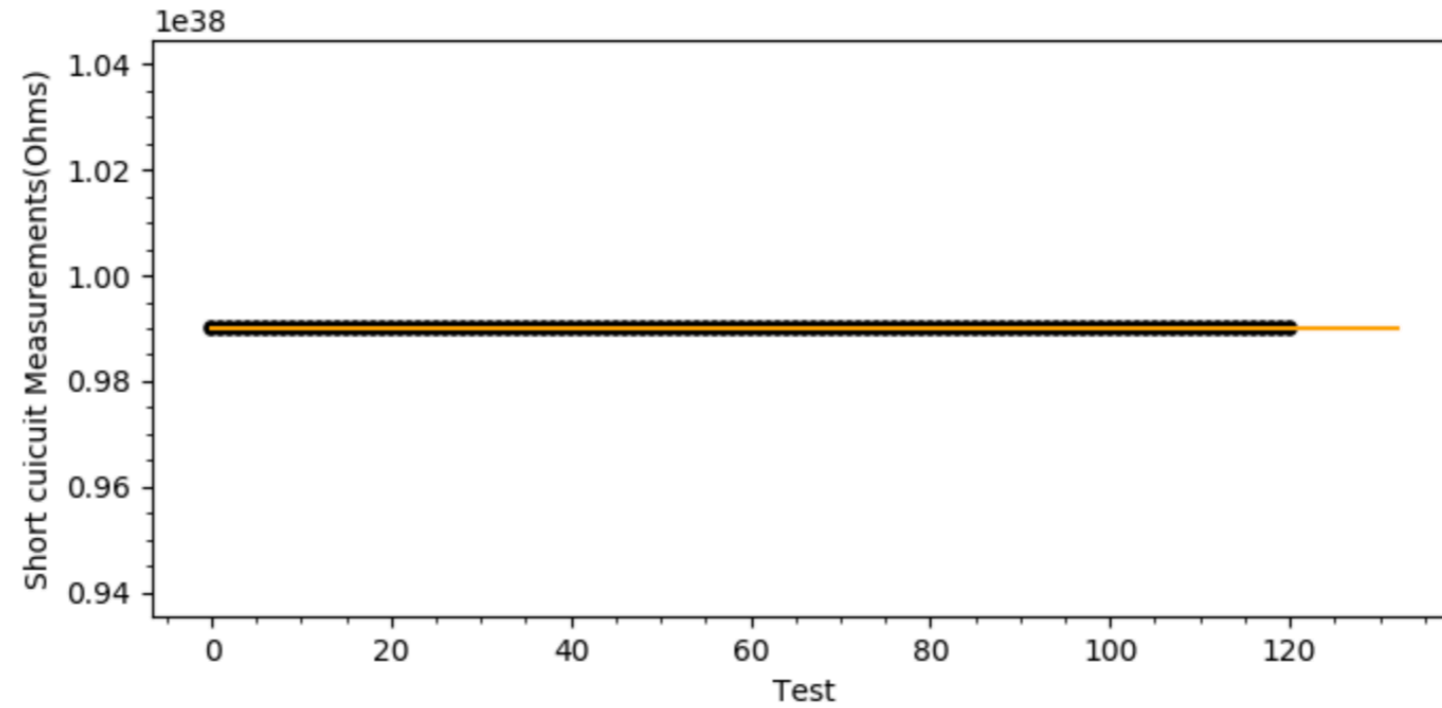
Tests each tape in less than 1 hour

- For each network of LV lines a fit of resistance versus length will be made and an upper limit on the slope will be used as part of the QC
- For the power lines, the QC will simply use an upper limit on the resistance measured

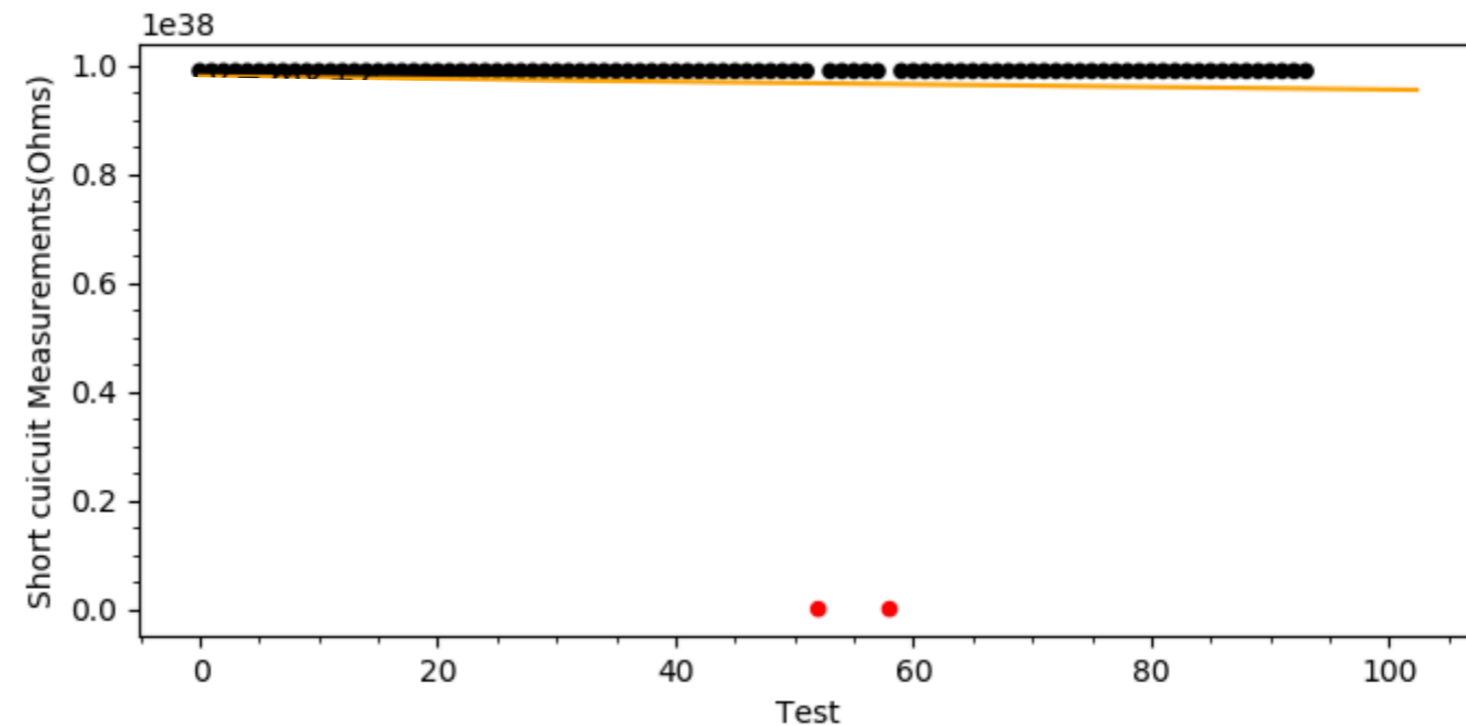


- Poor insulation between neighboring lines can lead to short circuits

No Defect



Defect



- A robotic system has been developed to perform automated QC of all bus tapes for the ATLAS ITk strips detector
- The system has effectively identified several types of bus tape defects
- The techniques developed can be used for QC of general high-density bus tapes for detectors that use similar strategies for data extraction.

- **Oxford:** Roy Wastie, Tony Weidberg
 - ▶ Bus-tape design, robot design, development, and testing, Bus-tape co-curing
- **LBNL:** Carl Haber
 - ▶ Bus-tape co-curing
- **Yale:** Jeff Ashenfelter, Tom Barker, Will Emmet
 - ▶ Stave core assembly, UMass robot electronics
- **The ATLAS Collaboration**