

DCS Controller



DCS Controller Chip Status

- Chips arrived yesterday
- Carrier board already submitted and expected to arrive this week
 - Both Bonn and Dortmund are possible options for wire bonding
- Schematic for Master board to test bridge logic and regulators is almost finished
- Two or three more days for routing then master board can be submitted
- Hopefully master board will also be available by the time chips are wire bonded

Bridge Logic Status

- Basic functionality to read/write and interface already developed => only for SDOs
- Timeout counters and SDO abort codes missing in case of no response from PSPP or some other errors.
- Plan to finish and test complete bridge logic using SDOs before christmas holidays and if possible also on FPGA