

Large Area Monolithic Pixel Detectors implemented in 180 nm HVCMOS Process

Tuesday 26 February 2019 14:00 (20 minutes)

HVCMOS pixel detectors developed for applications in particle physics (experiments Mu3e, ATLAS, CLIC) will be presented. The detectors have been implemented in 180 nm commercial HVCMOS process. High resistivity substrates and quadruple well structure have need used. The sensors have been realized as systems on a chip, they contain active pixel matrix, readout circuits and digital blocks. Design and measurement results will be presented.

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