

STREAM 3rd Annual Meeting and CV Writing & Job Interview Workshop

Thursday 24 January 2019

WP2 (CMOS Technologies Assessment) & WP3 (Smart Sensor Design and Layout) - Auditorium A. Pictet A100 (at Sciences II building) (09:50 - 12:30)

-Conveners: Norbert Wermes; Marlon B. Barbero; Alexandre Rozanov

time	[id] title	presenter
09:50	[21] Test results of irradiated monolithic CMOS pixel circuits in LFoundry 150 nm technology for the ATLAS Inner Tracker Upgrade	Mr CHEN, Zongde
10:10	[22] Main Developments at CPPM	BHAT, Siddharth
10:30	Coffee Break	
10:50	[23] Sidewall Spacer NVM Characterization and Modeling	VINCENZI, Tommaso
11:10	[24] Image Sensor for Electron Microscopy	MATEOS, Horacio
11:30	[25] Pixel Sensor Design Implementation and Test Results for the ATLAS ITk upgrade -- TJ-Monopix & CDR53B	MOUSTAKAS, Konstantinos
11:50	[26] Depleted monolithic active pixel sensors with a column-drain read-out architecture for the ATLAS ITk upgrade	CAICEDO SIERRA, Ivan Dario
12:10	[27] Progress in asynchronous pixel designs in TOWERJAZZ 180 nm	BERDALOVIC, Ivan