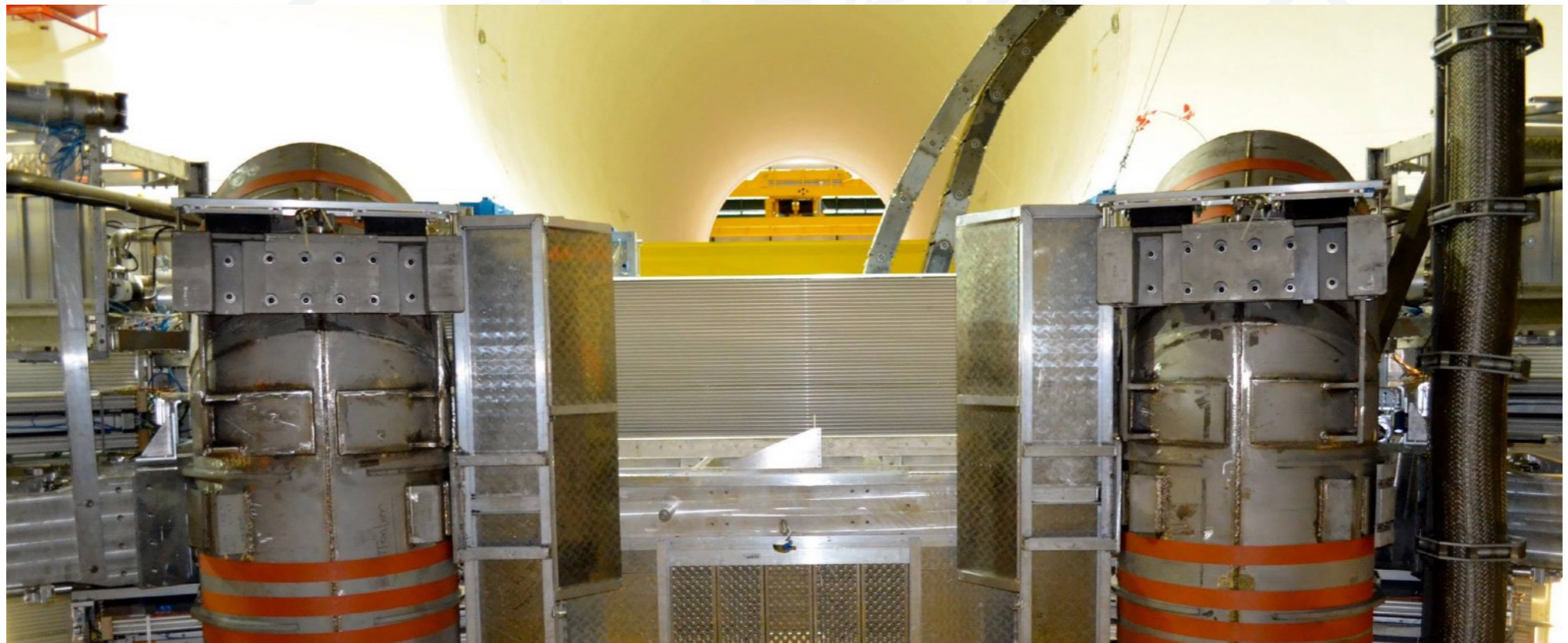


# Efficient tracking in the search for rare processes

13th December 2018

Rebecca Carney

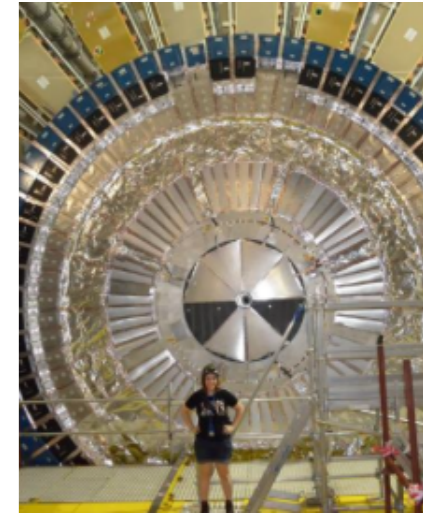
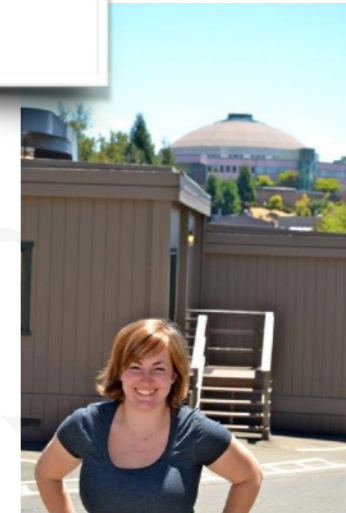




Student at Stockholm university



Graduate RA at LBNL



## PhD in Instrumentation Physics

- Four year PhD programme with Instrumentation Physics dpt at SU.
- Student of SU but based at Berkeley Lab.
- Unique opportunity: work in a US National Lab and near industry, whilst on a European PhD.



**Sam Silverstein**

**Lead advisor**

Stockholm University



**Sara Strandberg**

**Advisor**

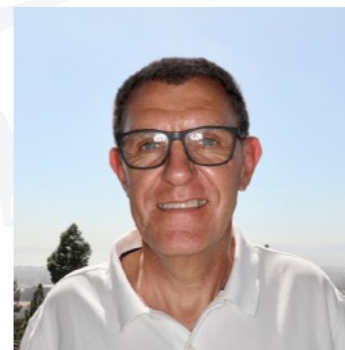
Stockholm University



**Maurice Garcia-Sciveres**

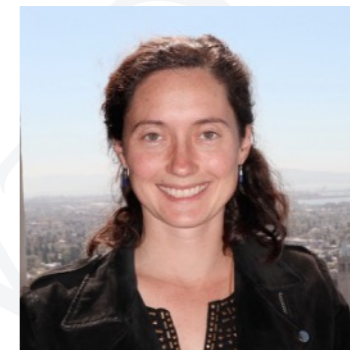
**Advisor,**

**technical supervisor**

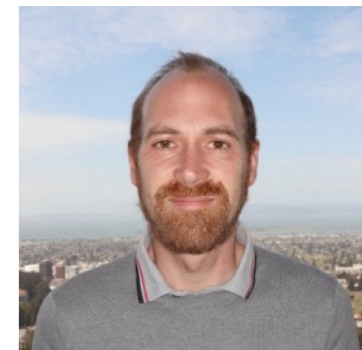


**Paolo Calafiura**

**Technical supervisors**



**Laura Jeanty**



**Simone Pagan Griso**





- First two years of degree concluded by a [Swedish Licentiate thesis](#) and defense with external committee and opponent.
- My program's focus is [instrumentation](#); data analysis work is not a required part of the degree.
- However, I [requested adding a data analysis component](#) to my thesis.

## Instrumentation for silicon tracking at the HL-LHC

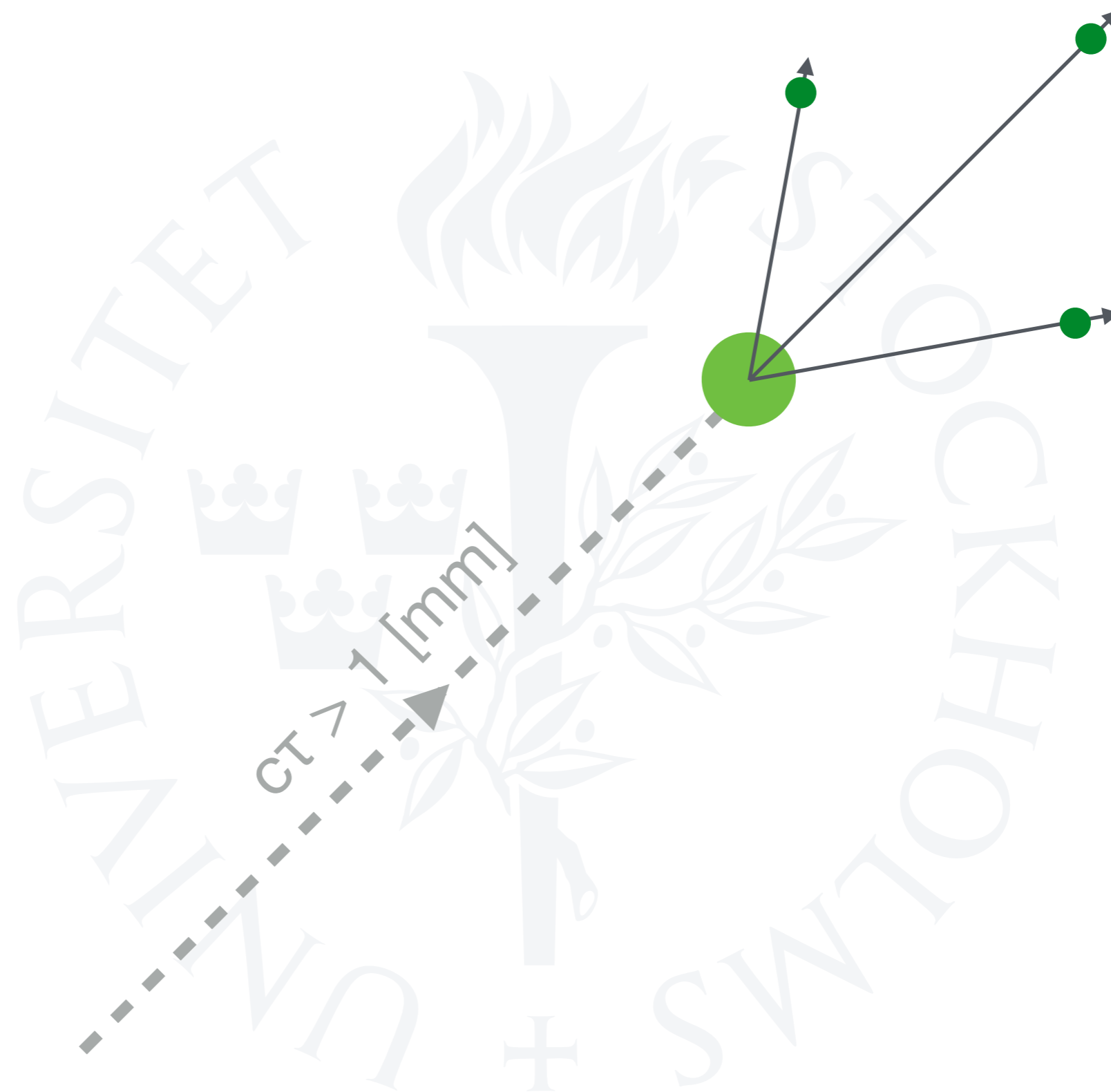
*Licentiate thesis*

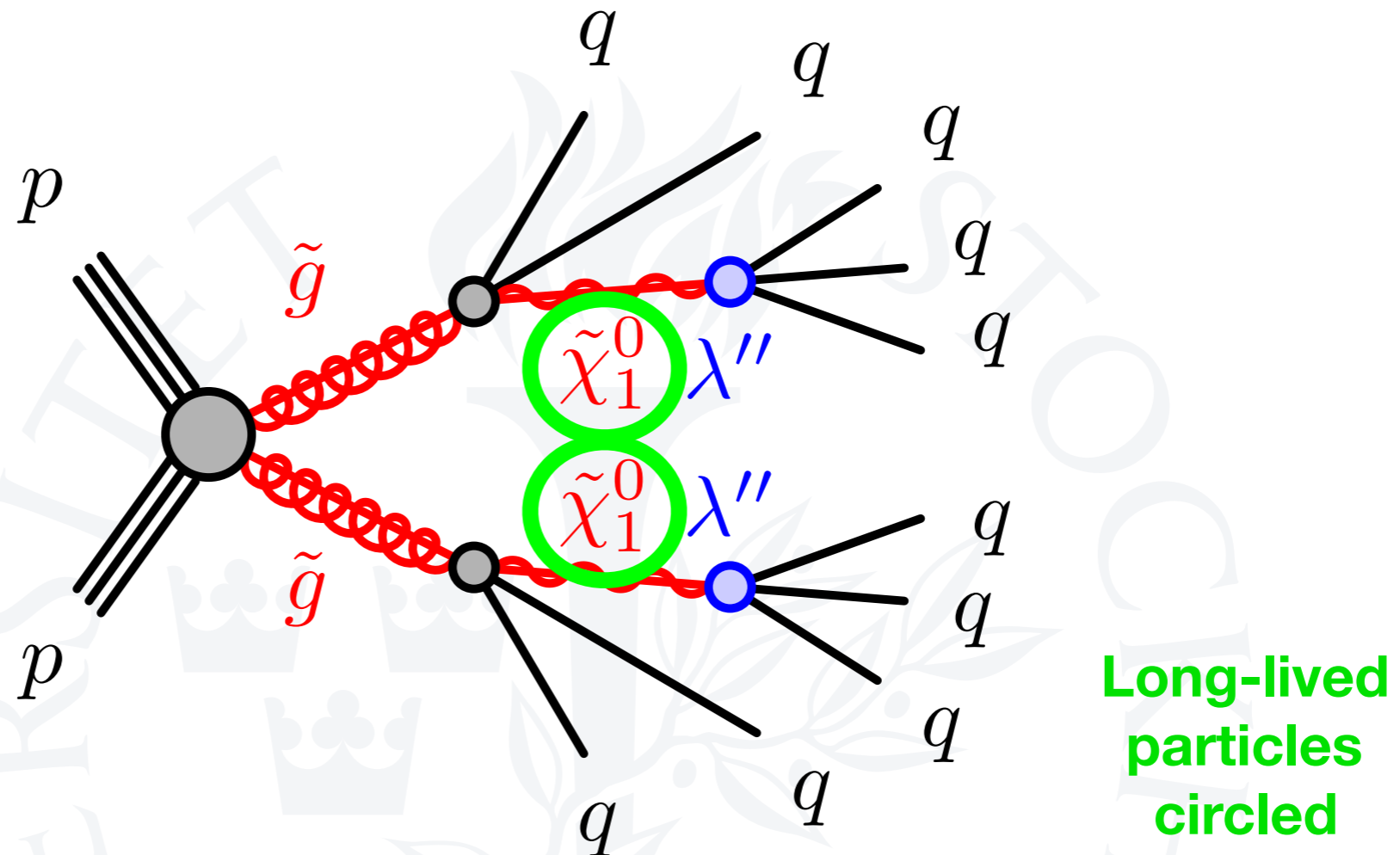
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- (i) Search for physics beyond the Standard Model with displaced vertices decaying in the Inner Detector to, and produced in association with, high-energy jets using 13 TeV ATLAS Run 2 data
- (ii) Implementation of a discrete spiking Kalman filter on IBM's TrueNorth neuromorphic chip

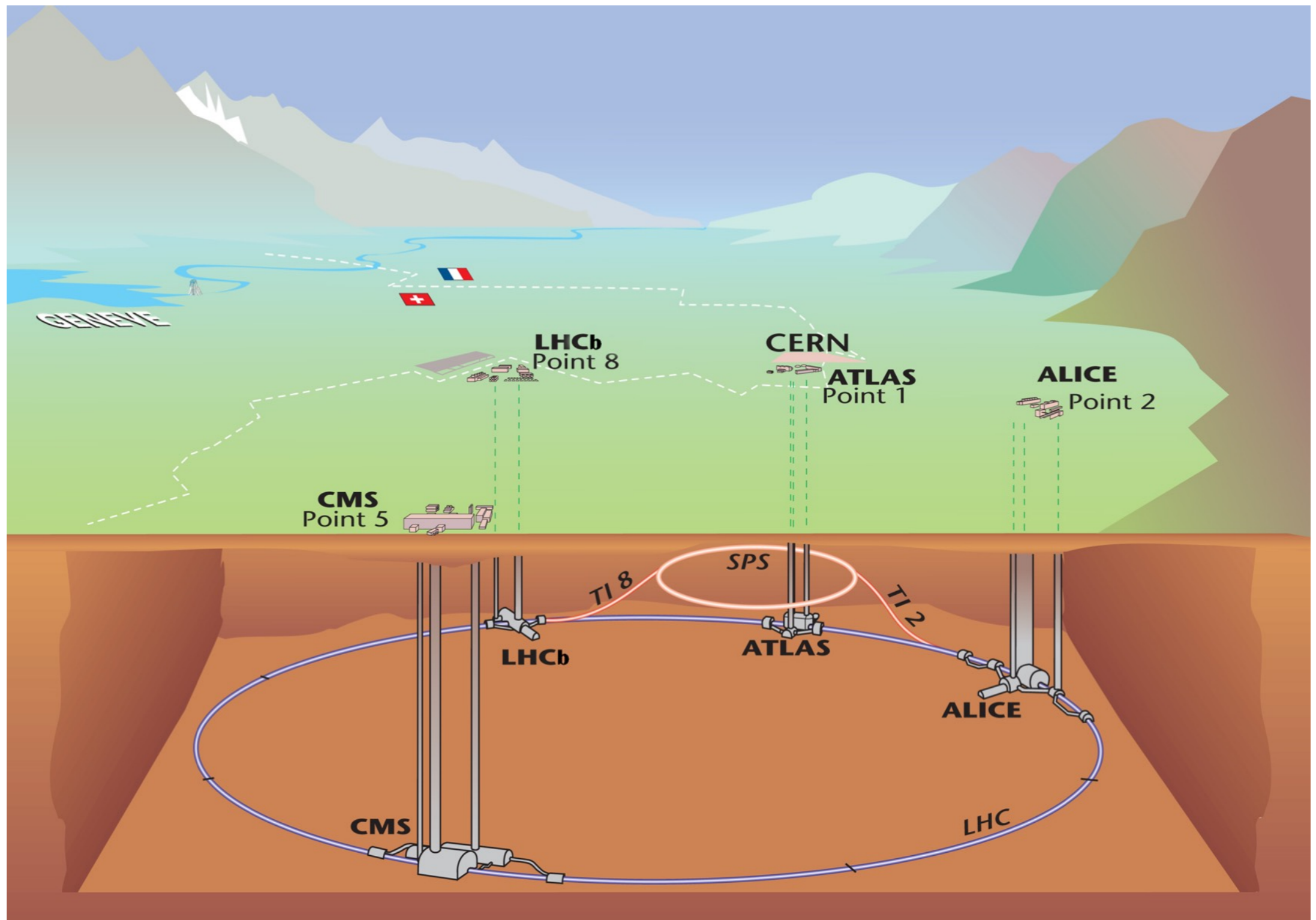




- Small coupling (e.g. B-hadron decay)
- Nearly degenerate masses (e.g. free neutron decay)
- Heavy mediator (e.g. charged pion decay)

My analysis: targeting this signal model, long-lived neutralino decaying via RPV coupling to, and produced in association with, high-energy jets.

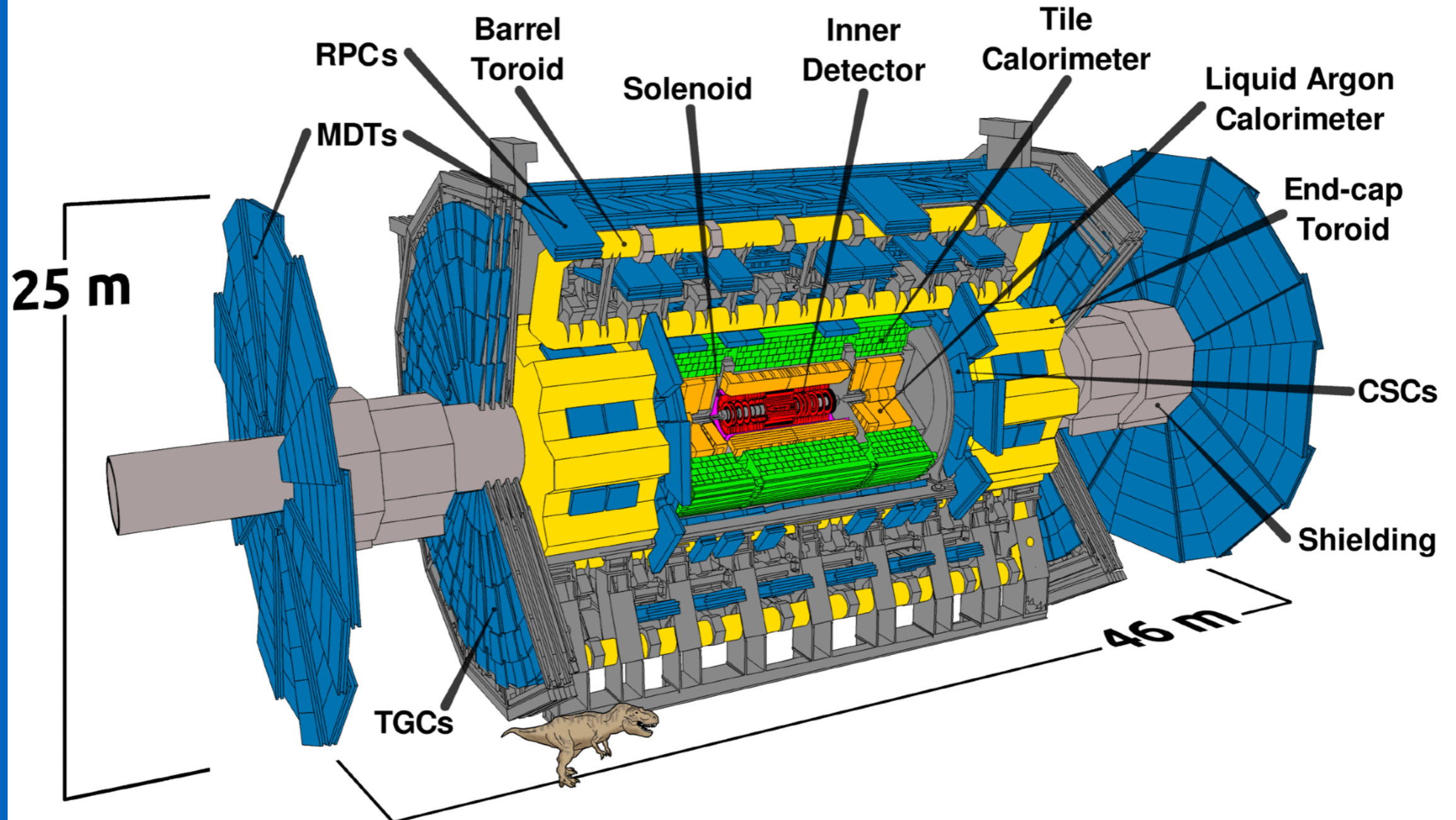
How do we search for this process?



<https://cds.cern.ch/journal/CERNBulletin/2008/38/News%20Articles/1125888>

The Large Hadron Collider accelerates protons which collide in detectors shown above with a centre-of-mass energy of 13 TeV.



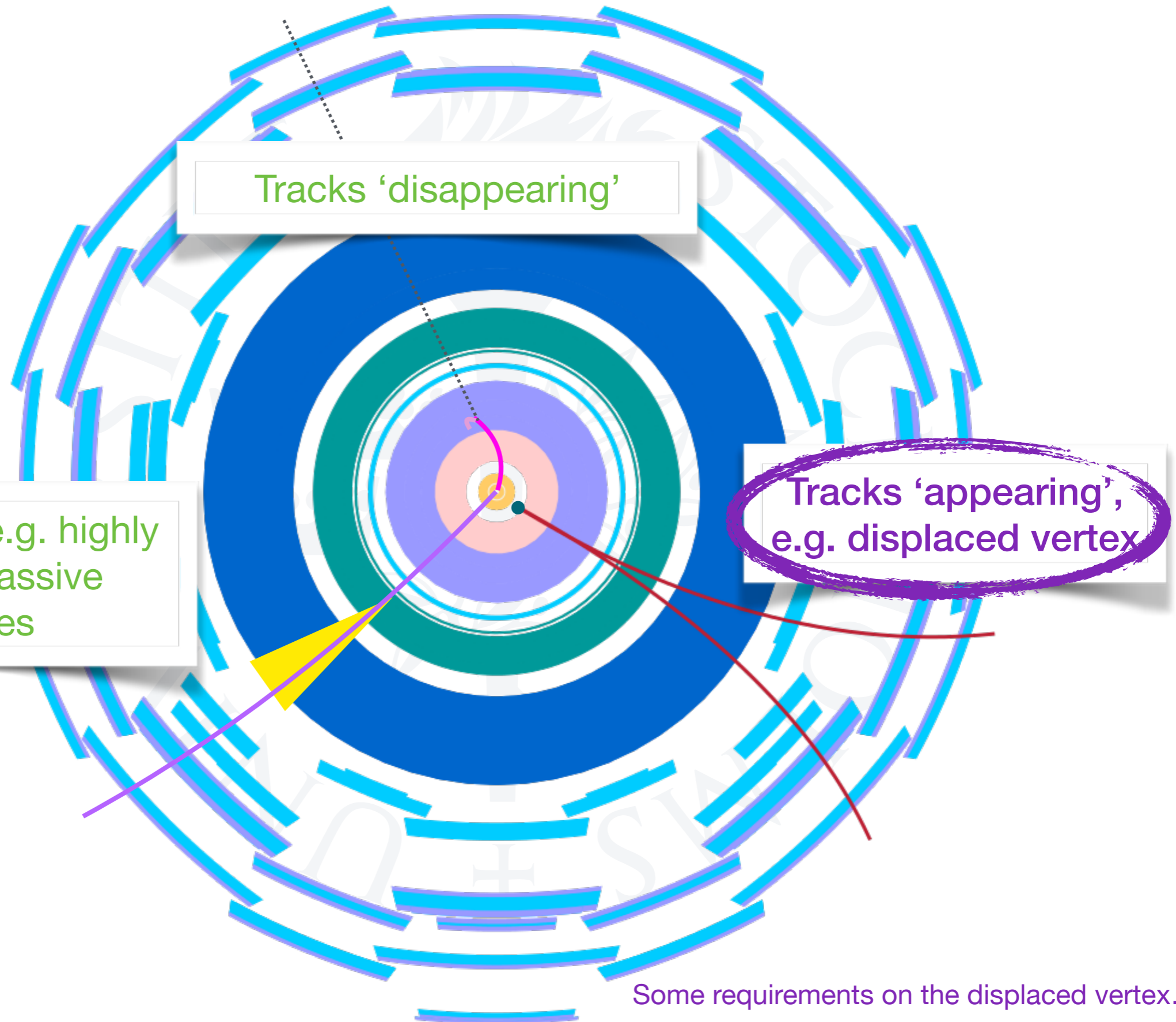


- Inner Detector
- LAr Calorimeters
- Muon detector
- Solenoid
- Tile Calorimeter
- Toroid

What do long-lived particles look like in the detector?







Tracks 'disappearing'

Tracks 'appearing',  
e.g. displaced vertex

Large  $dE/dx$ , e.g. highly ionizing massive particles

Some requirements on the displaced vertex...

<https://doi.org/10.1103/PhysRevD.92.072004>

**DV < 300 mm (first SCT barrel layer)**  
(track and vertexing efficiency)

**DV<sub>r</sub> > 4 mm**

(to avoid getting  
PV/ reduce HF bkg)

**nTracks > 4**

(to avoid fakes)

**DV mass > 10 GeV**

( reduce resonant and **hadronic** backgrounds )

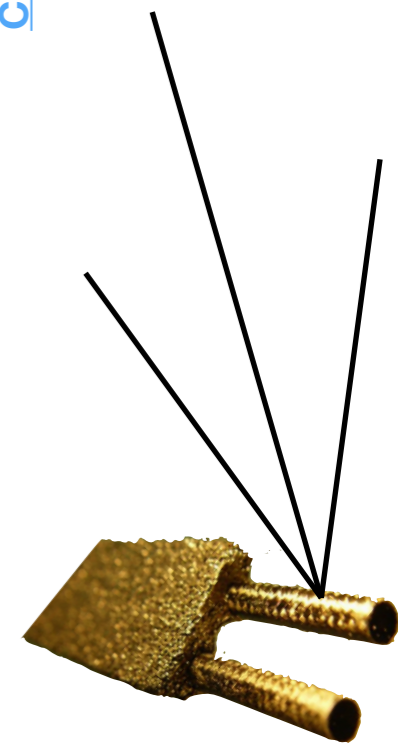
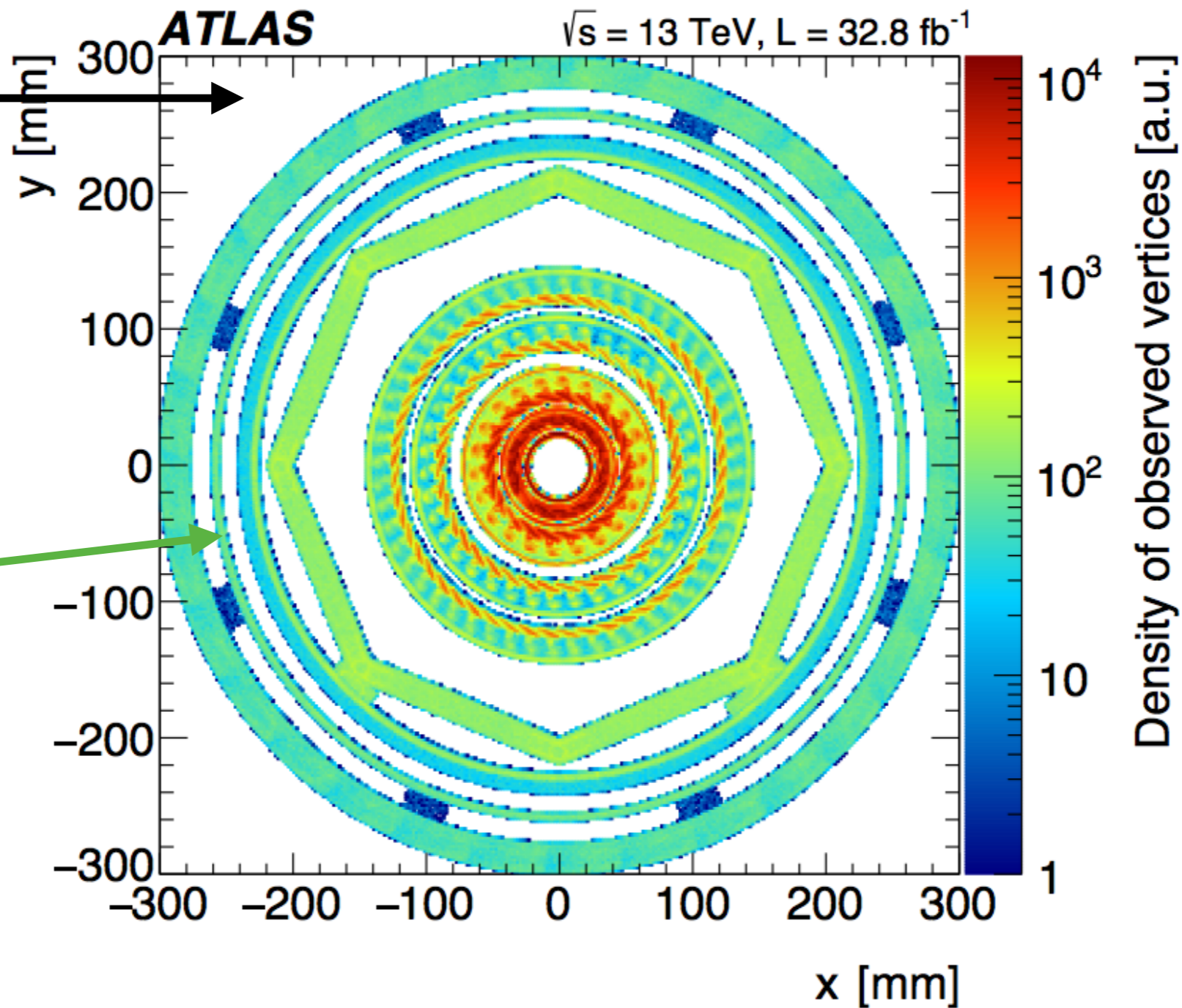


Fiducial region

Vetoed region

Hadronic interactions with material is a significant background for DV's.

Veto map excludes around 42% of fiducial volume!

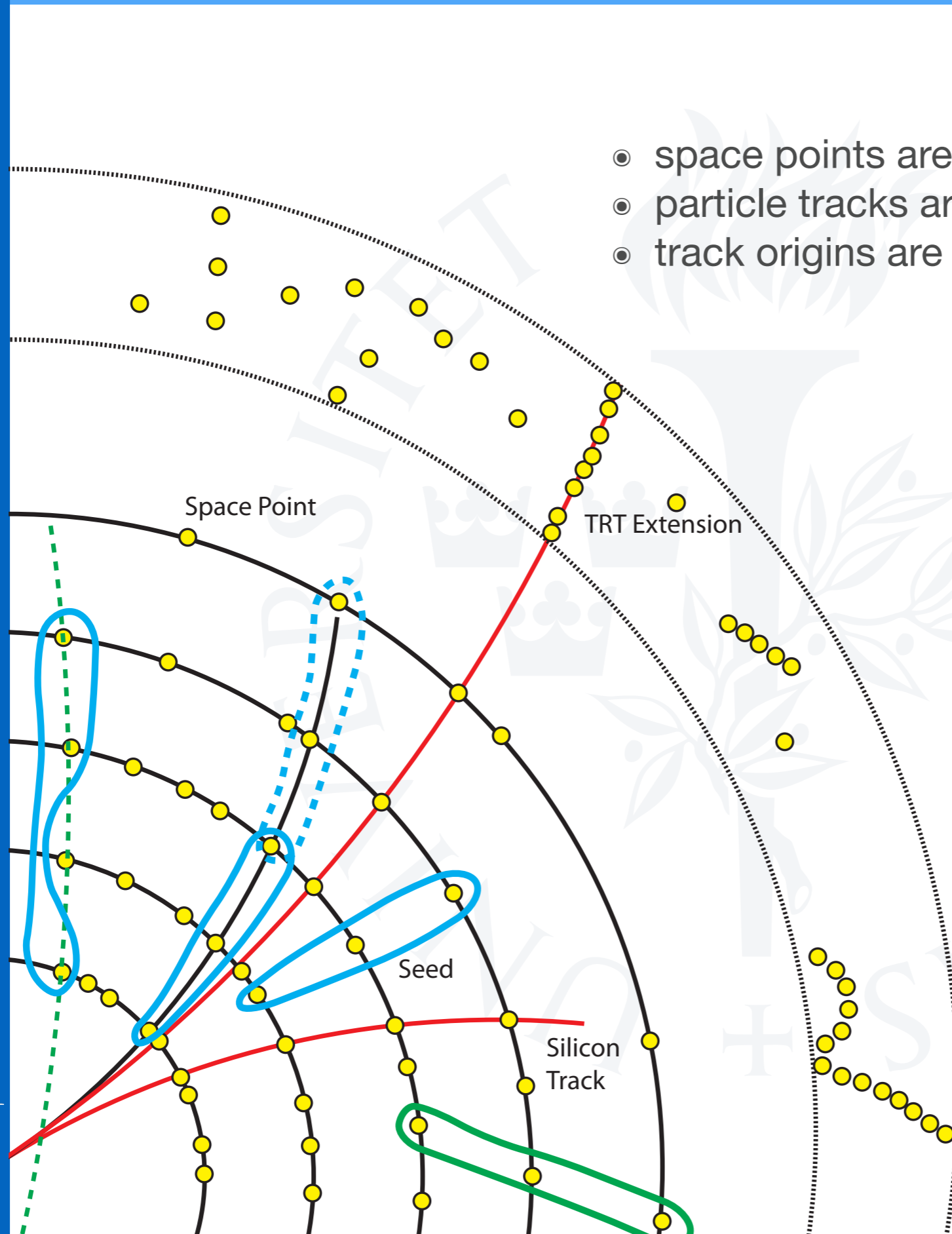


Let's go back to how we construct vertices in the first place....

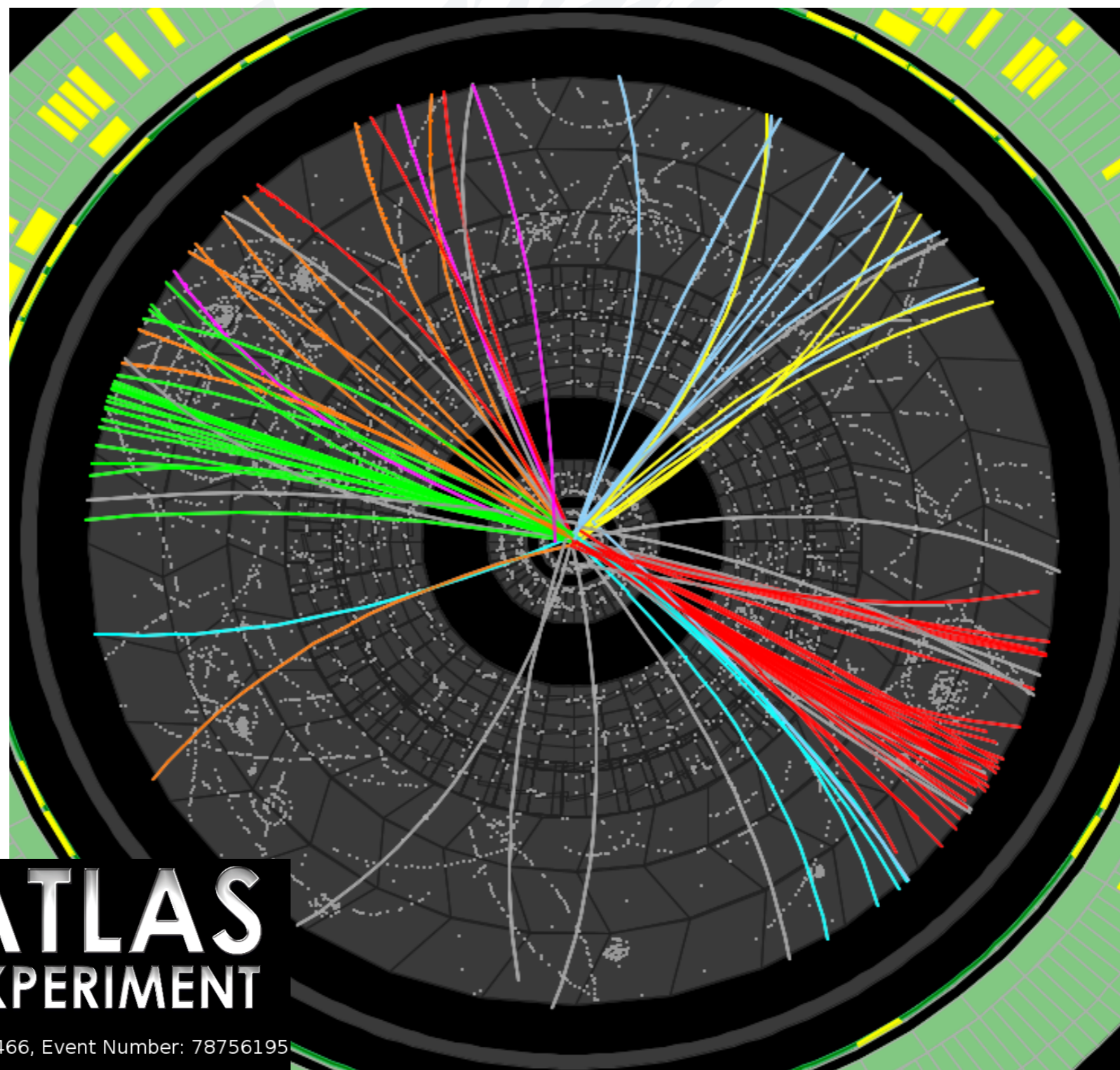




- space points are created from charge clusters
- particle tracks are created from space-points
- track origins are clustered into a 'vertex'



With so many tracks, must reduce combinatorics by imposing restrictions



Run Number: 166466, Event Number: 78756195

Date: 2010-10-08 08:05:57 CEST

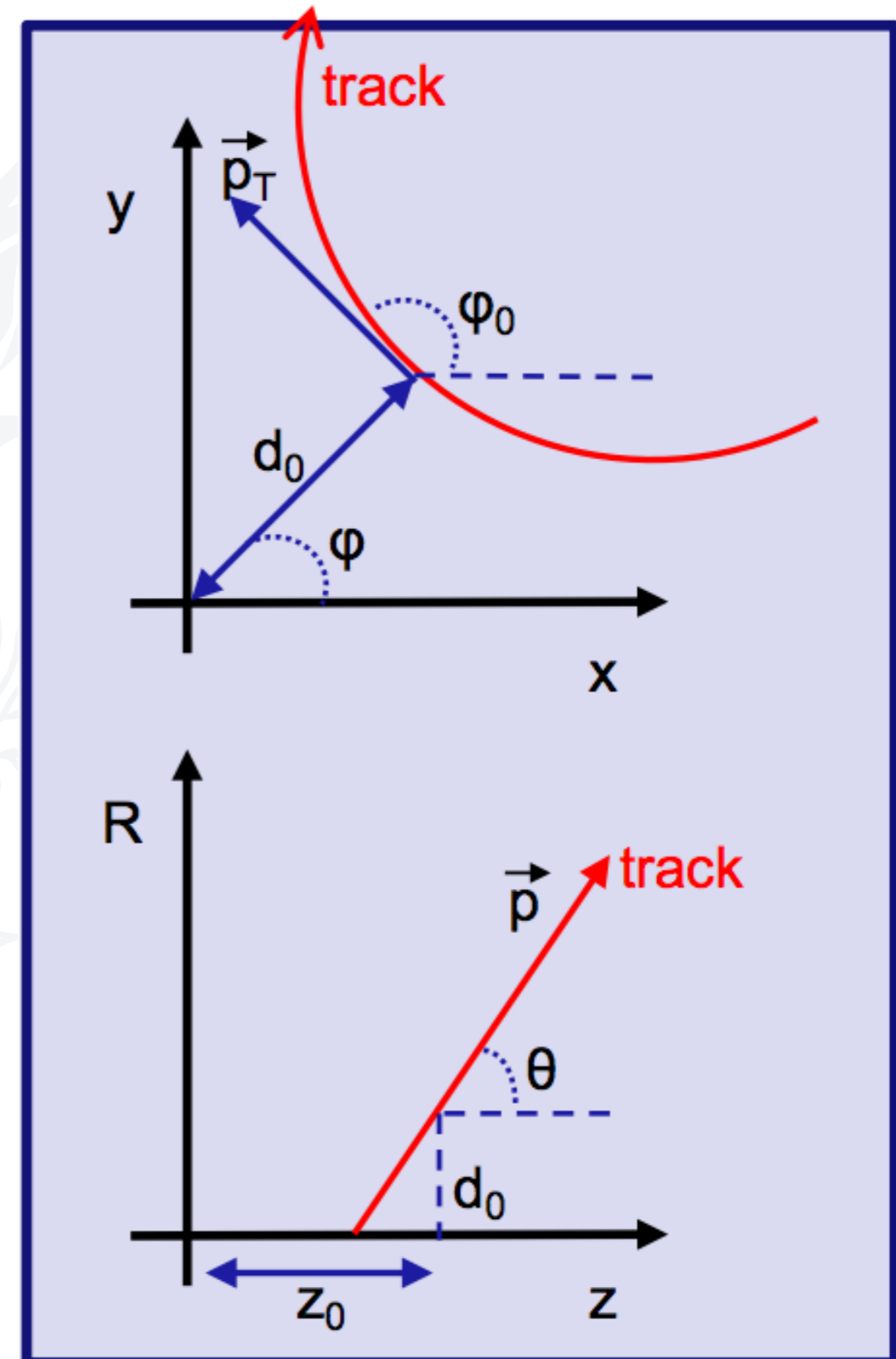
[https://twiki.cern.ch/twiki/pub/AtlasPublic/EventDisplayStandAlone/JiveXML\\_166466\\_78756195-YX-RZ-LegoPlot-2010-11-16-13-45-36.png](https://twiki.cern.ch/twiki/pub/AtlasPublic/EventDisplayStandAlone/JiveXML_166466_78756195-YX-RZ-LegoPlot-2010-11-16-13-45-36.png)





A track can be characterized by a set of parameters...

- We expect most particles to originate from centre of detector
- small distance of closest approach ( $d_0$ ,  $z_0$ )
- restricting this value helps reduce combinatorics



Perigee parameters



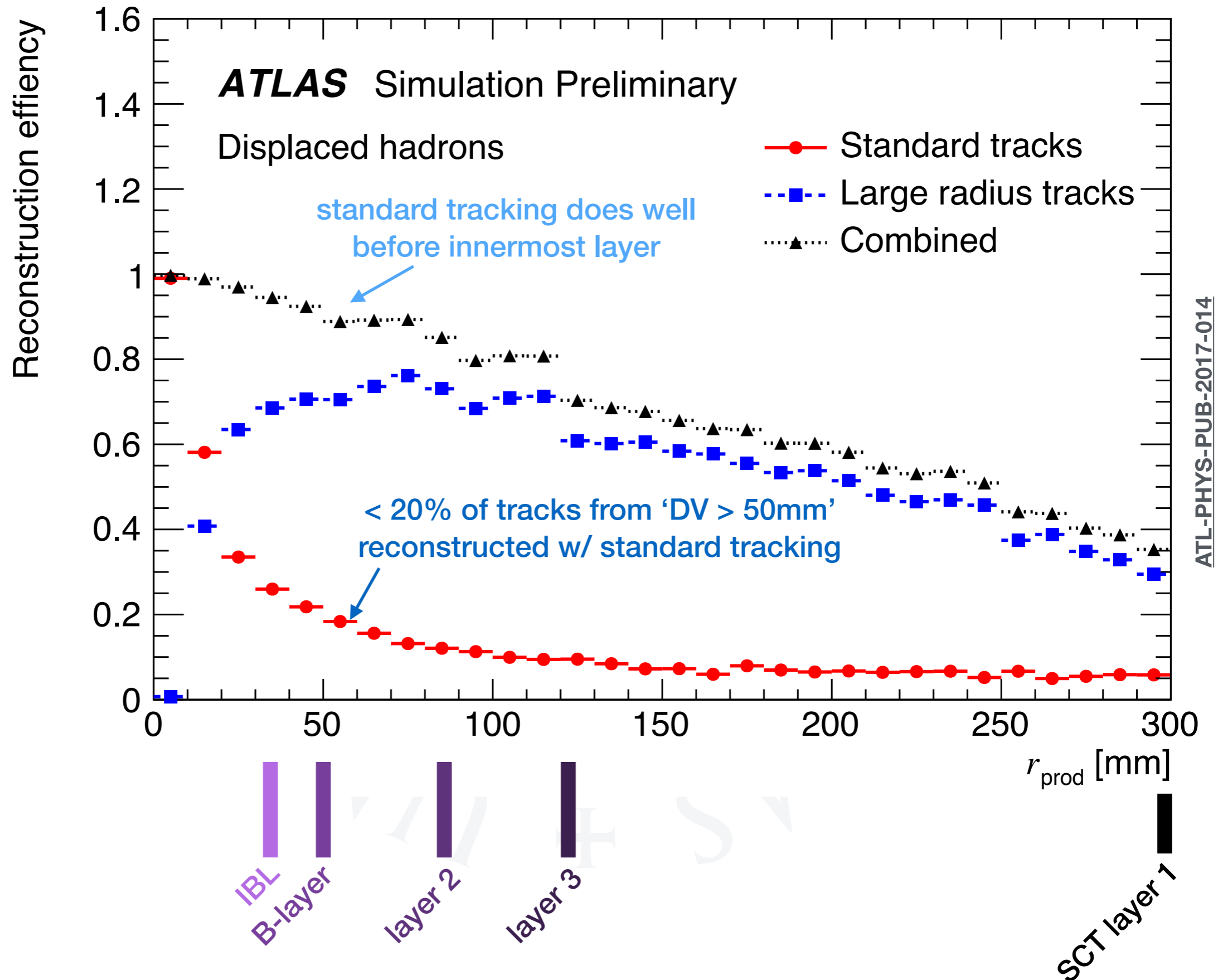
- Standard reco: **reco tracks pointing back to IP**

small  $d_0$

- Special reco (**Large Radius Tracking**): relaxes  $d_0$ .

large  $d_0$

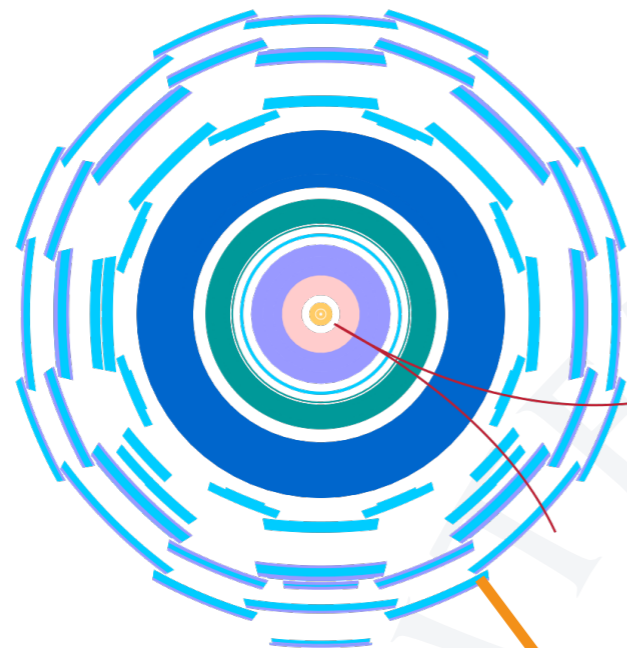
- LRT builds **new tracks** with hits not part of standard tracks.
- **Both** standard and LRT tracks used to build DV.



ATL-PHYS-PUB-2017-014







Must access RAW data to run Large Radius Tracking (LRT) and Displaced Vertex (DV) reconstruction algorithms.  
Expensive & time consuming...

physics\_main  
raw data

standard  
reconstructed  
data

analysis

**filter**

~1% of RAW  
data

**LRT, DV**

special  
reconstructed  
data

analysis

... so filter out interesting events before reconstruction.

## DV\_MultiJetFinalTracklessFilter =

next slide  $\longrightarrow$  { tracklessJets & [(2j & 3j) ^ 4j ^ 5j ^ 6j ^ 7j] & DVMultiJetTriggerFlags }

# jets	Lowest unprescaled triggers in Run2 [GeV]
3	175
4	85
5	55
6	45
7	45

# jets	Jet pT cut [GeV]
2	220
3	120
4	100
5	75
6	50
7	45



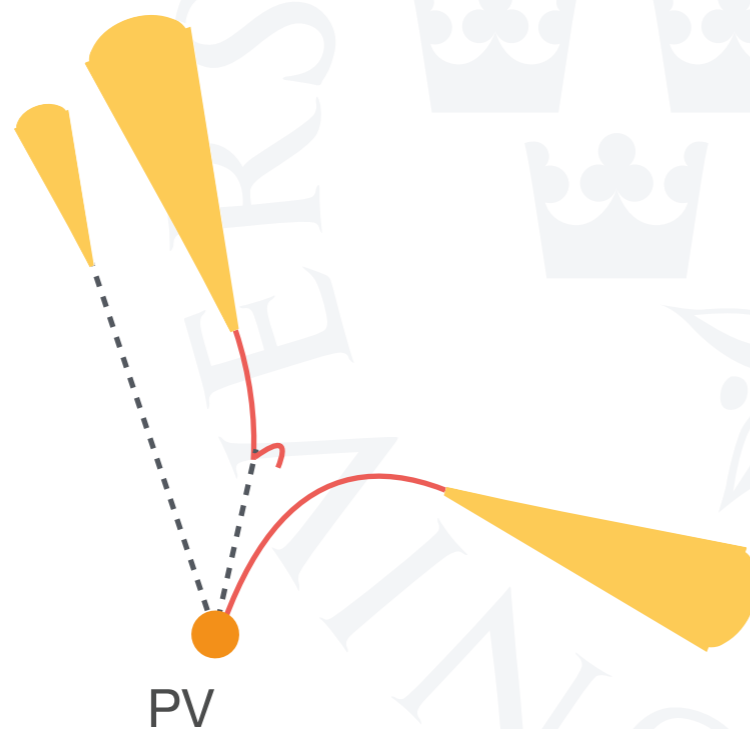
```
DV_MultiJetFinalTracklessFilter =  
  { tracklessJets &  
    [(2j & 3j) ^ 4j ^ 5j ^ 6j ^ 7j] &  
    DVMultiJetTriggerFlags }
```



PV

jet is 'trackless' if  $\Sigma p_T^{\text{track}} < 5 \text{ GeV}$   
tracks matched to PV & jet

```
DV_MultiJetFinalTracklessFilter =
  { tracklessJets &
    [(2j & 3j) ^ 4j ^ 5j ^ 6j ^ 7j] &
    DVMultiJetTriggerFlags }
```

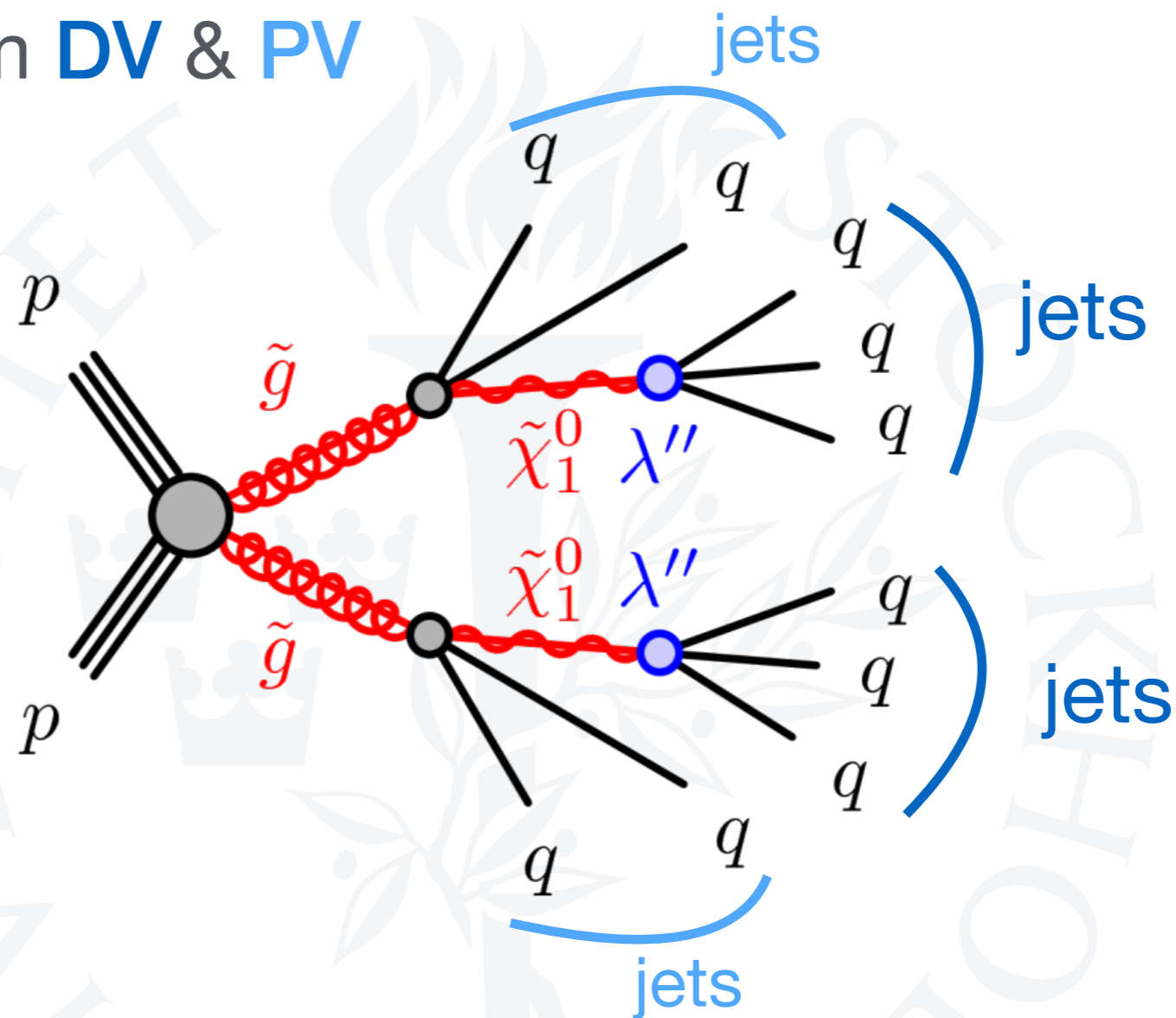


# jets	Jet pT cut [GeV]	Eta cut
single	70	2.5
double	25	2.5

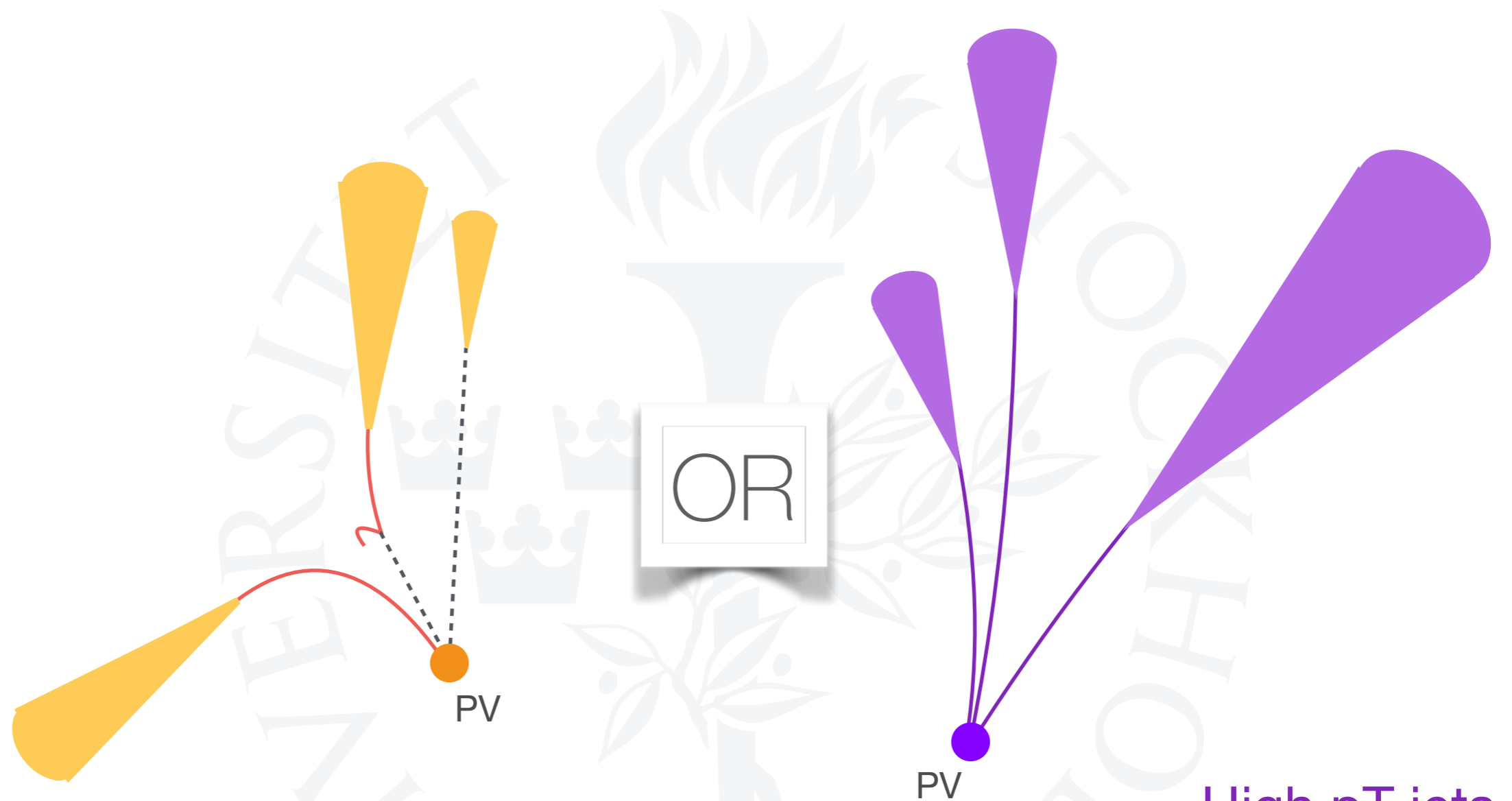
**tracklessJets** = single **OR** double

To pass the filter, a triggered events has to have at least:  
1 or 2 trackless jets (single or double)

## Jets from DV &amp; PV



- During the analysis, the filters were studied for inefficiencies.
- If neutralino lifetime is *short* ( $<100$  ps), jets from neutralino decay are largely inside first pixel layer, so jets are very likely to have tracks associated with them: trackless jet **requirement** is inefficient.



**Trackless jets**  
Single or double

**High  $p_T$  jets**  
No trackless jet requirement



Pre-existing filter had this logic:

# jets	Jet pT cut [GeV]
2	220
3	120
4	100
5	75
6	50
7	45

DV\_MultiJetFinalTracklessFilter =

{ tracklessJets &  
 [(2j & 3j) ^ 4j ^ 5j ^ 6j ^ 7j] &  
 DVMultiJetTriggerFlags }

2015 + 2016 data

Updated filter for DV+Jets search:

# jets	Jet <i>High</i> pT cut [GeV]
2	500
3	180
4	220
5	170
6	100
7	75

{ DV\_MultiJetFinalTracklessFilter ^  
 [(2j<sub>h</sub> & 3j<sub>h</sub>) ^ 4j<sub>h</sub> ^ 5j<sub>h</sub> ^ 6j<sub>h</sub> ^ 7j<sub>h</sub>] }

2017 + 2018 data

these cuts increase the rate by no more than 1 Hz

So how much of our signal can we recover with this addition?

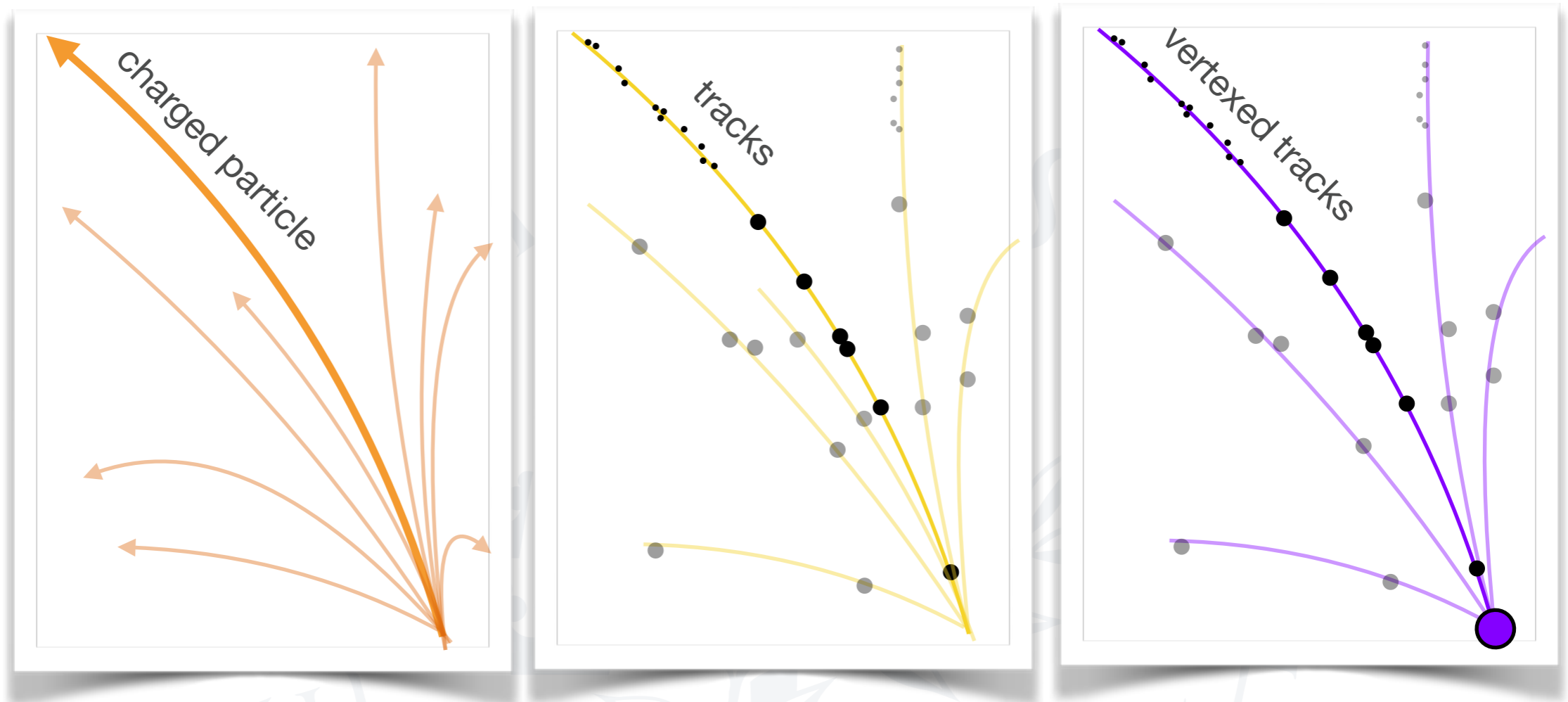


Fundamentally, these filters seek to minimize unnecessary computations

How has adding the high  $p_T$  filters changed the event acceptance rate?







- Aside from studying the efficiency of the DRAW filter, part of my work on this analysis has included studying the efficiency of the DV reconstruction, analysis software framework & sim studies.
- Searching for unique signatures from rare processes requires special reconstruction: additional processing.

Fundamentally, these filters seek to minimize unnecessary computations

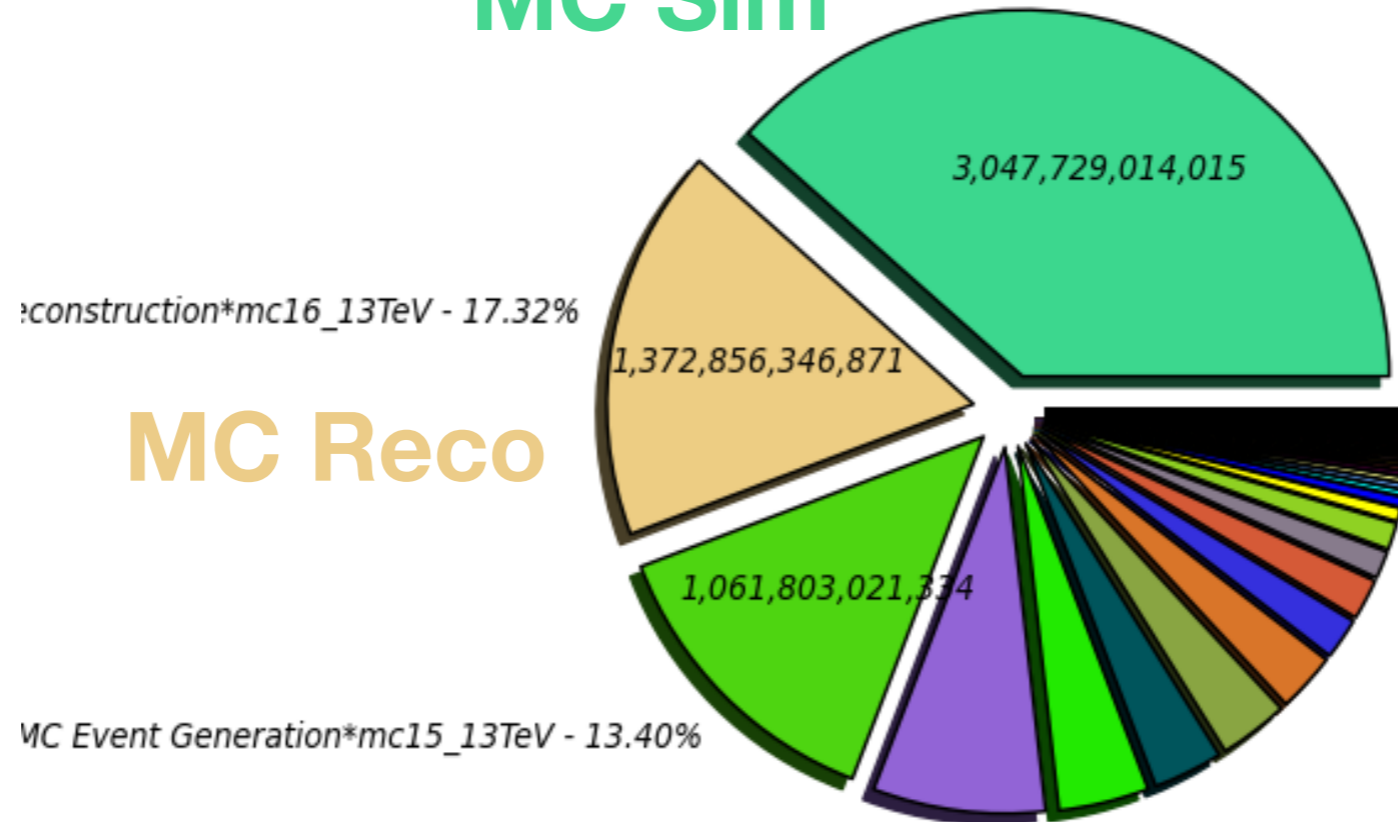
As we prepare for HL-LHC, measures like these will become paramount



CPU consumption Good Jobs in seconds (Sum: 7,925,655,375,001)

## MC Sim

MC Simulation Full\*mc16\_13TeV - 38.45%



## MC Reco

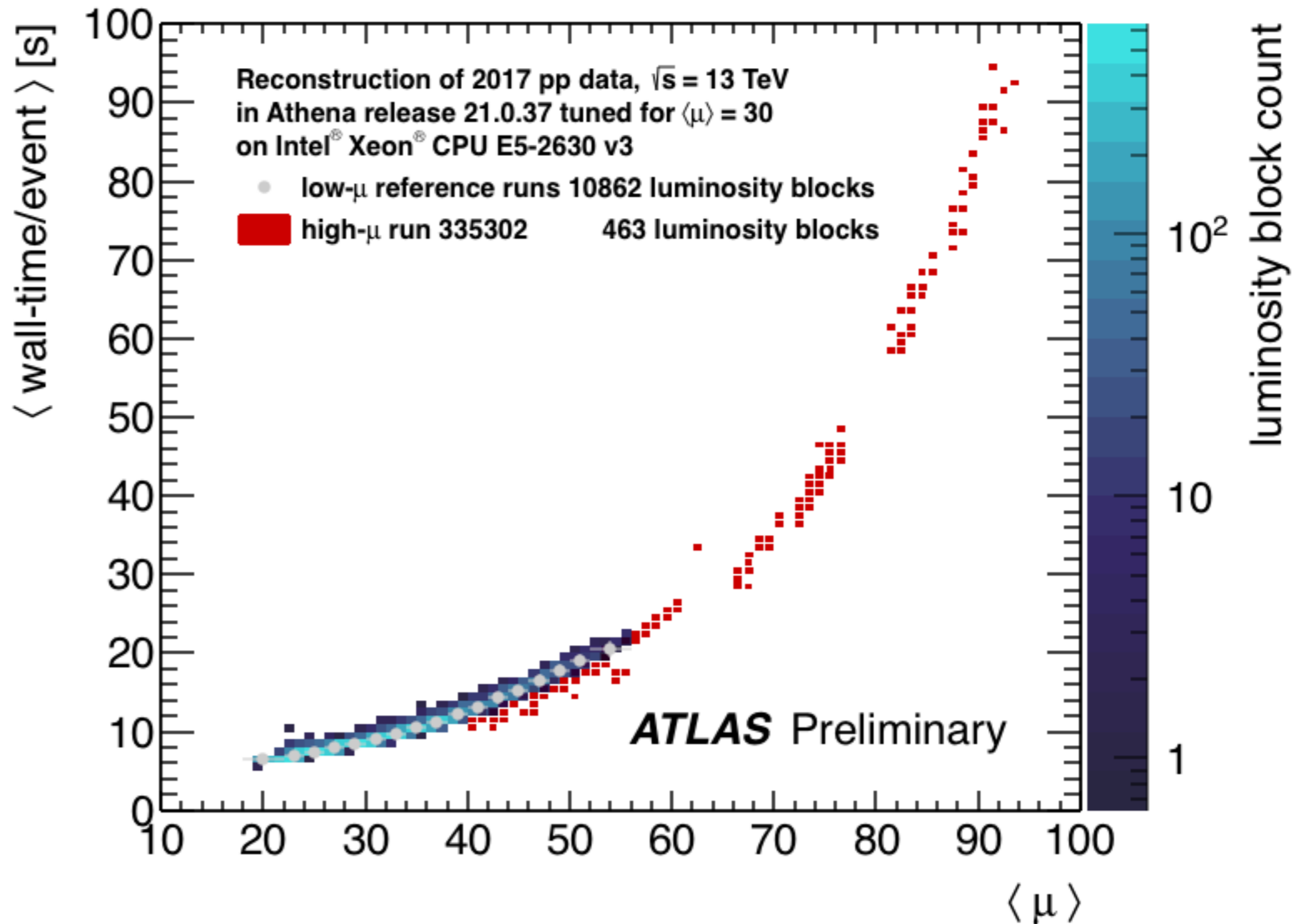
MC Event Generation\*mc15\_13TeV - 13.40%

## MC EvGen

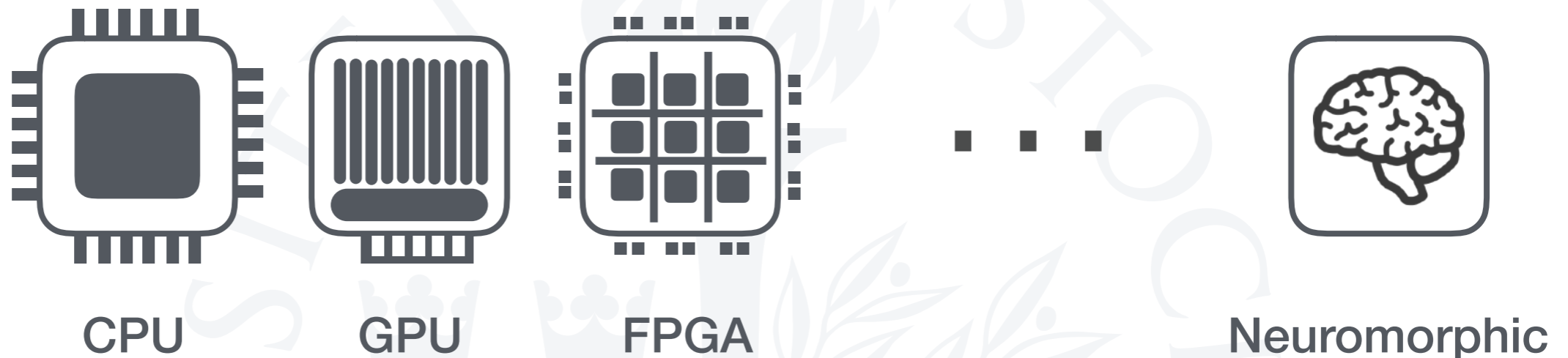
<http://tinyurl.com/ycz6nkee>

Currently, largest CPU consumption from Sim: G4 calorimetry - massive progress in last couple of years to reduce this! However, in HL-LHC a different process will take over...



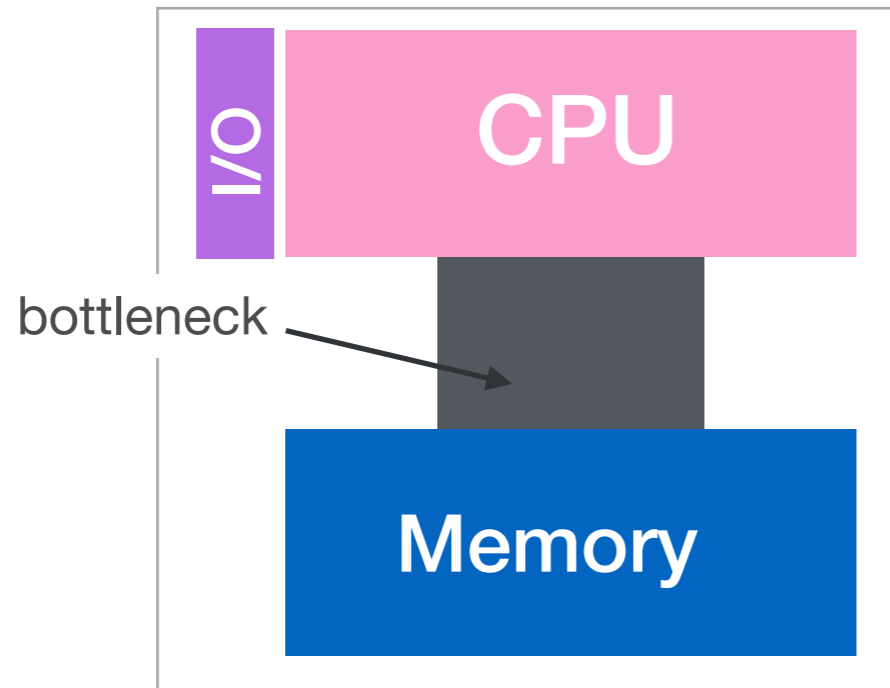


- During HL-LHC: **dominated by reconstruction,**
- Our computing requirements will outstrip our resources by a factor of 3
- A different approach is needed: supplement the grid with HPC and parallelize.



- HPC is trending towards parallelism.
- Hardware efforts include exploring FPGA's, GPU's, multi-core processors, and cutting edge architecture

What about neuromorphic computing?

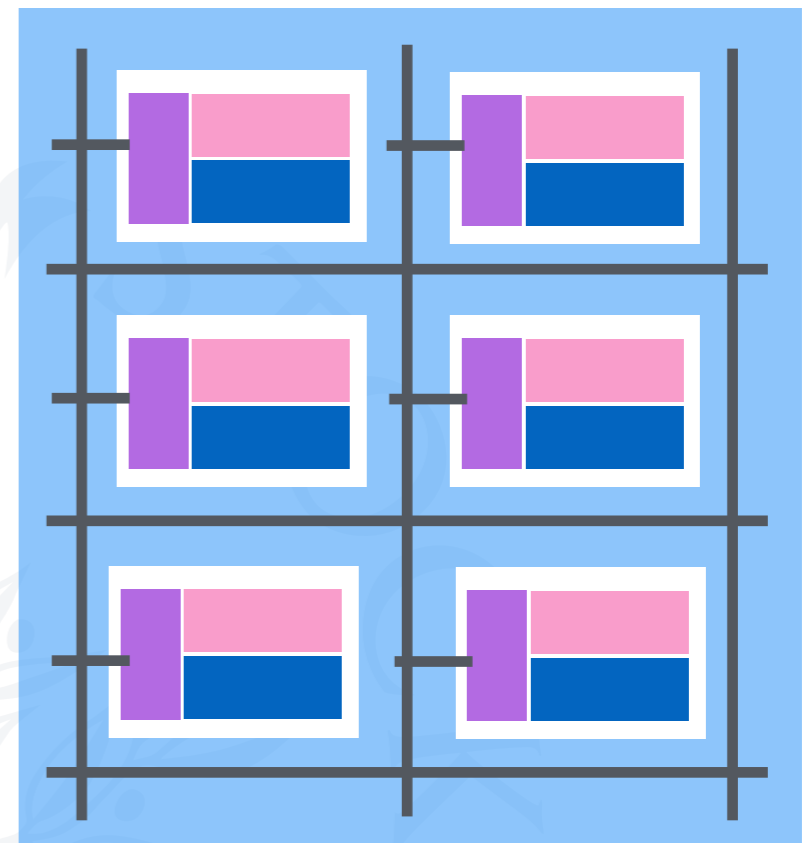


Traditional von Neumann architecture

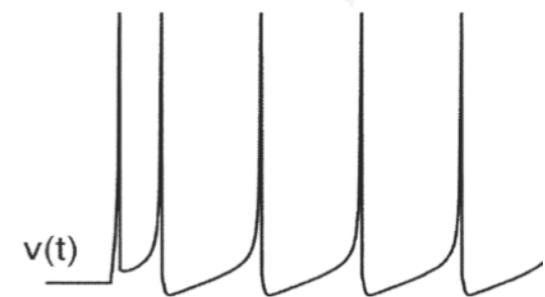
01000011

Binary n-bit words

Instruction set

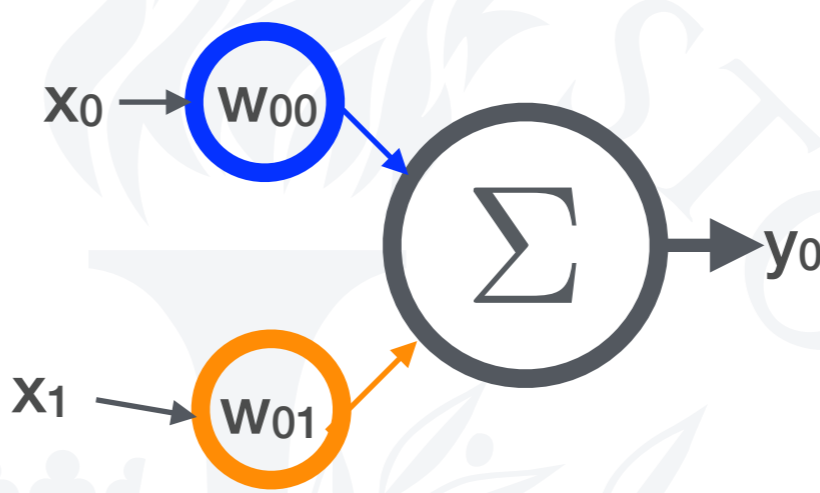
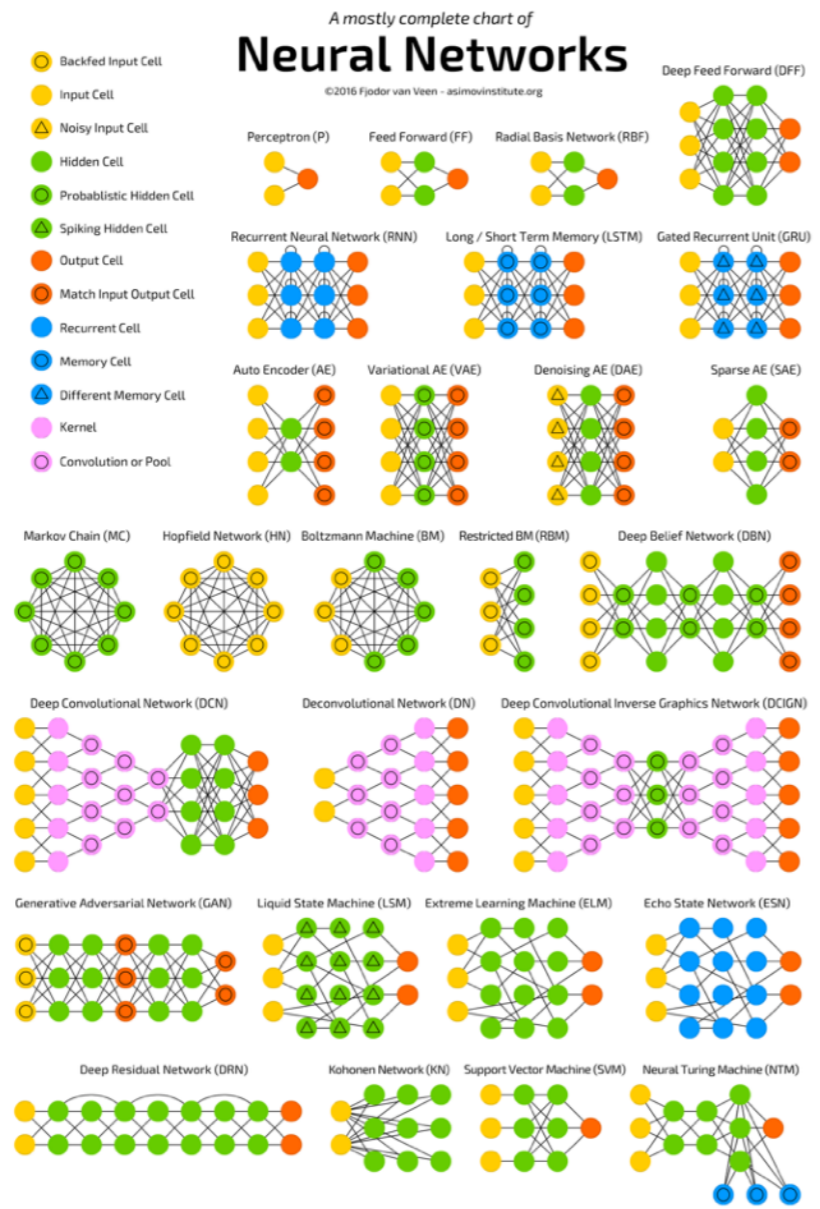


Distributed, parallel architecture



Temporally separated spikes

Connectivity and weight model file



$$y_j = \sum_i w_{ij} x_i$$

Plot	Equation
	$f(x) = x$
	$f(x) = \begin{cases} 0 & \text{for } x < 0 \\ 1 & \text{for } x \geq 0 \end{cases}$
	$f(x) = \frac{1}{1 + e^{-x}}$
	$f(x) = \tanh(x) = \frac{2}{1 + e^{-2x}} - 1$
	$f(x) = \tan^{-1}(x)$
	$f(x) = \frac{x}{1 +  x }$
	$f(x) = \begin{cases} 0 & \text{for } x < 0 \\ x & \text{for } x \geq 0 \end{cases}$
	$f(\alpha, x) = \begin{cases} 0.01x & \text{for } x < 0 \\ x & \text{for } x \geq 0 \end{cases}$
	$f(\alpha, x) = \begin{cases} \alpha x & \text{for } x < 0 \\ x & \text{for } x \geq 0 \end{cases}$
	$f(\alpha, x) = \begin{cases} \alpha x & \text{for } x < 0 \\ x & \text{for } x \geq 0 \end{cases}$
	$f(\alpha, x) = \begin{cases} \alpha(e^x - 1) & \text{for } x < 0 \\ x & \text{for } x \geq 0 \end{cases}$
	$f_{t_l, a_l, t_r, a_r}(x) = \begin{cases} t_l + a_l(x - t_l) & \text{for } x \leq t_l \\ x & \text{for } t_l < x < t_r \\ t_r + a_r(x - t_r) & \text{for } x \geq t_r \end{cases}$ $t_l, a_l, t_r, a_r$ are parameters.
	$f(x) = \max(0, x) + \sum_{s=1}^S a_s^2 \max(0, -x + b_s^s)$
	$f(x) = \ln(1 + e^x)$
	$f(x) = \frac{\sqrt{x^2 + 1} - 1}{2} + x$

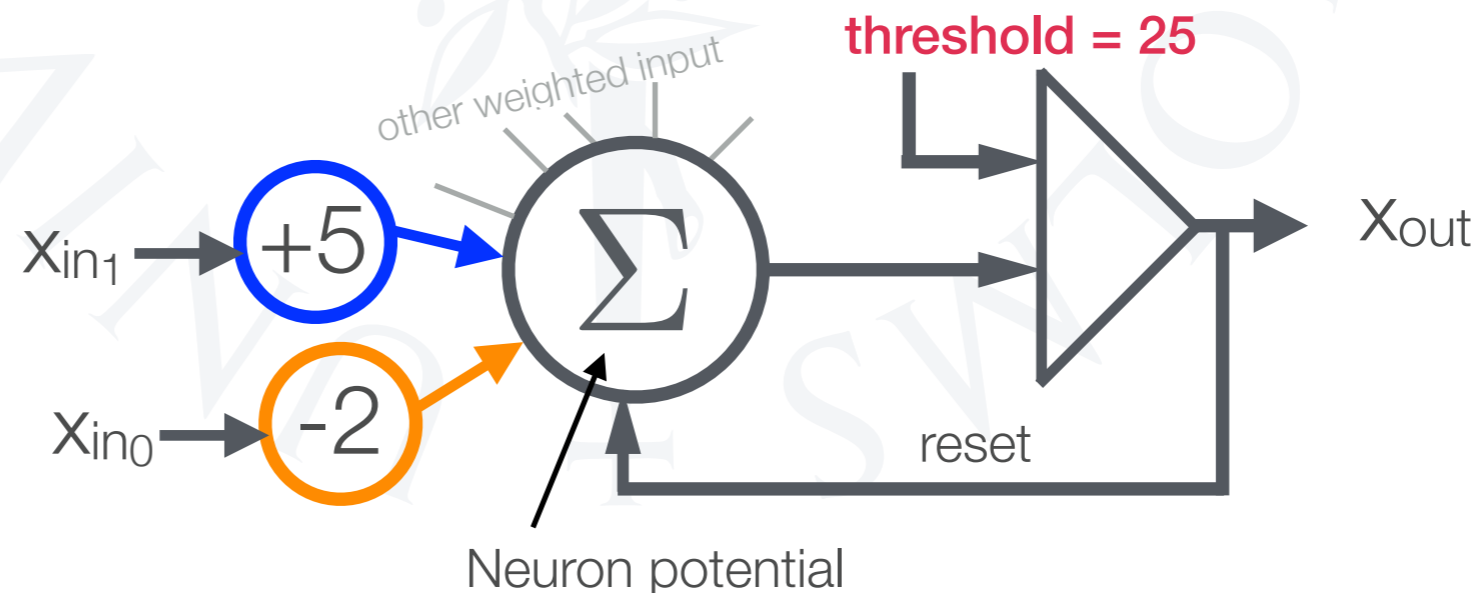
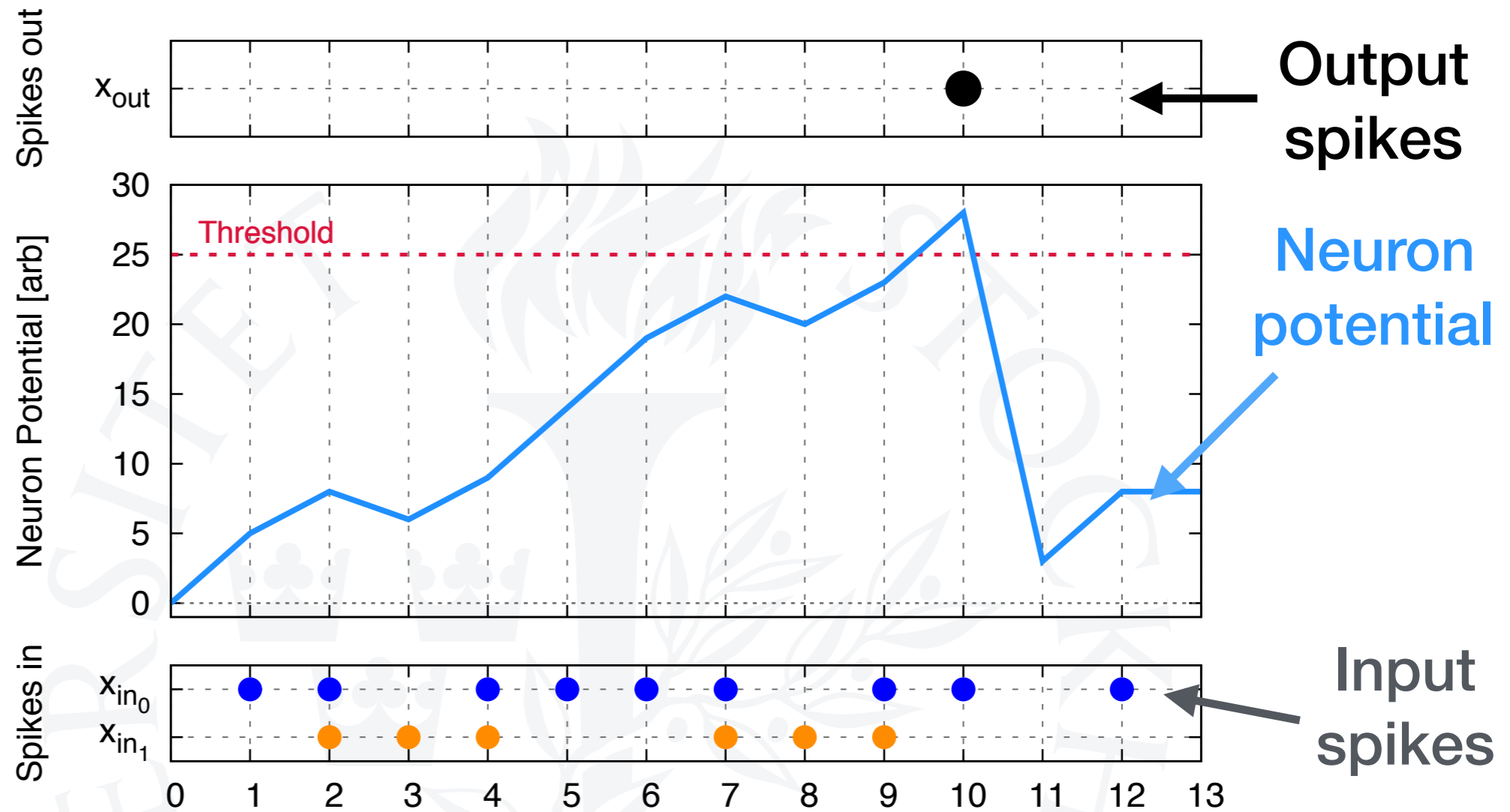
Topology

Sum of weighted inputs

Activation function

ANN's and neuromorphic networks have much in common.

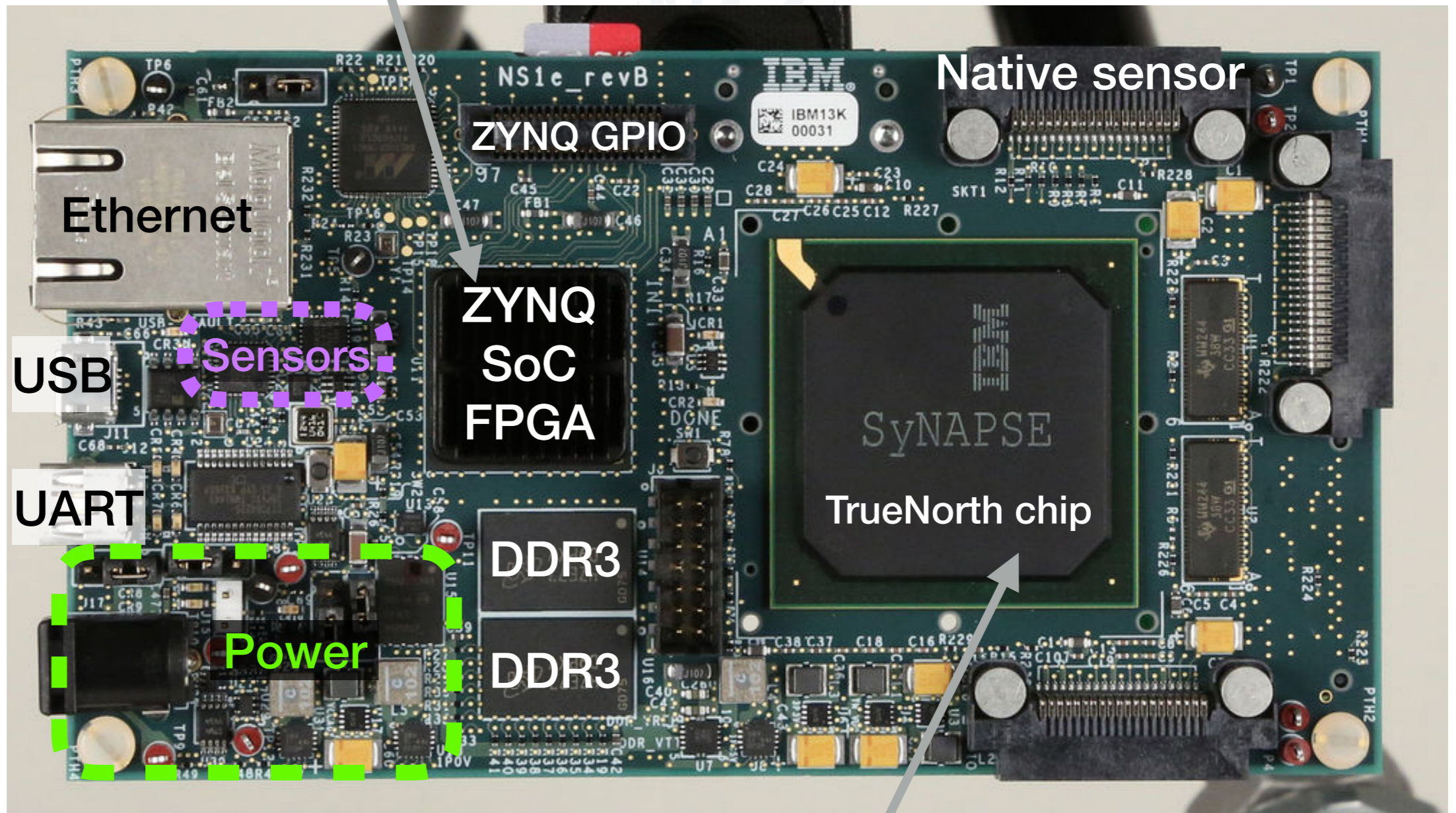






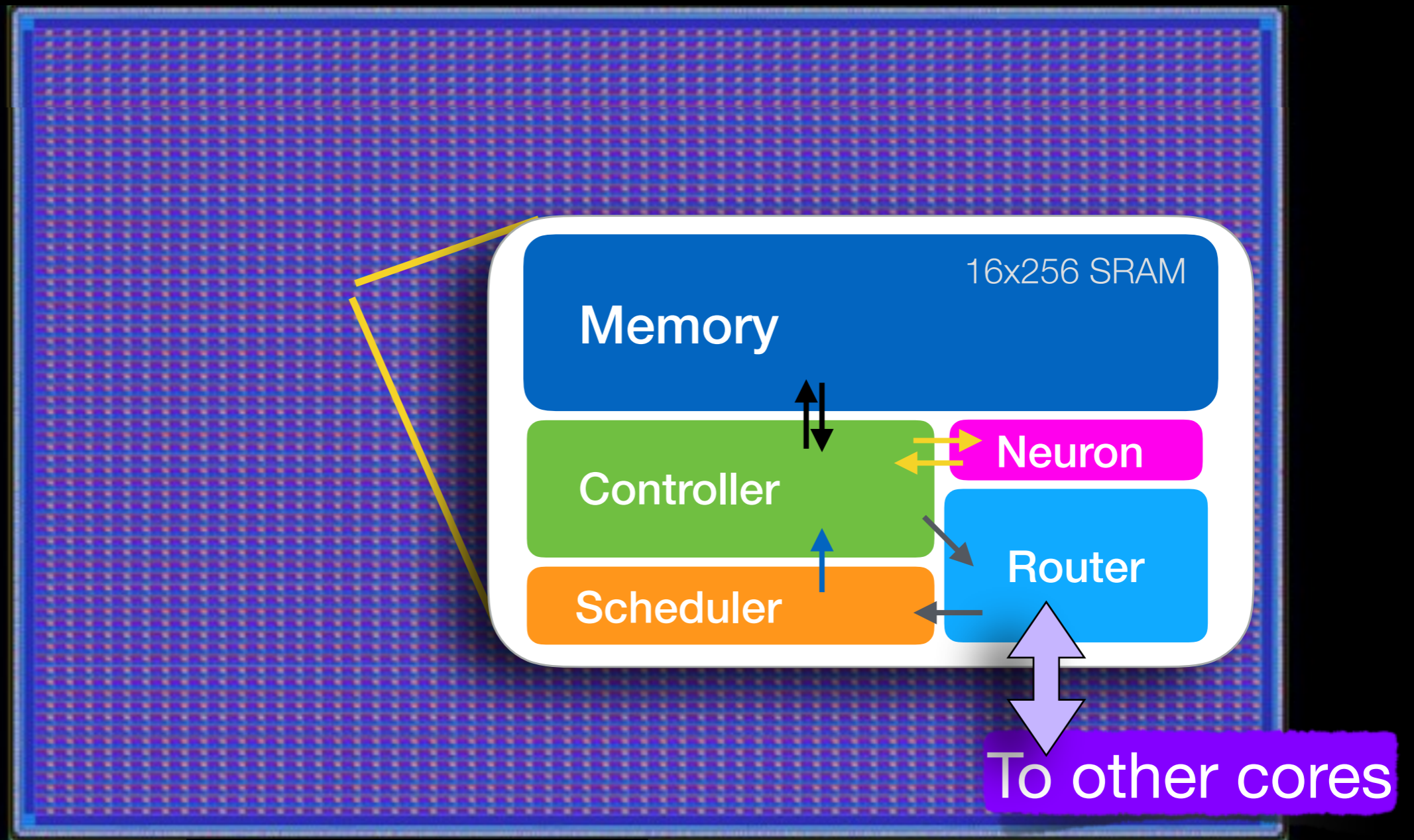


spike transduction



2015 IBM released **TrueNorth** test boards to US national labs





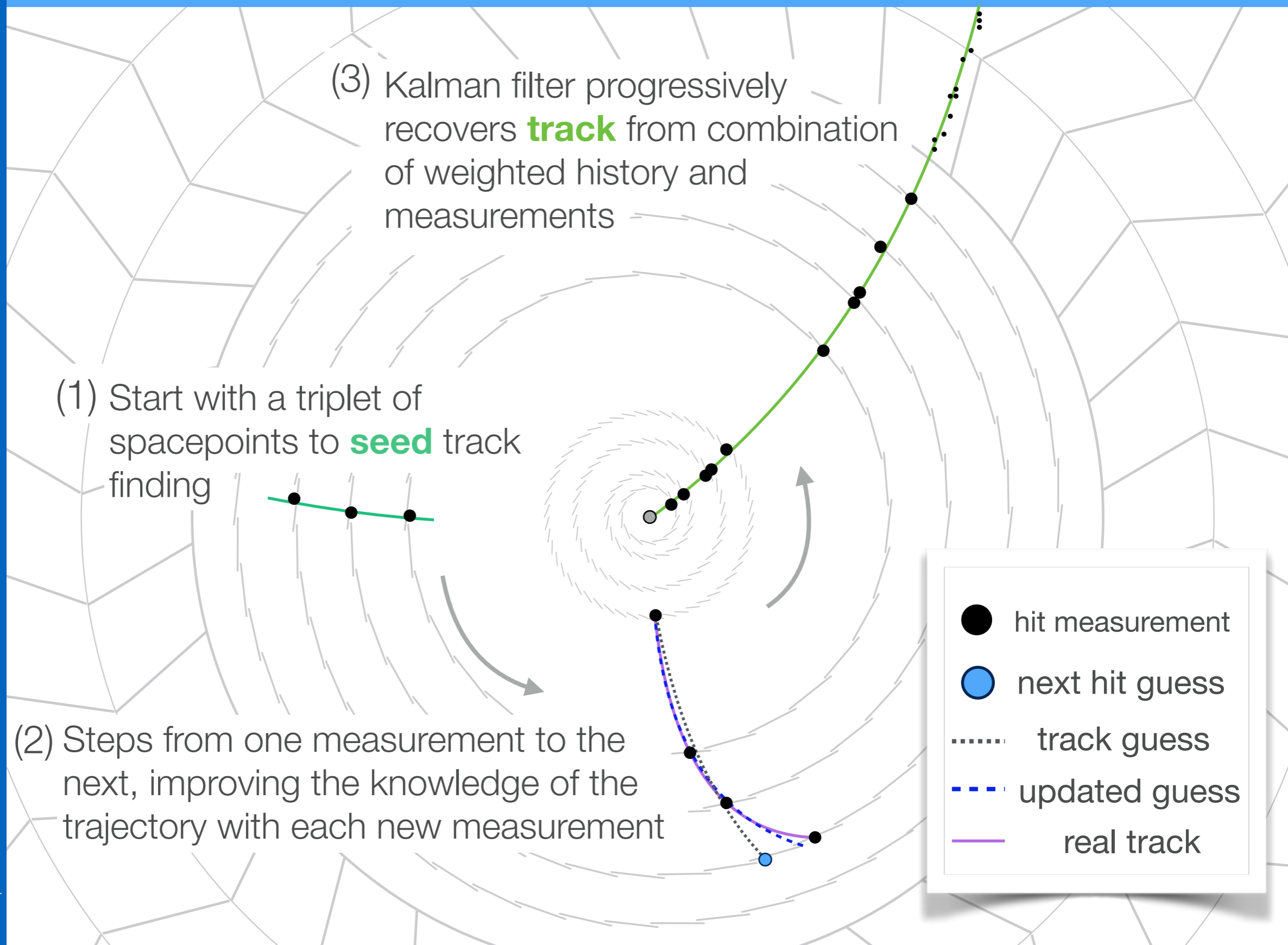
64x64 cores

Mixed synchronous-asynchronous logic, global 'tick' is synchronization point (1 kHz), 10's-100's of mW



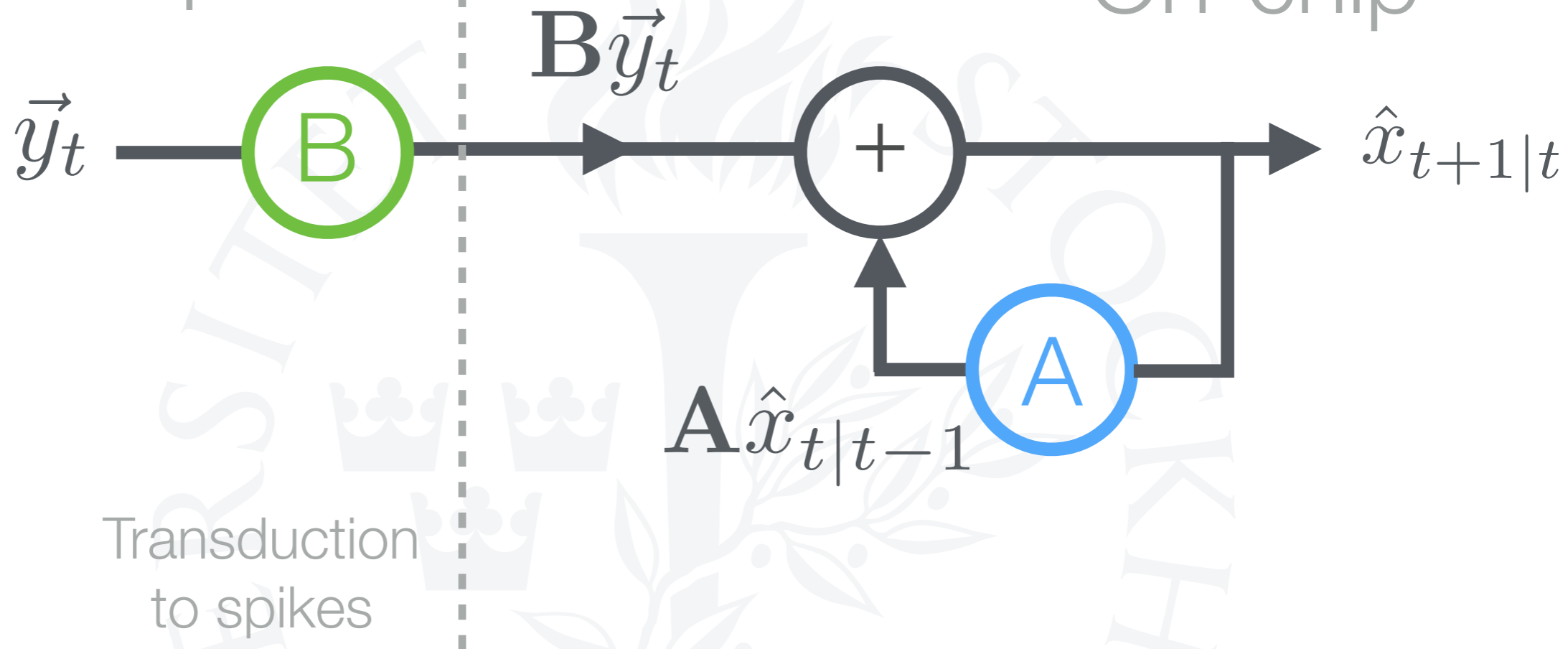
To understand how these design features affect usability of neuromorphic chip

Implement a tracking algorithm in TrueNorth



Off-chip

On-chip



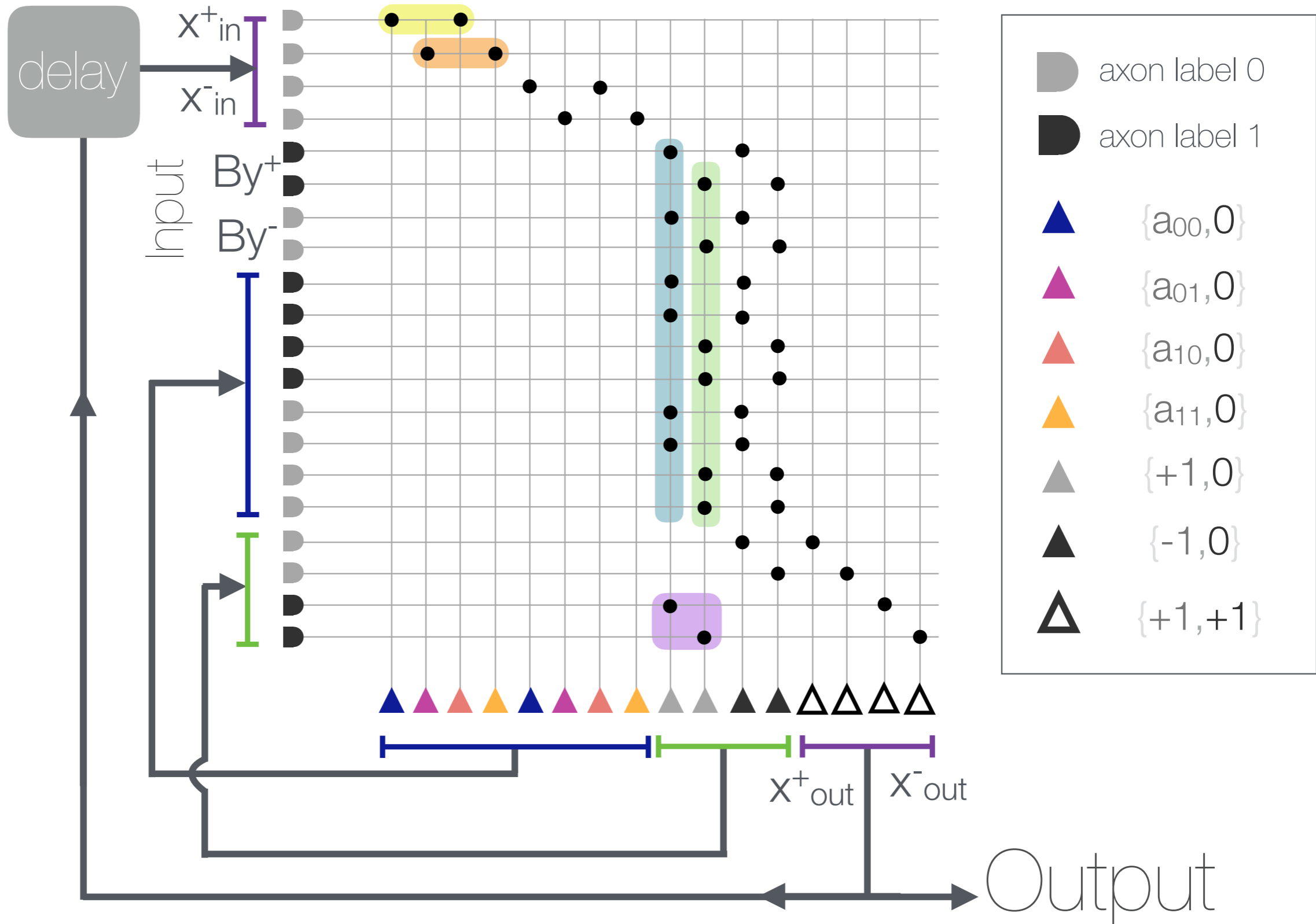
Discrete, steady state Kalman filter update

$$\begin{aligned}\hat{x}_{t+1|t} &= (\mathbf{F} - \mathbf{L})\hat{x}_{t|t-1} + \mathbf{L}\vec{y}_t \\ &= \mathbf{A}\hat{x}_{t|t-1} + \mathbf{B}\vec{y}_t\end{aligned}$$

$y$  = state measurement  
 $x$  = state prediction  
 $F$  = transition matrix  
 $L$  = Kalman gain  
 $w$  = process noise  
 $v$  = measurement noise

A state estimate and a noisy measurement

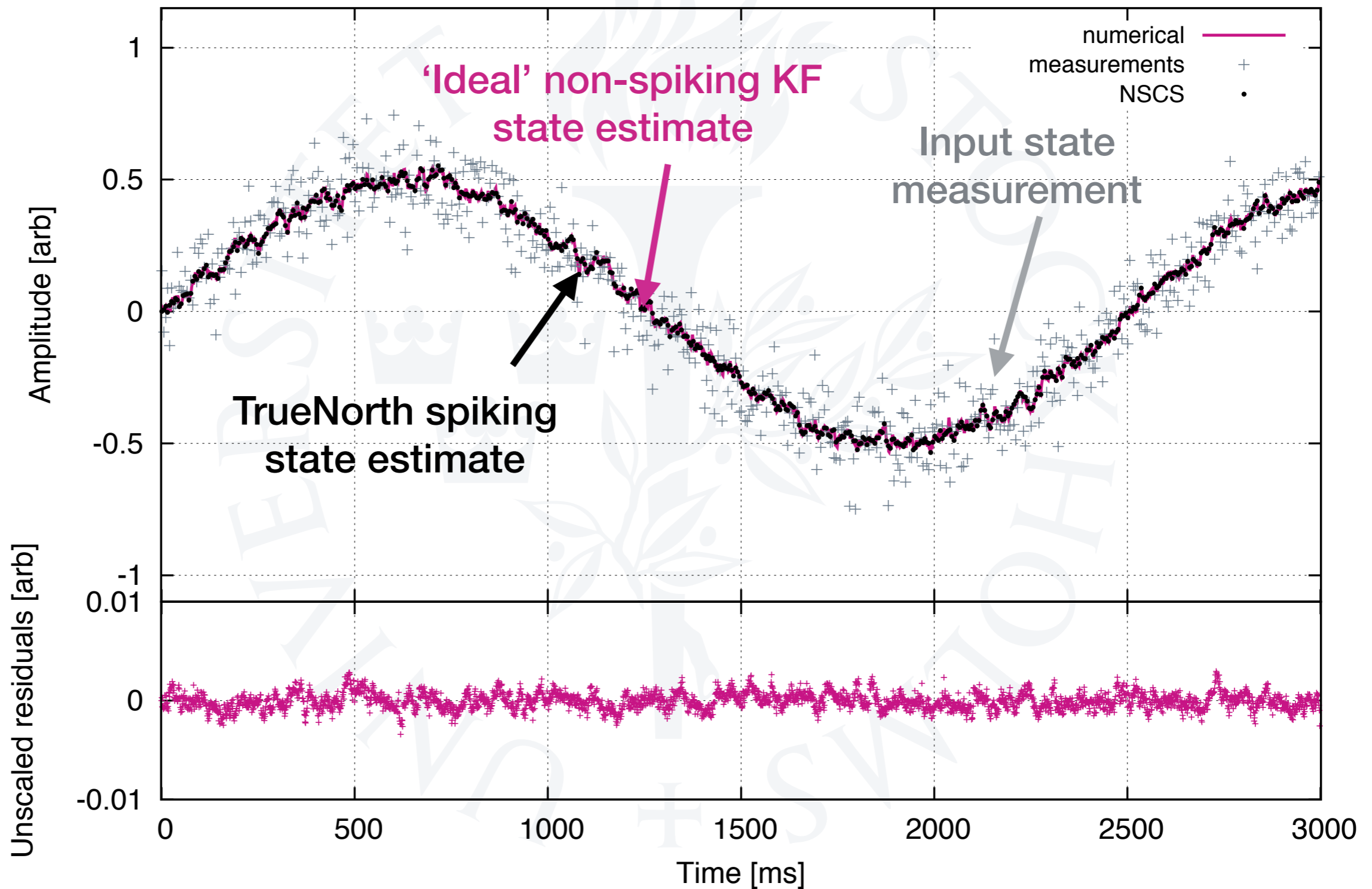
$$\vec{x}_{t+1} = \mathbf{F}\vec{x}_t + \vec{w}_t, \quad \vec{y}_t = \vec{x}_t + \vec{v}_t$$



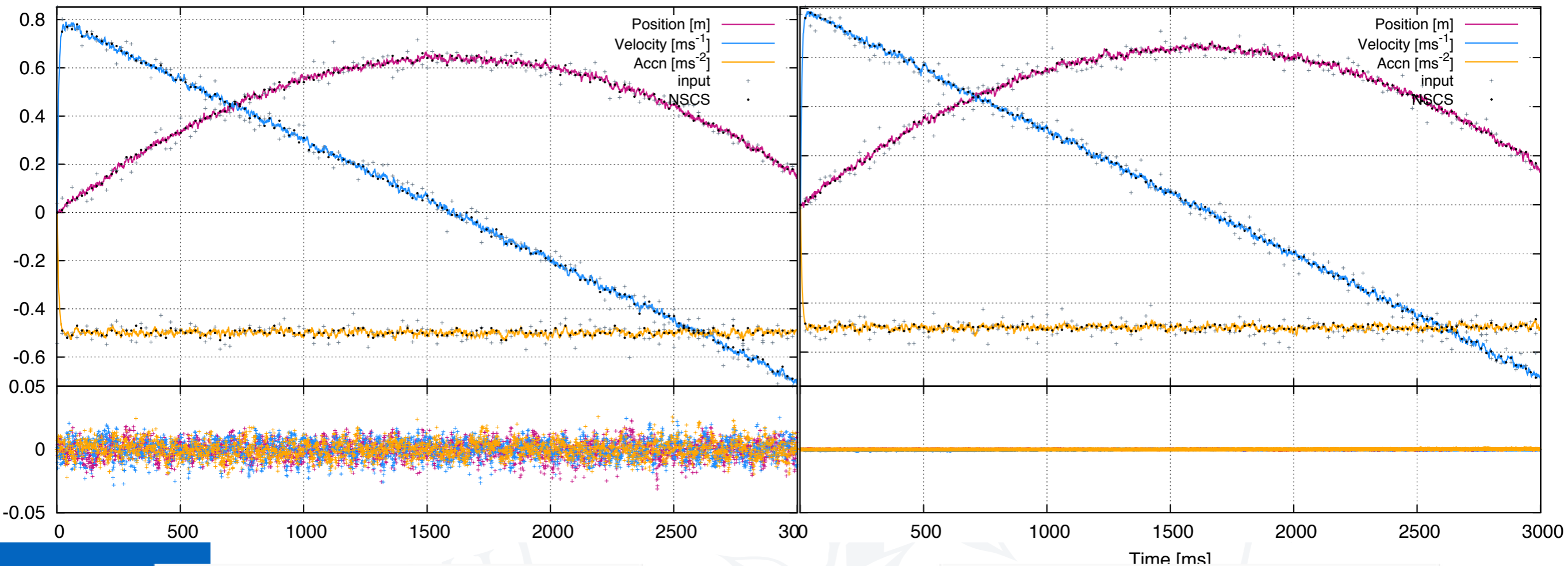
Does it work?

(rate encoded discrete steady-state, 2-dim KF)

NSCS KF vs numerical KF for  $\sin(x)$  - 1k tick encoding window, 1ms tick



1D Projectile tracking: 100 tick encoding, 1e-3 process noise, 1e-3 meas. noise, 1e-3 sampling rate projectile tracking: 10k tick encoding, 1e-3 process noise, 1e-3 meas. noise, 1e-3 sampling rate



**100** tick  
encoding window

**10,000** tick encoding  
window

Encoding (i.e. input precision) massively affects performance!



In binary:  
In unary:

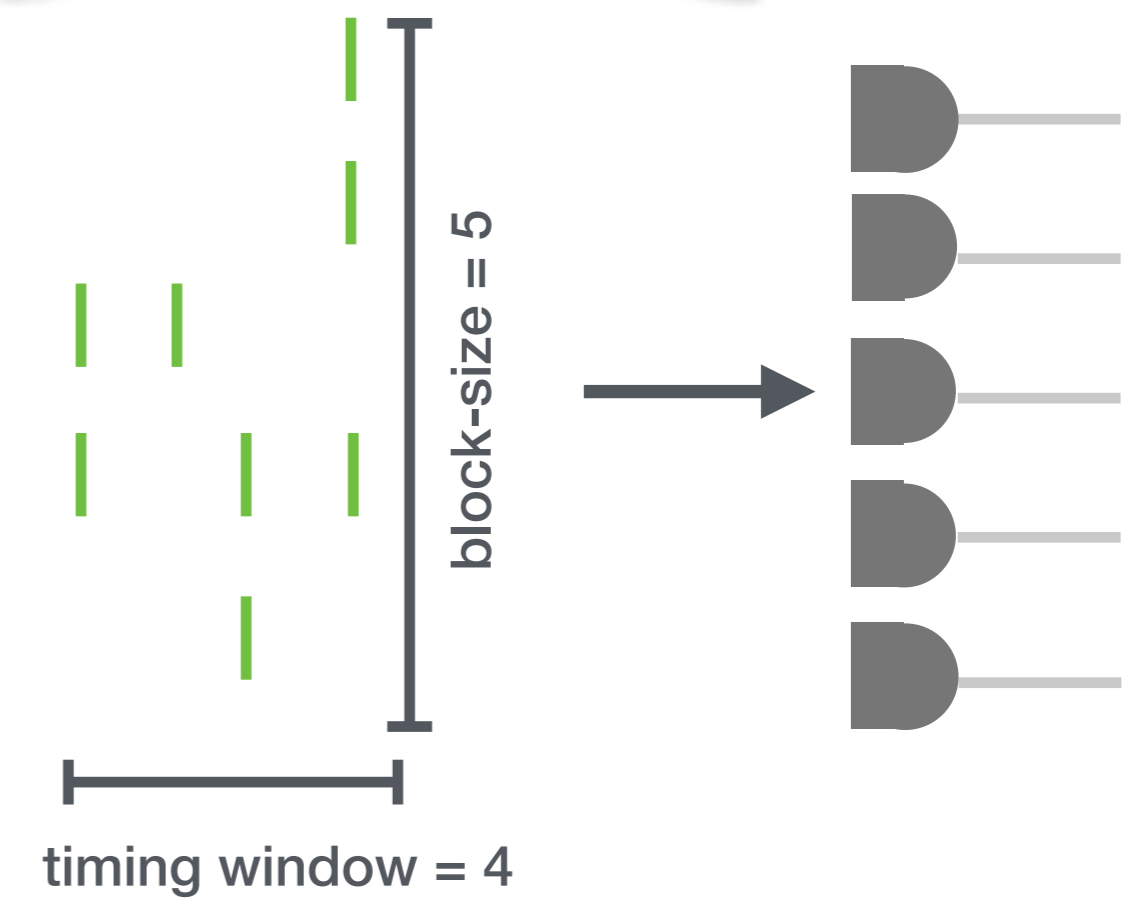
100 != 010 != 001  
100 == 010 == 001

## Rate encoding

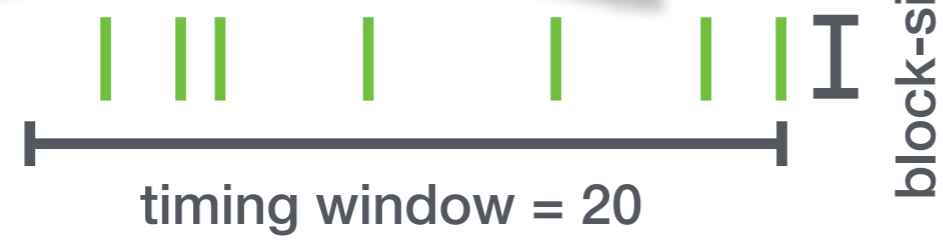
## Spatial & rate encoding

$x = 1.0 = 20 \text{ spikes}$

$x = 0.4 = 8 \text{ spikes}$



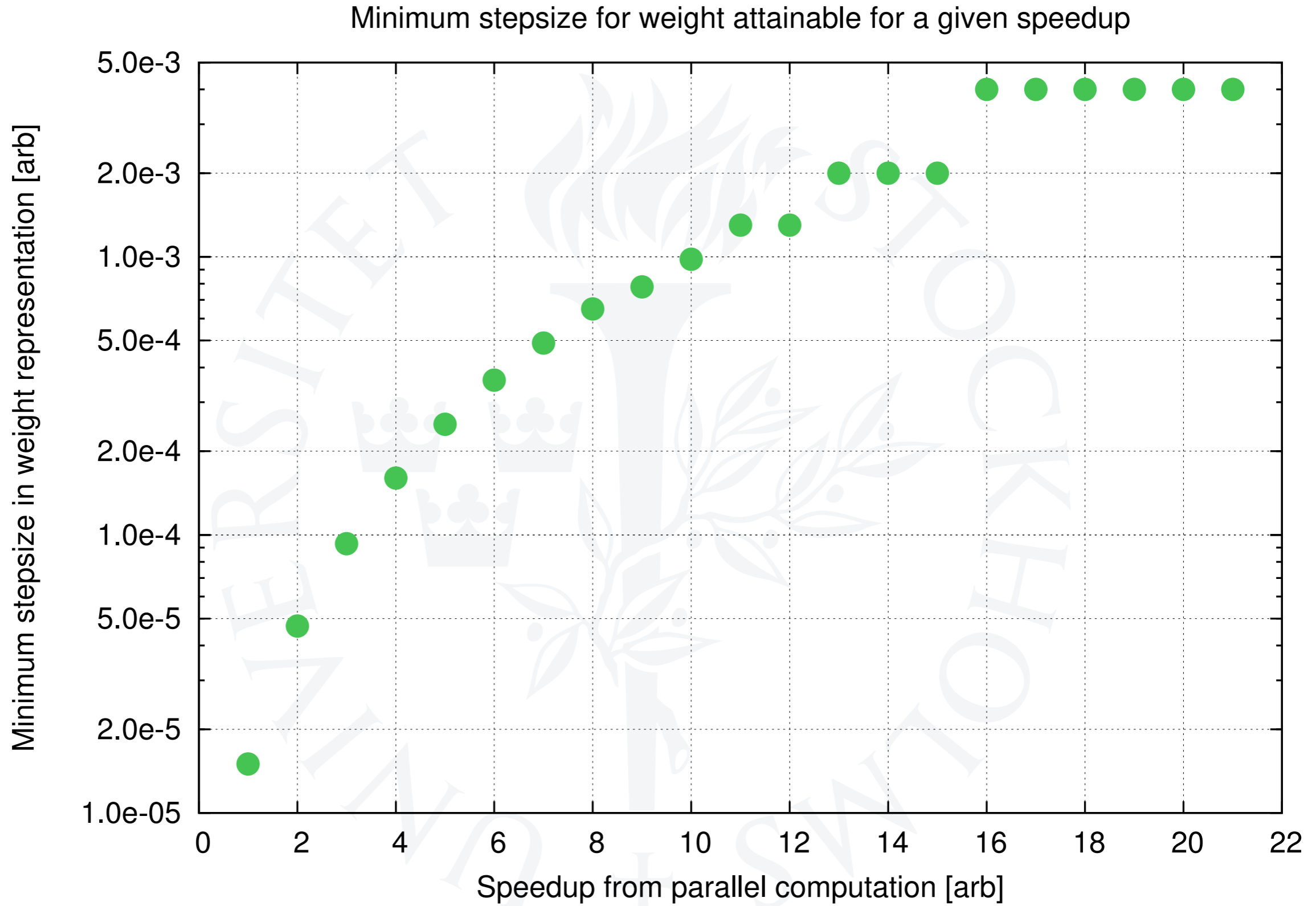
$x = 0.4 = 8 \text{ spikes}$



But if axons used for parallel inputs, can't be used to compensate for other things...



## Limits on core size

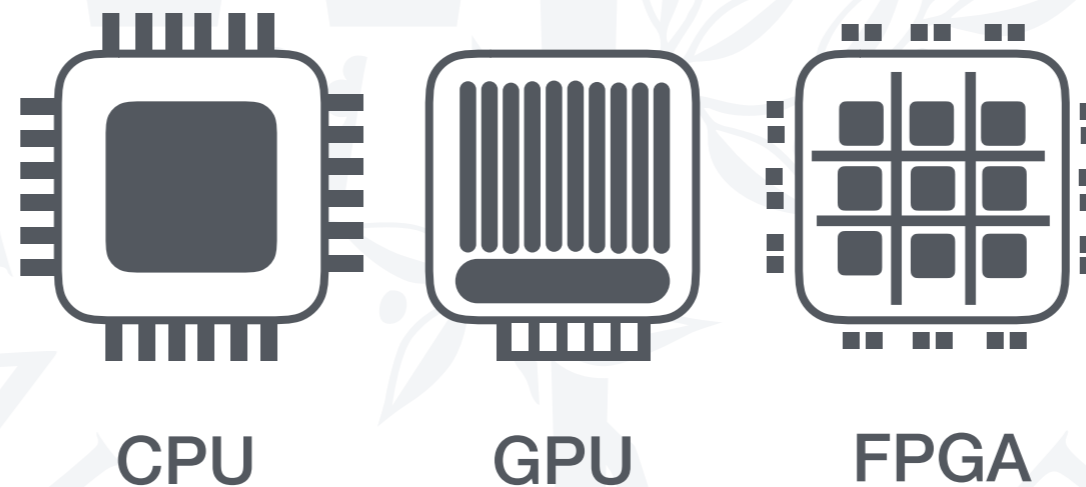


A compromise must be struck between accuracy and speedup: necessary because of the chips design and the way information is encoded



## Key points

- TrueNorth compromises flexibility and neuron complexity for low power.
- **Slow clock**: 2 kHz max clock: cannot be sped up
- **Small cores**: Parallelism of multiplications per core limited to 21x.
- **Complicated encoding**: spike encoding/decoding + tick must be done off-chip..
- **Fixed functionality at runtime**: cannot implement either real-time weight updates or back-prop.



Could neuromorphic hardware feature in our toolkit in 10 years time? **It seems more likely we would use a hardware ANN in a GPU or FPGA.**

## Conclusions

- Expanding our tracking capabilities open us up to interesting BSM physics signatures such as those seen with long-lived particles.
- High multiplicity events lead to non-trivial event reconstruction, particularly track reconstruction.
- Understanding how to minimize cpu usage with both algorithms and hardware is necessary to compensate for our burgeoning computing requirements.

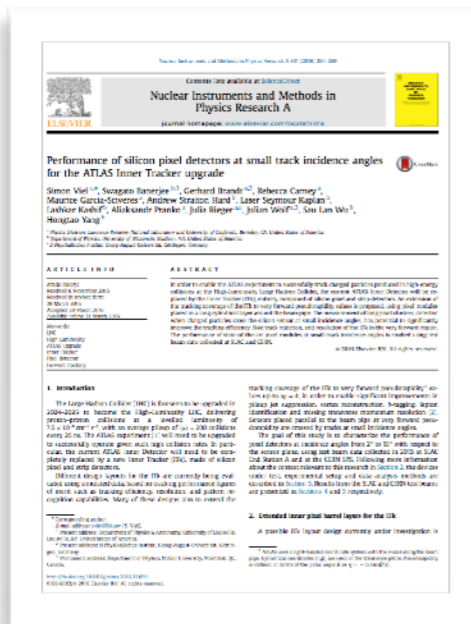
## Beyond PhD...

- During my postdoc I'd like to continue to tackle interesting problems that require a strong understanding of the detector.
- I want to do this by taking a leading role in ITk Pixel production, and by applying the skills and insight from DV+Jets to new challenges in data analysis!





Papers and contributions

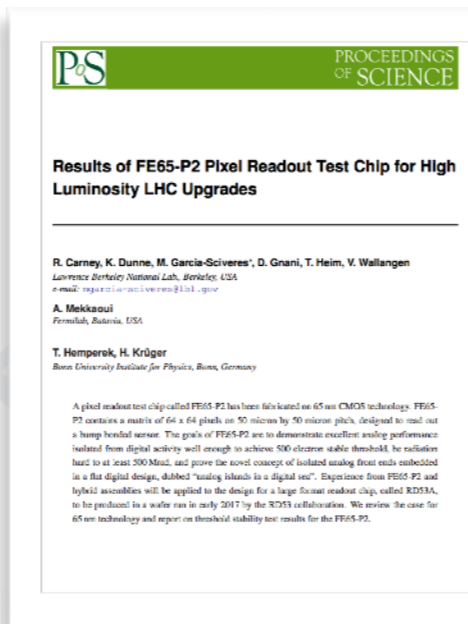


Performance of Silicon Pixel Detectors at Small Track Incidence Angles for ATLAS ITk

Published NIM A 11/15

https://doi.org/10.1016/j.nima.2016.03.099

Prepared modules, ran testbeam, wrote analysis framework

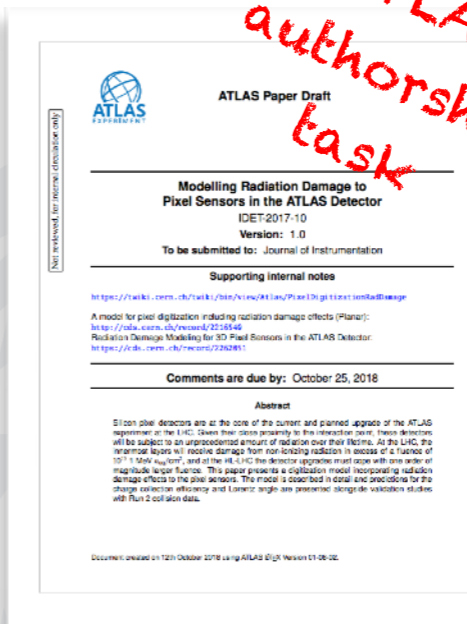


Results of FE65-P2 Pixel Readout Test Chip for High Luminosity LHC Upgrades

Published PoS ICHEP 11/16

https://doi.org/10.22323/1.282.0272

Digital verification of FE, prepared & ran chips during testbeam and irradiation

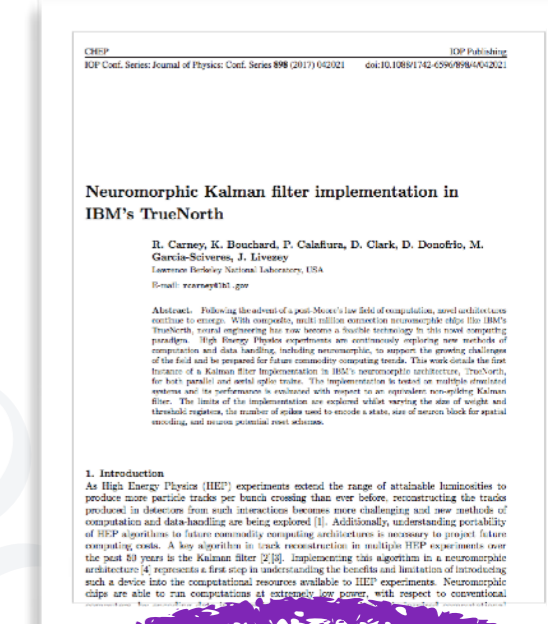


Modelling Radiation Damage to Pixel Sensors in the ATLAS Detector

ATLAS Phase 2 approval

https://glance.cern.ch/atlas/analysis/papers/details.php?id=10403

Wrote new digitizer for rel. 21 and rel. 22, restructured PixDig pkg for rel. 22



Neuromorphic Kalman filter implementation in IBM's TrueNorth

Published J. Phys Conf. 09/17

https://doi.org/10.1088/1742-6596/898/4/042021

Designed and implemented KF in TN, ran studies, wrote paper

ATLAS authorship task



And search for new physics using Run 2 data currently in the works...



# Backup



# DV+JETS



small coupling  $\leftarrow$

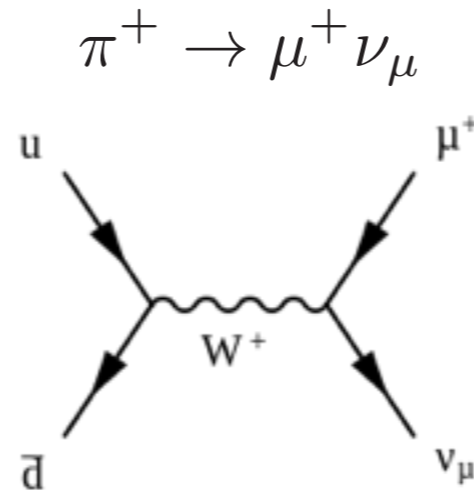
$$\Gamma \sim y^2 \left( \frac{m}{M} \right)^n m$$

Set by symmetry structure, typically  $n \geq 4$

$m \ll M$

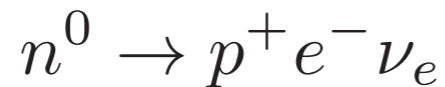
hierarchy of scales  $\leftarrow$

- Heavy mediator (e.g. charged pion decay)



$$\Gamma \sim g_2^2 \left( \frac{m}{m_W} \right)^4 m$$

$$\Gamma \sim g_2^2 \left( \frac{m_n - m_p}{m_W} \right)^4 (m_n - m_p)$$



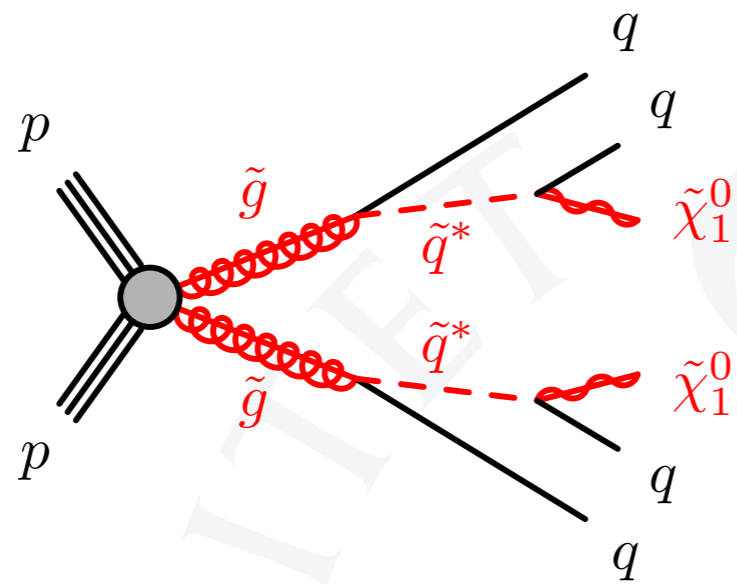
- Nearly degenerate masses (e.g. free neutron decay)

- Small coupling (e.g B-hadron decay)



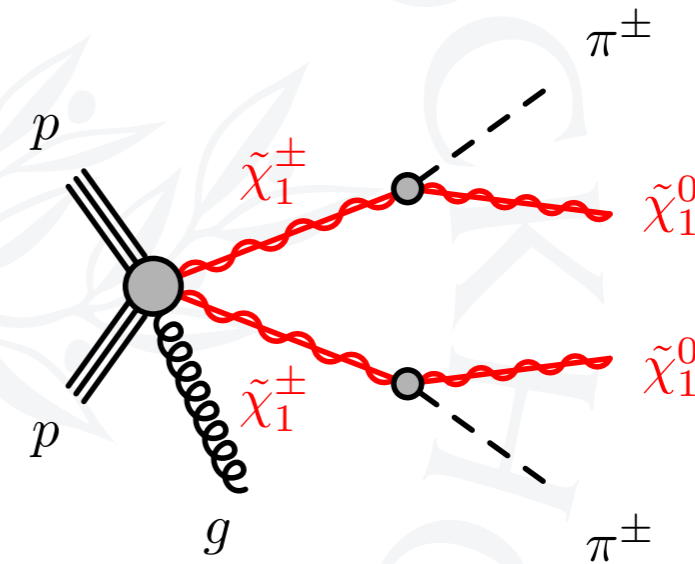
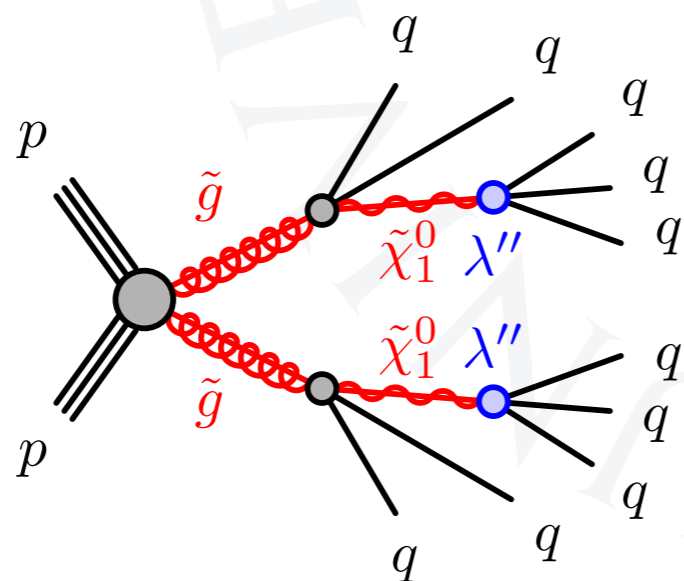


More detail: **SUSY**



Large mass difference

Degenerate mass



Small coupling



- R-parity is a discrete symmetry, defined as:

$$R_p = (-1)^{3B+L+2S}$$

- Standard Model particles have  $R_p = 1$ , and SUSY particles  $R_p = -1$
- R-parity violating Lagrangian can be simplified as:

$$W_{\mathcal{R}_p} = \lambda_{ijk} L_i L_j \bar{E}_k + \lambda'_{ijk} L_i Q_j \bar{D}_k + \lambda''_{ijk} \bar{U}_i \bar{D}_j \bar{D}_k.$$

lepton violating  
terms

baryon violating  
terms

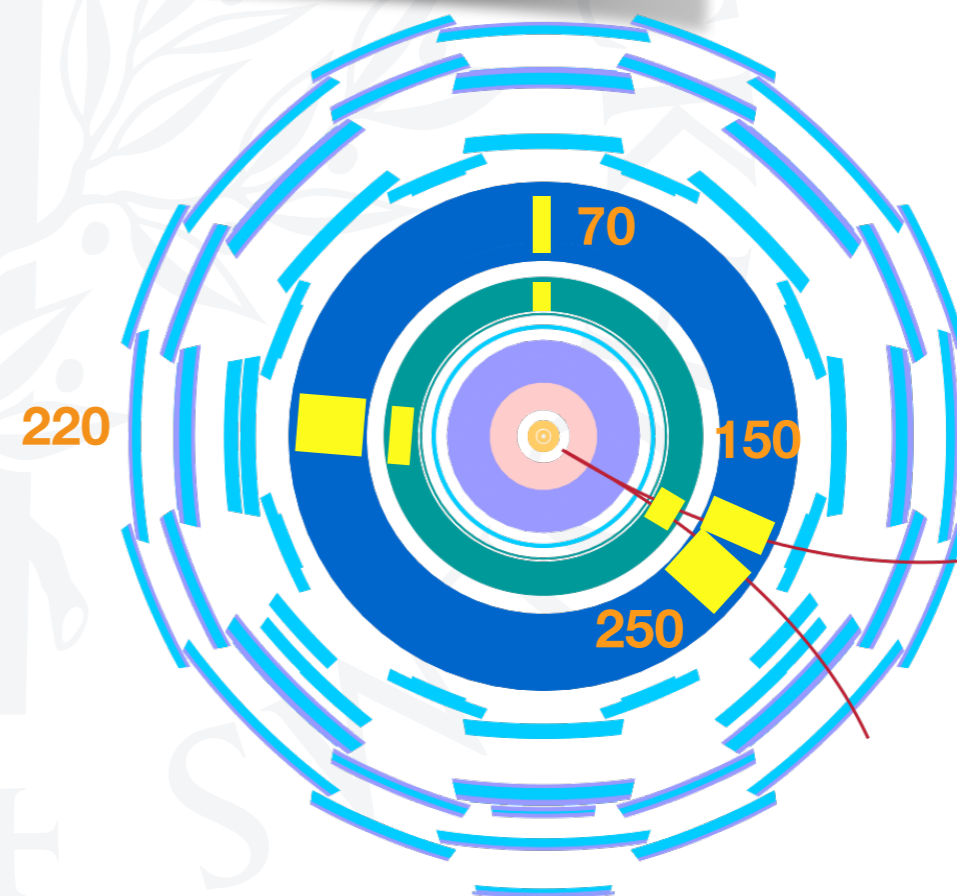


Second part is jet multiplicity flags:

```
DV_MultiJetFinalTracklessFilter =
{ tracklessJets &
[(2j & 3j) ^ 4j ^ 5j ^ 6j ^ 7j] &
DVMultiJetTriggerFlags }
```

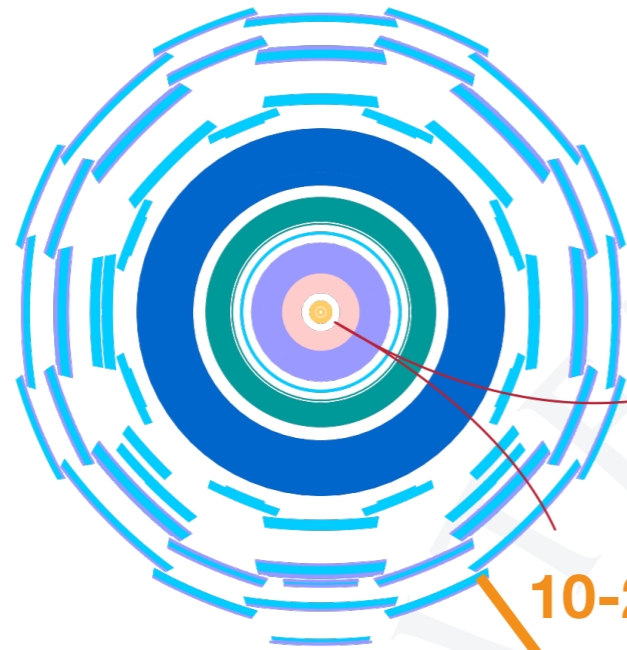
2015-2016 DV Multijet DRAW filter

# jets	Jet pT cut [GeV]	Low. unpre. trig. [GeV]
2	220	-
3	120	175
4	100	85
5	75	55
6	50	45
7	45	45



e.g. this scenario would pass the (2j & 3j) flag but not the 4j flag

DRAW filter before special reco



Must access RAW data to run Large Radius Tracking (LRT) and Displaced Vertex (DV) reconstruction algorithms.  
Expensive & time consuming...

10-20 kHz from HLT

physics\_main raw data

standard reconstructed data

analysis

**filter**

~1% of RAW data

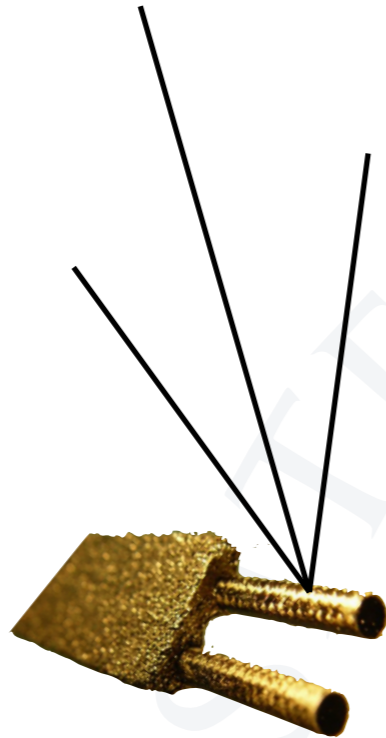
**LRT, DV**  
50-100 Hz

special reconstructed data

analysis



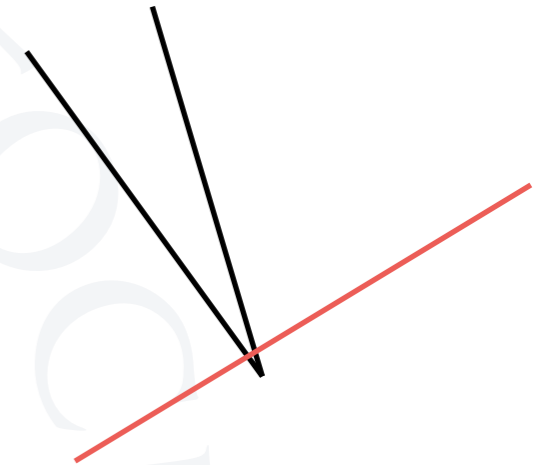
RPVLL\_filter ~ 60% of bandwidth of similar RAW filters



Hadronic  
interactions



Merged low-  
mass vertices



Accidental  
crossings

Understanding backgrounds involves understanding how the vertices themselves are formed...

Other DV analyses from Run 1 paper either published or pushing ahead:



**DV + jets**

Run 2 effort in progress...

**DV + MET**

Run 2 preprint

**DV + muon**

Run 2: EB formed

**DV + dilepton**

Run 2:  
glance phase 1  
(SUSY-2017-04)

### DV + Jets analysis:

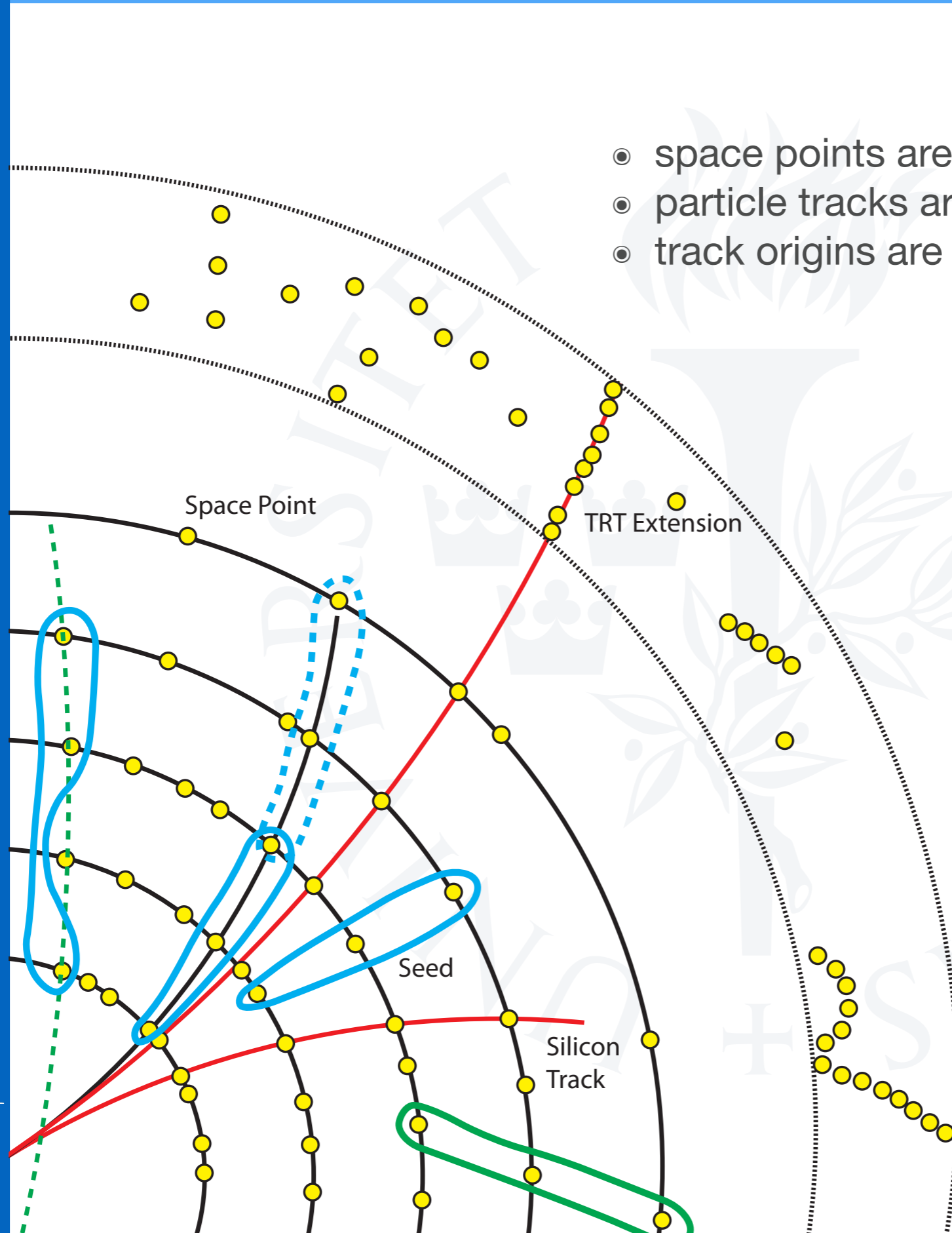
- I am one of 3 students on this analysis
- Developed analysis framework, **signal selections**
- Will now work on 1 of the 2 major backgrounds: **accidental crossings**
- Intend to write significant portion of note/paper
- Changes to DRAW, some of which discussed here



- In ATLAS reconstruction software, two vertices are merged if their significance is smaller than a given *displacement significance*.
- Displacement significance is effectively the length of the displacement between the two vertices, scaled by projecting the uncertainties in the basis vectors (x,y,z) onto the distance vector between the 2 vertices.
- How often does it occur that two vertices are merged by default and promoted to a higher mass?



- space points are created from charge clusters
- particle tracks are created from space-points
- track origins are clustered into a 'vertex'





## For successful reconstruction of DV mass:

- Must reconstruct tracks
- Must correctly reconstruct track parameters with fit
- Must use tracks to form DV or, if missed in forming DV, re-attach them after it is formed.

Recover the tracks

Fit the tracks

(Re-)attach tracks to reco DV

# TrueNorth & HPC

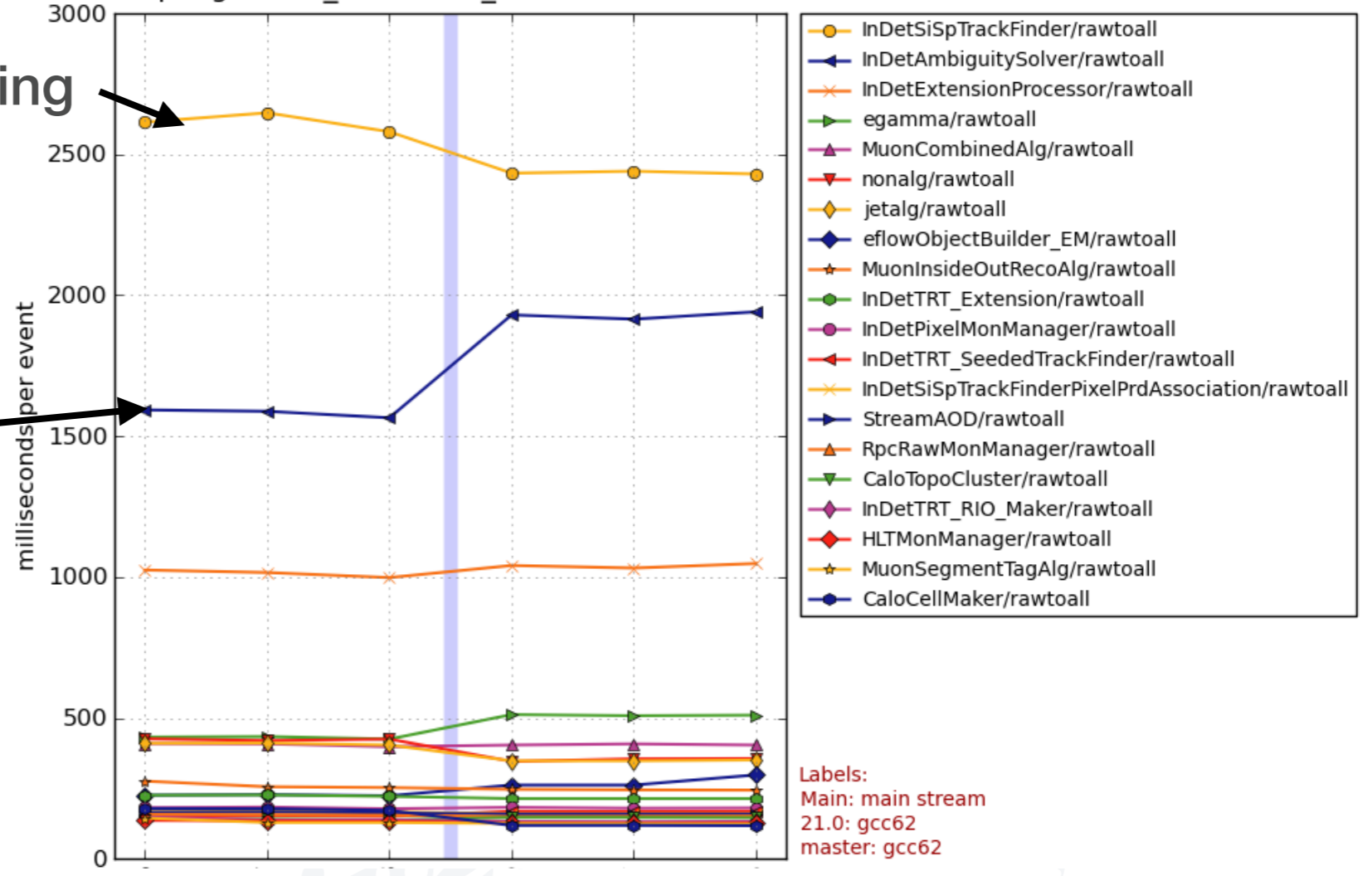


How various processes scale

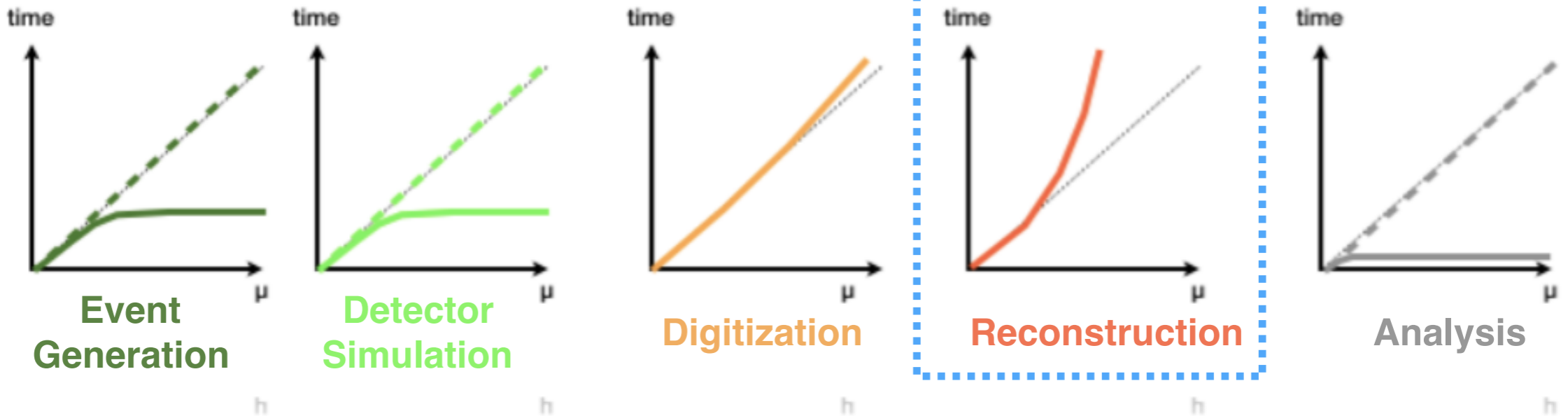
## Silicon track finding

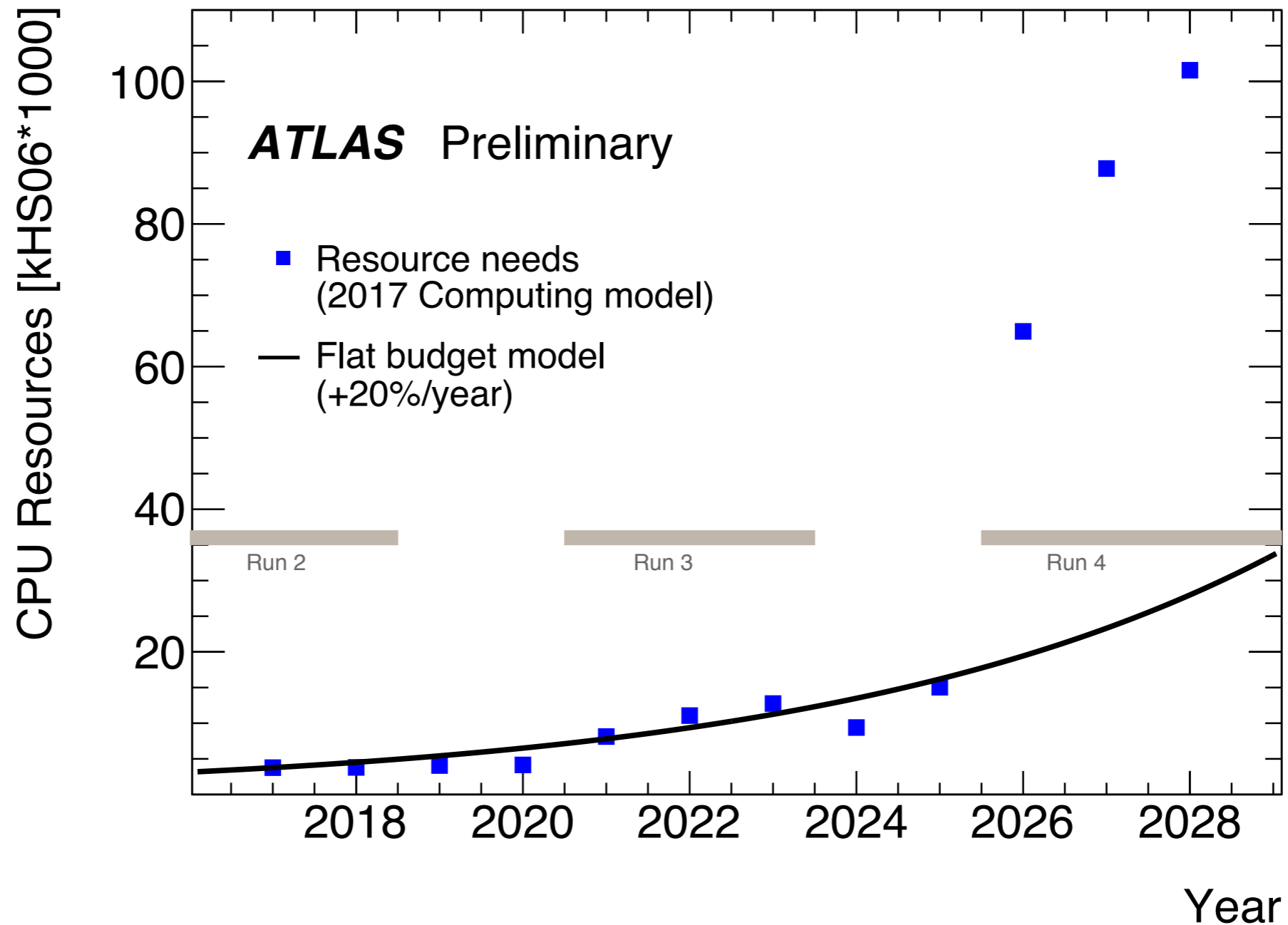
## Ambiguity resolution

Top algs in no\_command\_available of 248 events

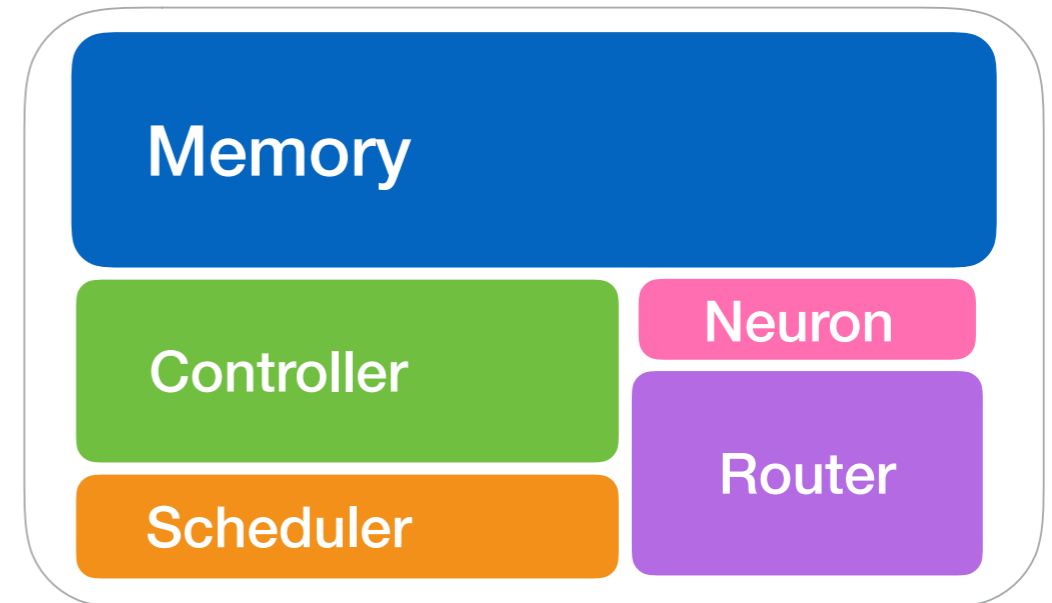
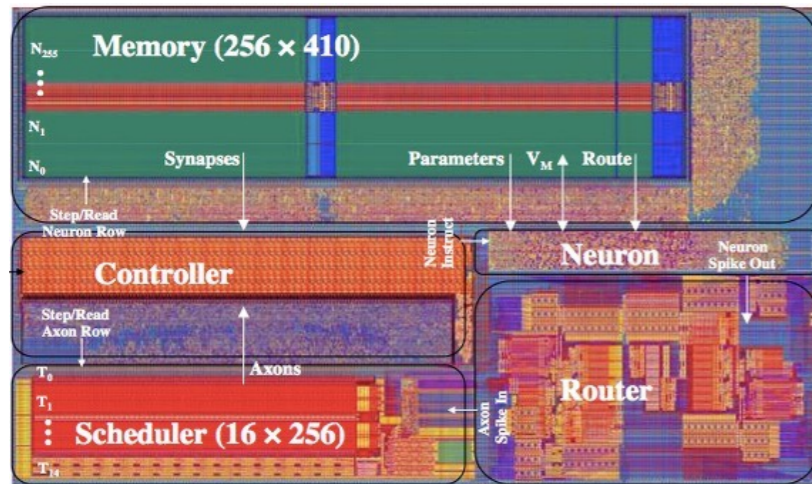


Labels:  
 Main: main stream  
 21.0: gcc62  
 master: gcc62





- Our requirements will outstrip our resources by a factor of 3
- A different approach is needed: supplement the grid and parallelize.

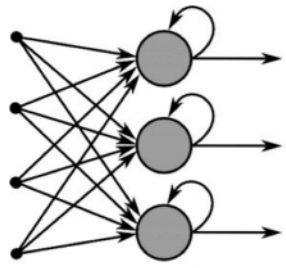


## Remainders in the neuron potential

- **Scheduler error:** spike delivery is ensured to be longer than max latency between source and destination. So if chip has a large model implemented and tick is sped up - spike generator may refuse to create file.
- **Token ctrl error:** If the spikes can't be processed in time, because of the volume of spikes in a single core.
- A TrueNorth chip can only take so many spikes per port, and then there is a firmware and software pipeline that must match this **throughput**. We see on the order of **15 million spikes** per second total throughput, which is **480 Mbps** data rate between FPGA and TN.

We never managed to cause either of the first 2 failure modes with our simple serial kaman filter. However we did cause the last error to be thrown when we tried to increase the tick rate from 1kHz to 10kHz. A 2kHz tick was possible.

Throttling arose when using a serial spike train and < 0.1% of the chip area. Unlikely more complicated models could be sped up on this version of TrueNorth.

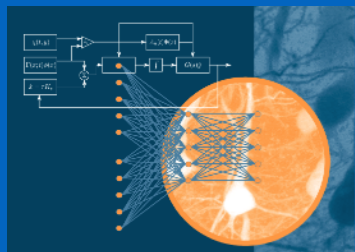


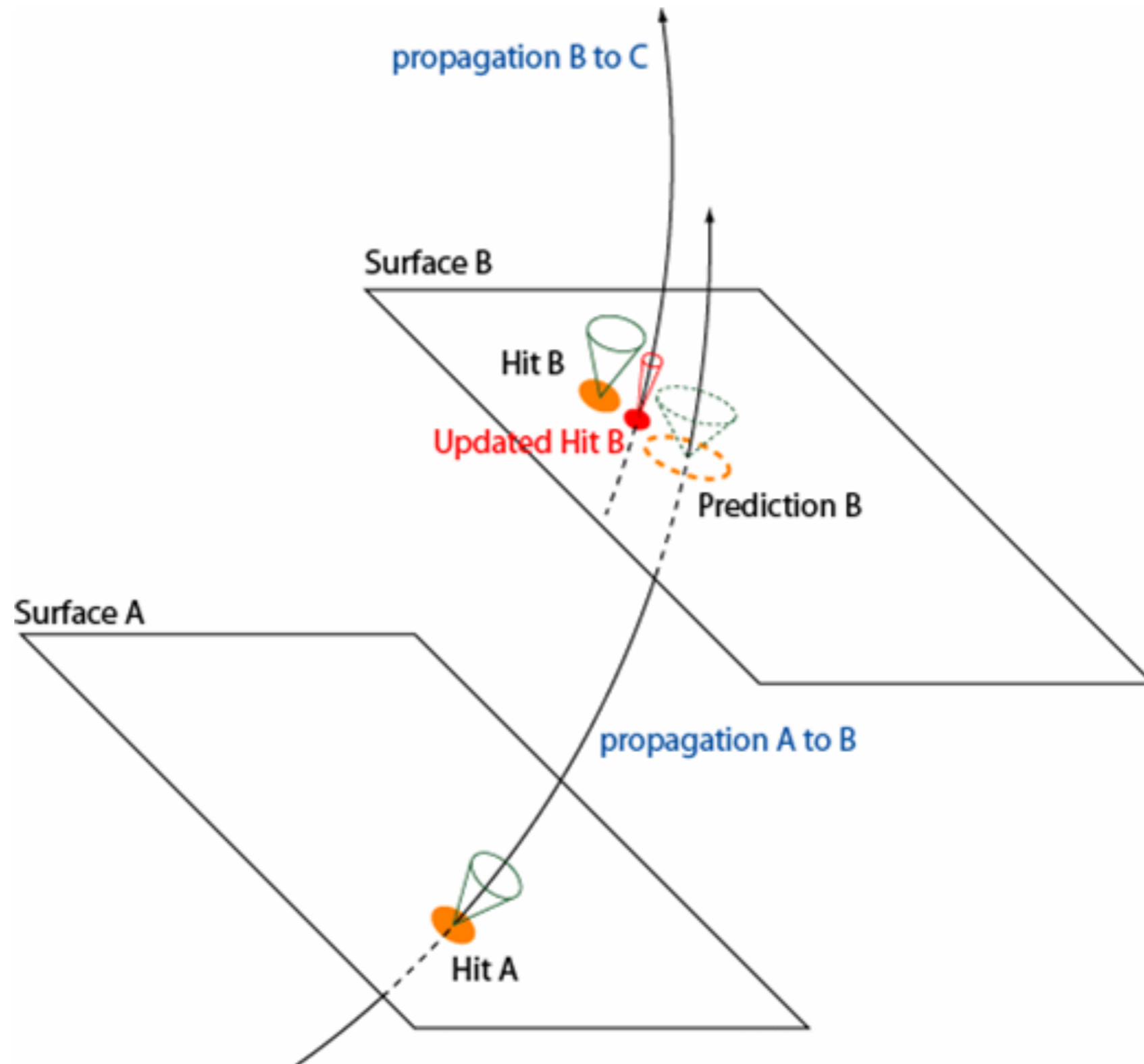
- Kalman filter implemented in ANN easily! Can perform covariance and Kalman gain matrix updates using recurrence &/or back-prop. depending on which KF. This is also true in FPGA and GPU implementations.
- **Cannot do this on TrueNorth because:** neuron parameters and connectivity cannot be changed at runtime and training is non-trivial (activation function is not differentiable). So would have to continuously stop and start chip to update model file.

## Other implementations we couldn't copy

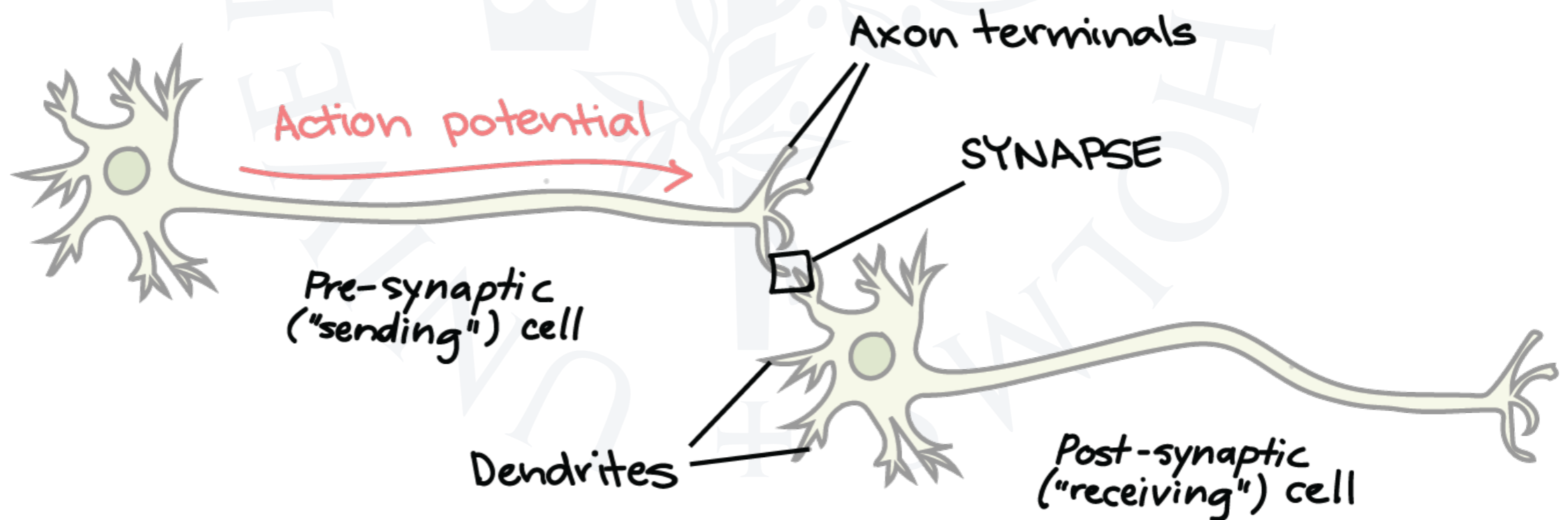
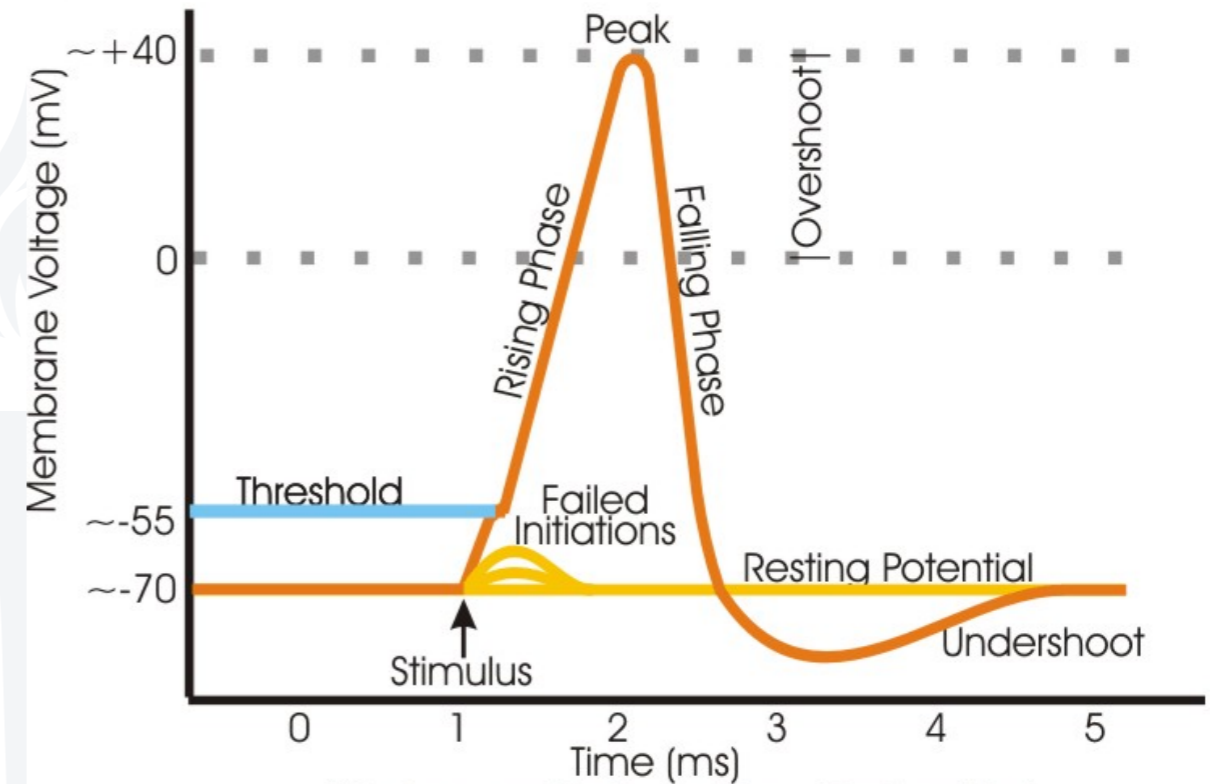
- [Neural Engineering Framework](#) implements linear control systems in *populations* of neurons. Perfect for a KF!
- **Cannot do this on TrueNorth because:** requires a more complex neuron model that includes non-linear leakage current & analogue spiking to shape post-synaptic current. Will not work for simple TrueNorth neuron model nor with digital spikes.

Things we  
couldn't do





- Neurons transmit information.
- Potential difference across inside & outside of cell.
- Incoming spikes change potential of membrane.
- Past a certain threshold spike is emitted.





## McCulloch-Pitts neuron

The McCulloch-Pitts neuron is a simplified version of the perceptron, without a bias and with no Hebbian learning rules. It has a binary output based on a thresholded step function.

$$y = \text{step} \left( \sum_{i=1}^N n_i s_i \right)$$

## TrueNorth neuron

### II. NEURON SPECIFICATION

Our neuron model is based on the leaky integrate-and-fire neural model with a constant leak, which we augmented in several ways. We begin by briefly reviewing the classic leaky integrate-and-fire neural model, followed by an in-depth description of our neuron model.

#### A. The Leaky Integrate and Fire (LIF) Neuron

The operation of the leaky integrate-and-fire (LIF) neuron model with a constant leak is described by five basic operations:

$$y = \text{step} \left( V_j(t-1) + \sum_{i=1}^{N=256} n_i(t) s_j^{G_i} w_{i,j} + \lambda_j \right)$$

## Hodgkins-Huxley neuron

Hodgkins-Huxley model is a biologically plausible neuron model that accounts for leakage across the membrane from different ions and different rates of ions. The  $I_i$  term is a parameterized model of the conductance across the membrane, with at least 20 parameters.

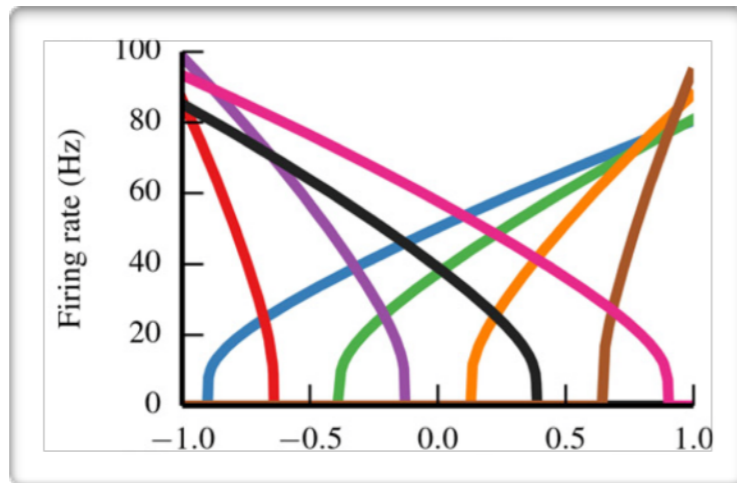
$$C_m \frac{dV(t)}{dt} = - \sum_i I_i(t, V)$$

## Leaky Integrate-and-fire neuron

One of the simplifications to this model to make it tractable is the LIF neuron. Leaky integrate-and-fire is a reduced complexity subset of HH. Grouping any current independent of input as a constant leak.

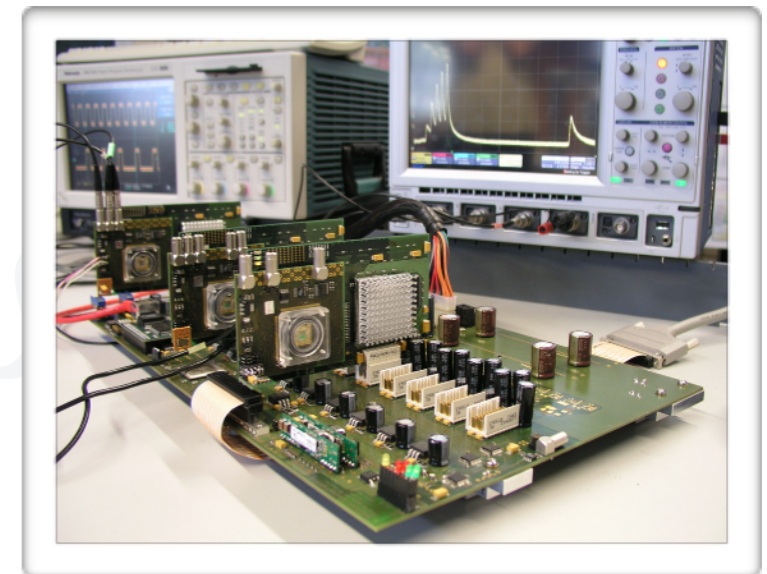
$$C_m \frac{dV_m(t)}{dt} = I(t) - \frac{V_m(t)}{R_m}$$

Calling TN neurons LIF seems to be a misnomer. They are far closer McCulloch-Pitts neurons and have none of the time-dependent capabilities of LIF neurons (aside from the persistent neuron potential).

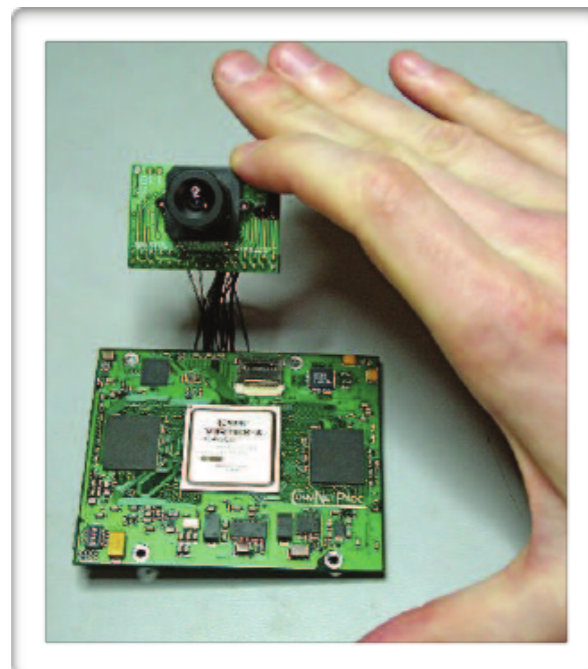


2013-18: [Neuromorph and Brainstorm](#) @ Stanford

2016: [Spinnaker 0.5M core machine](#)  
@ HBP/Manchester



2011: [Spikey](#) @ Heidelberg



2008: [NeuFlow](#)  
@ NYU (J. LeCunn)

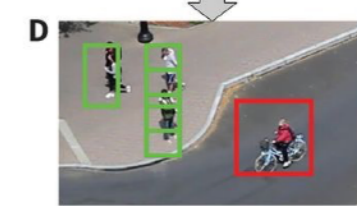
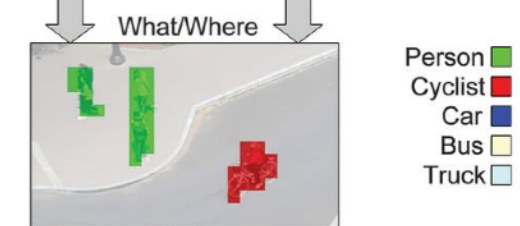
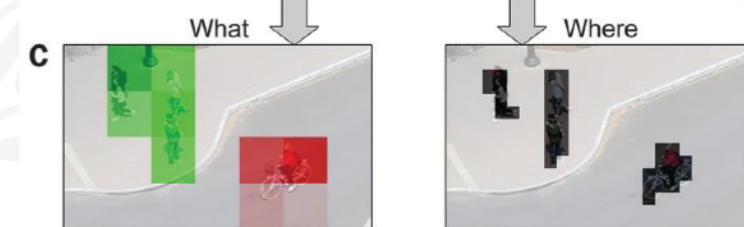
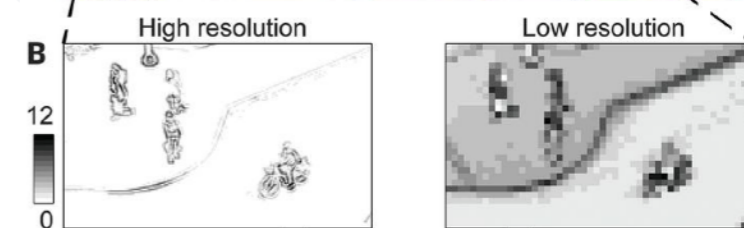
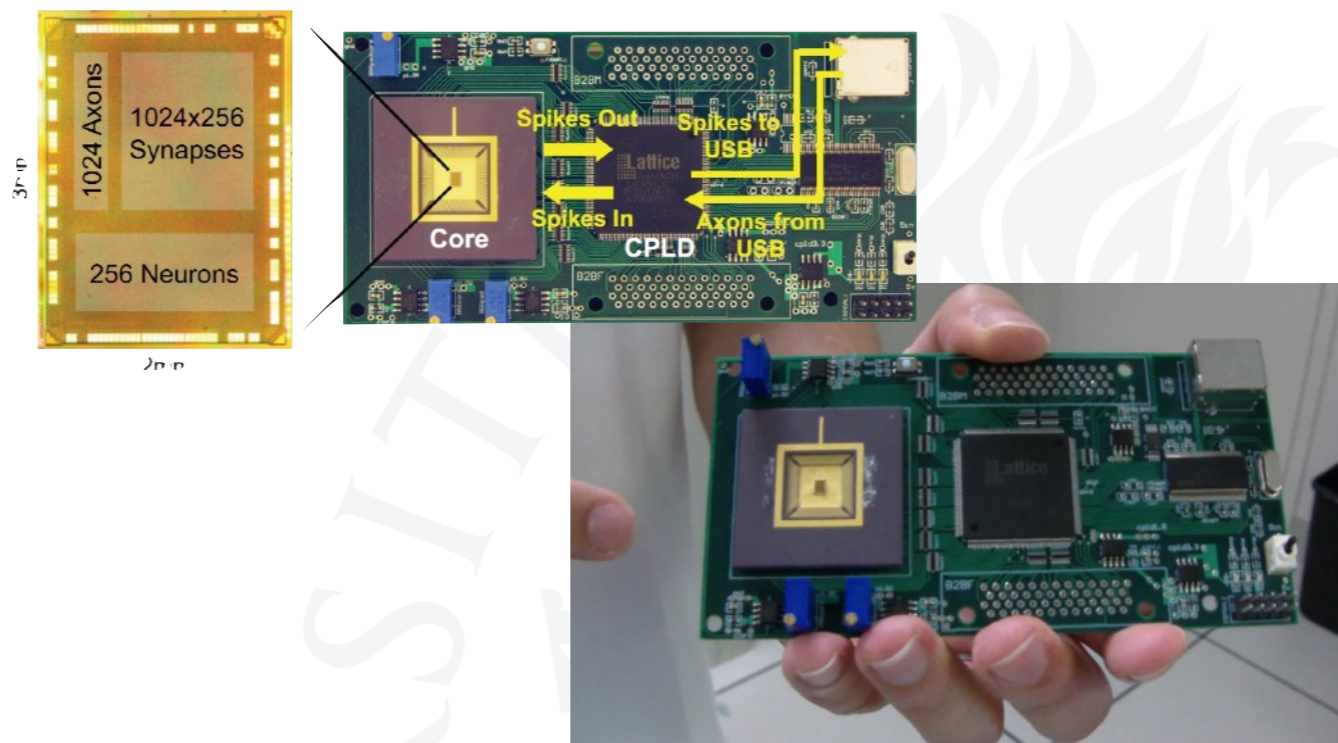
1. **Spinnaker**: scalable, low-power units
2. **Brainstorm**: NEF and populations of neurons
3. **Spikey**: analogue spikes, neuroplasticity, 10k speedup over real-time

**NeuFlow**: Conv. neural network on-chip. Not really neuromorphic.. but an example of NN hardware!

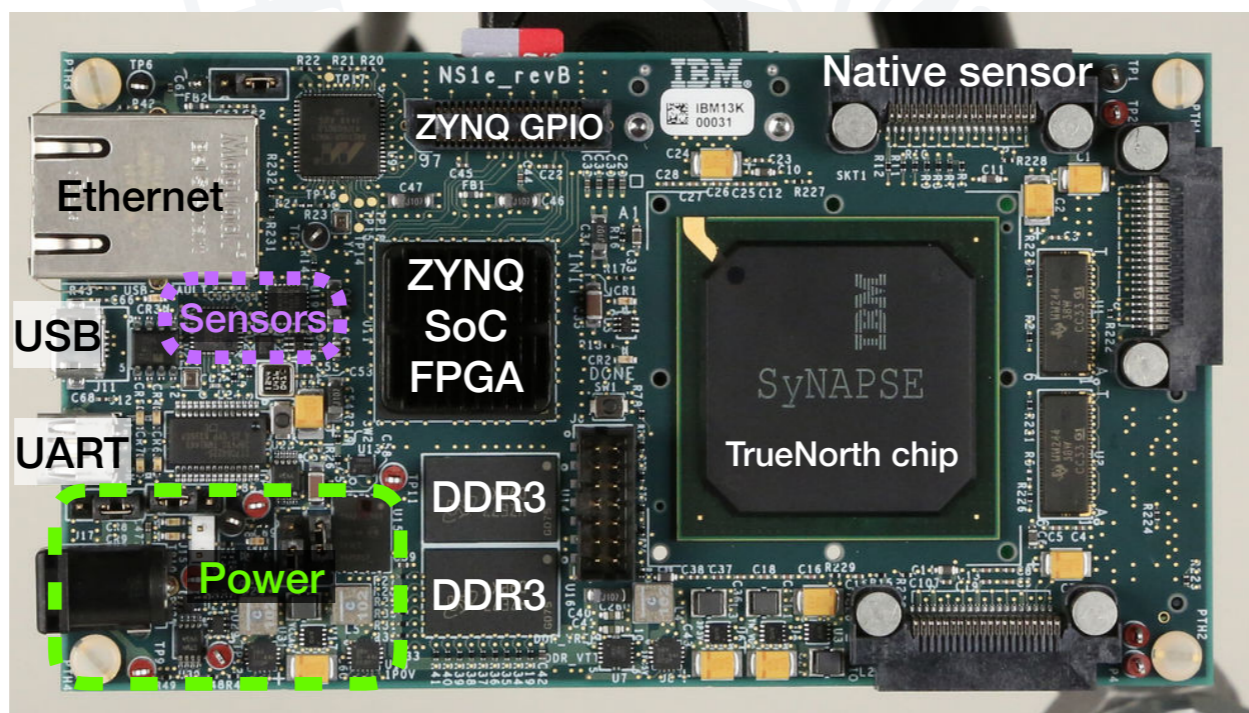
[More neuron-inspired computing at NICE 2016 \[link\]](#)



# 2011 - a single core demonstrator



# 2014/15 - TrueNorth chip to select institutes



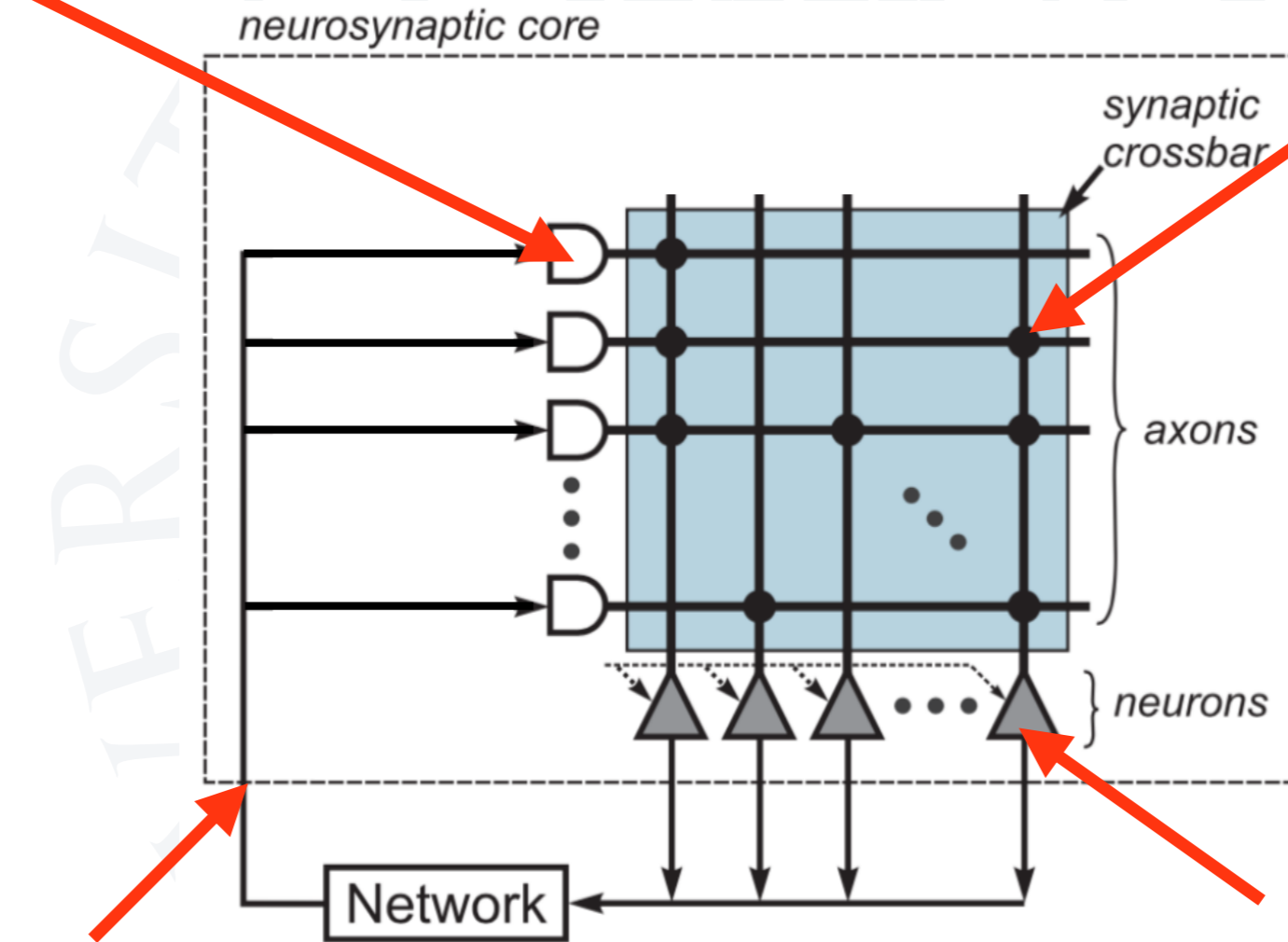
DARPA SyNAPSE: metric of one million neuron brain-inspired processor.





**Axons are the xbar input.  
Also provide a label  $\in \{1,2,3,4\}$**

**Synapses  $\in \{0,1\}$**

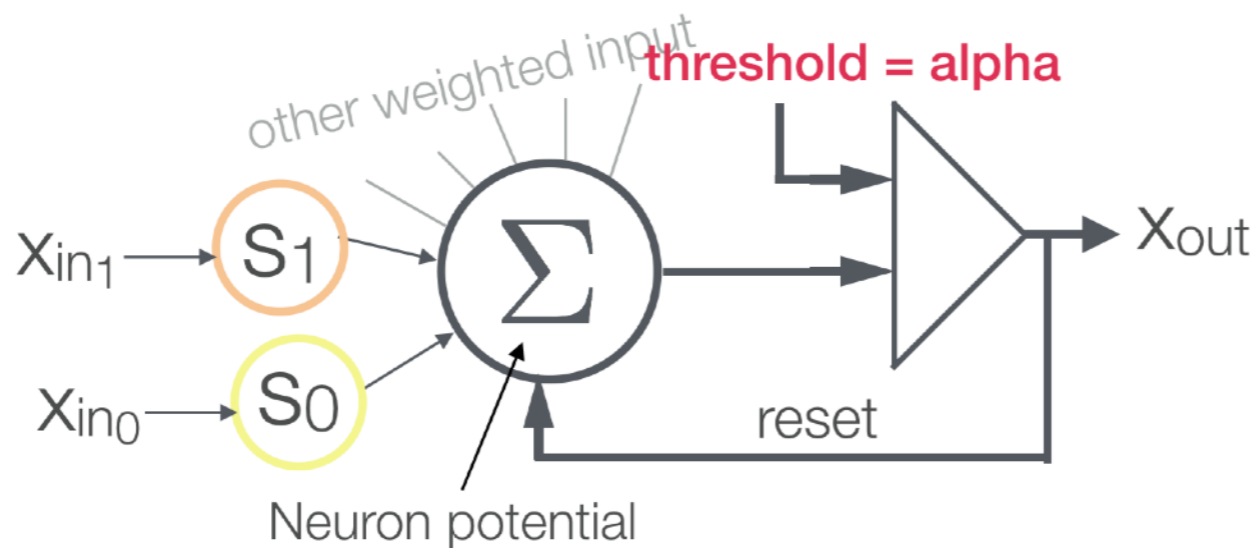
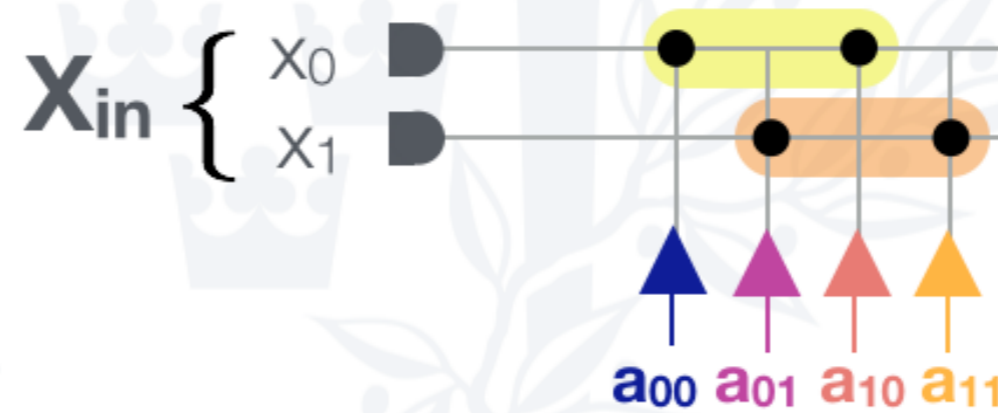


**Connections are between 1 neuron and 1 axon. Fan out/in requires multiple neurons/axons.**

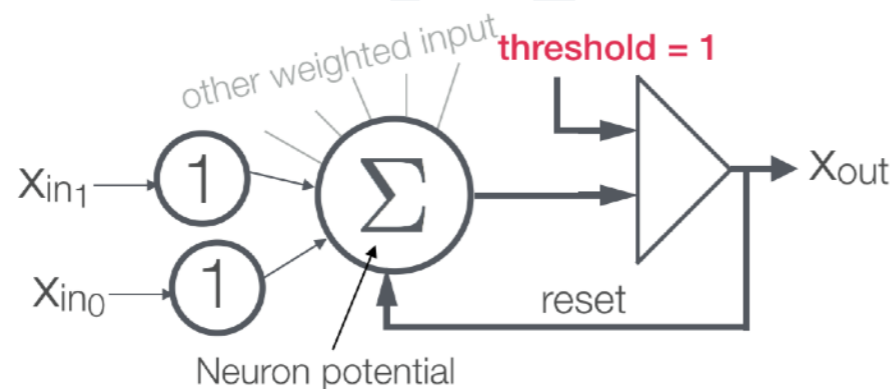
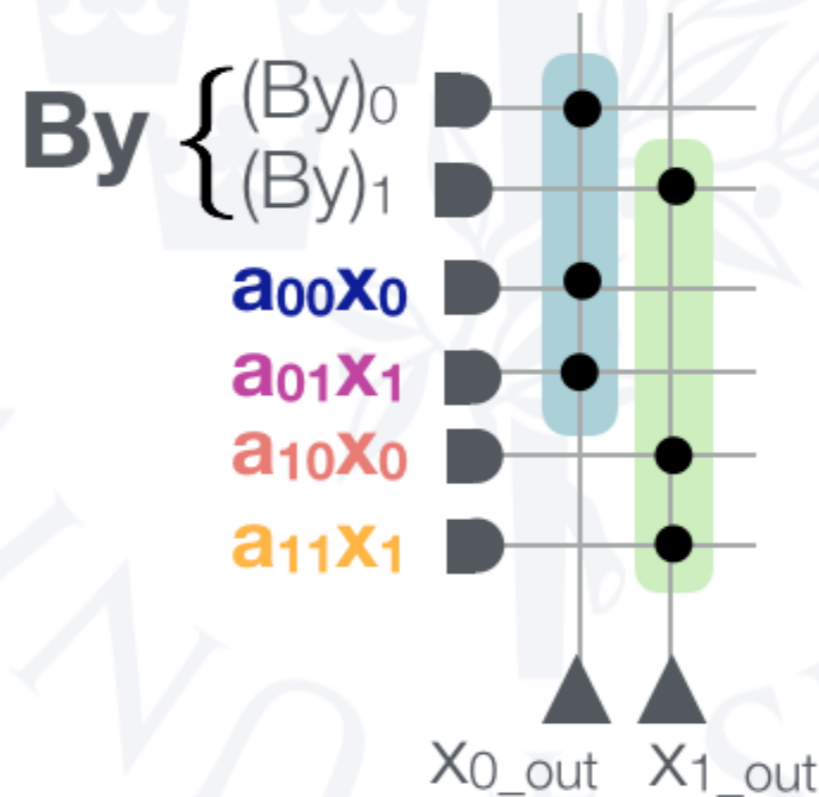
**Weights and threshold applied in neuron. A signed 8-bit weight can be saved per axon *label*.**

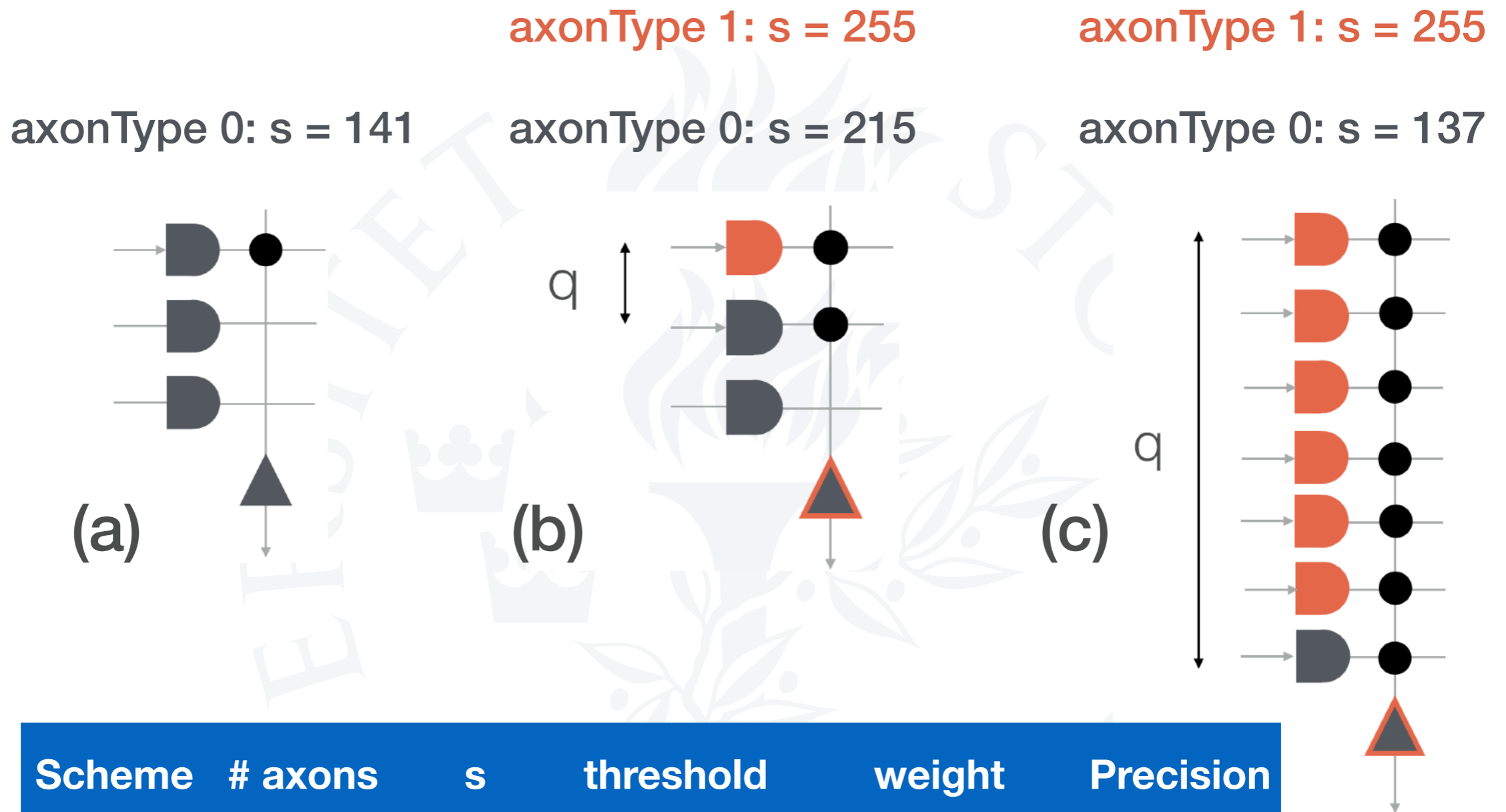


$$Ax = \begin{pmatrix} a_{00} & a_{01} \\ a_{10} & a_{11} \end{pmatrix} \begin{pmatrix} x_0 \\ x_1 \end{pmatrix} = \begin{pmatrix} a_{00}x_0 + a_{01}x_1 \\ a_{10}x_0 + a_{11}x_1 \end{pmatrix}$$

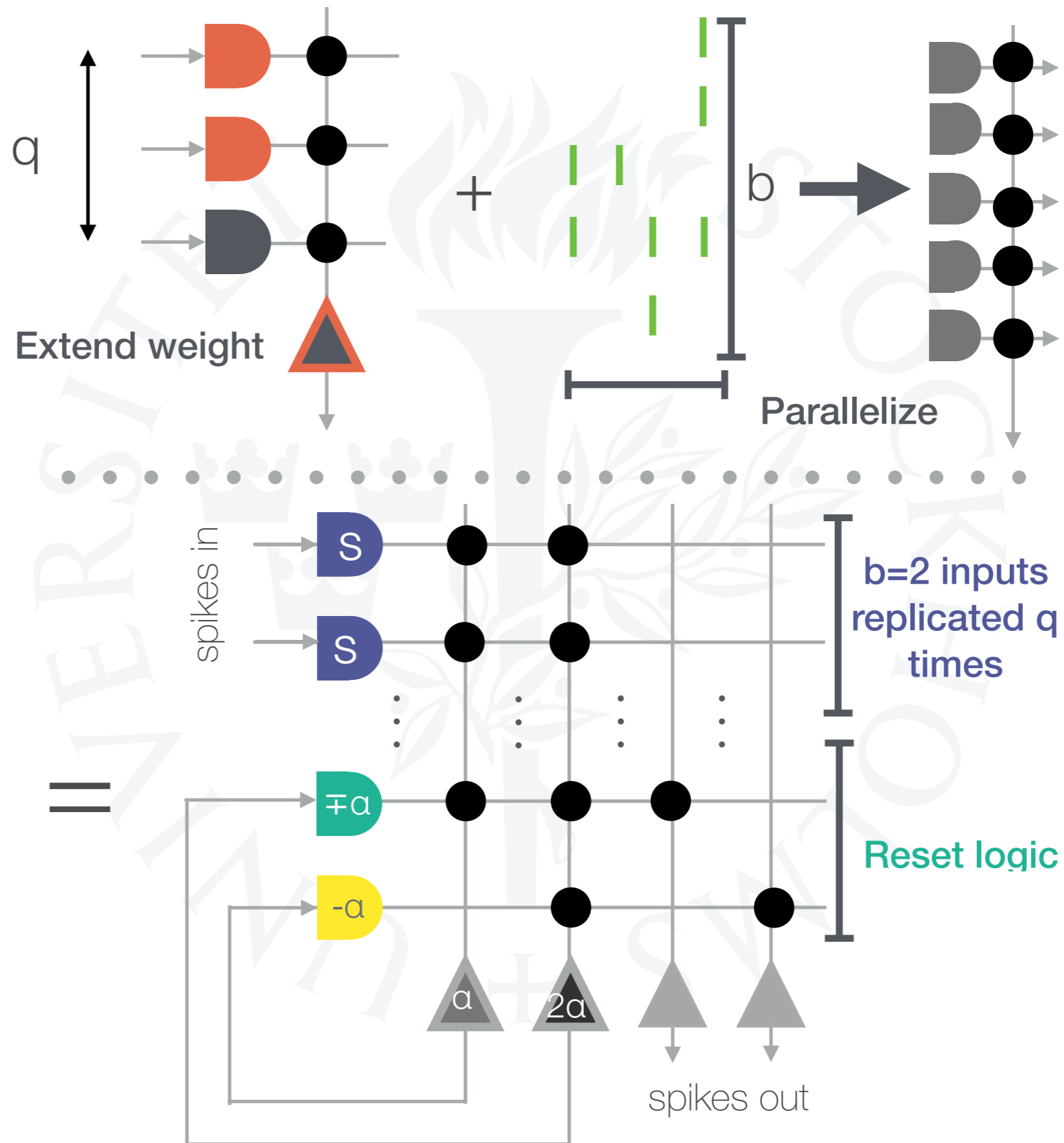


$$Ax + By = \begin{pmatrix} a_{00}x_0 + a_{01}x_1 \\ a_{10}x_0 + a_{11}x_1 \end{pmatrix} + \begin{pmatrix} (By)_0 \\ (By)_1 \end{pmatrix}$$



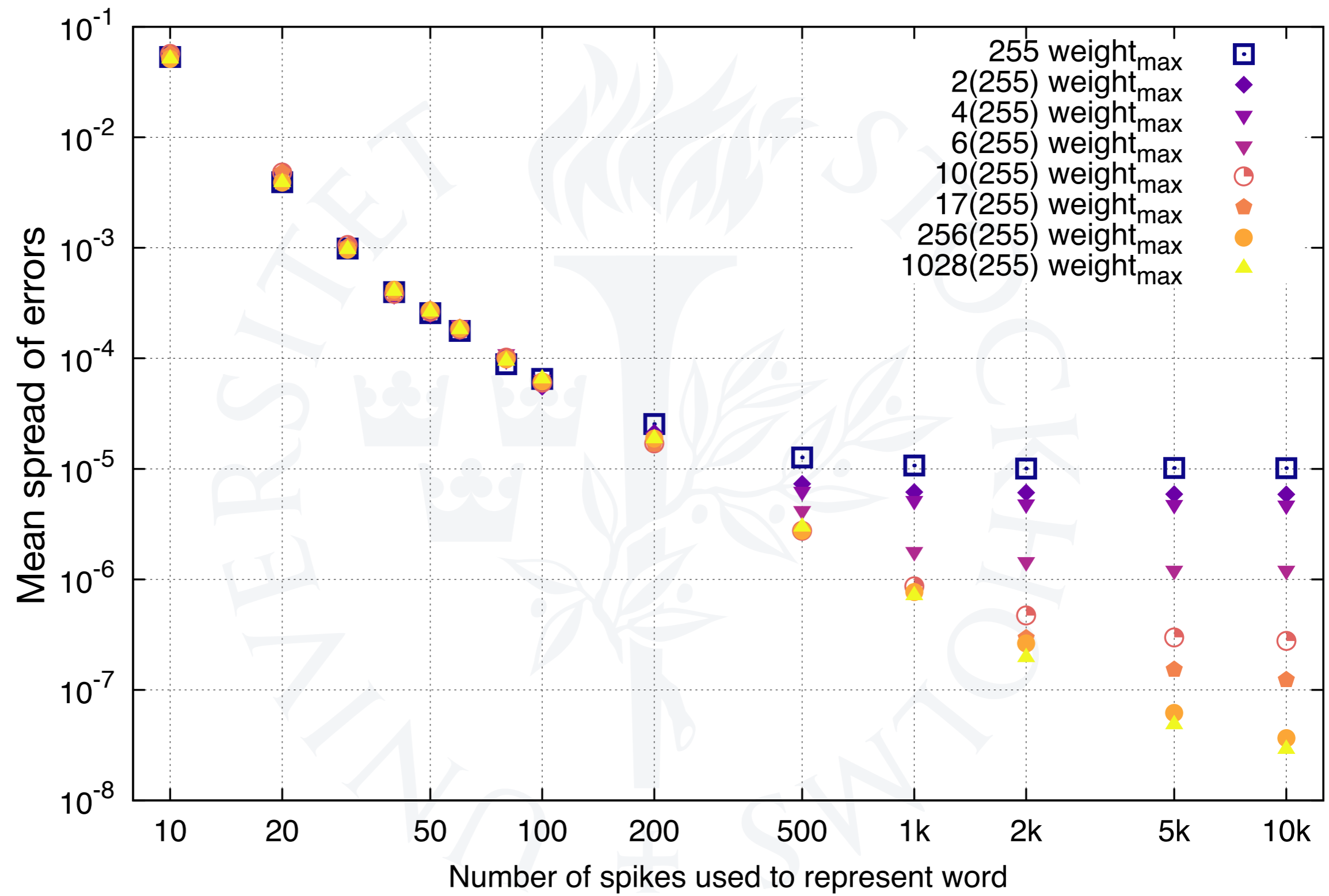


Scheme	# axons	s	threshold	weight	Precision
a	1	45	49	0.91837	3 s.f.
b	2	251	306	0.91830	4.s.f.
c	7	1922	2093	0.91829909	7.s.f.





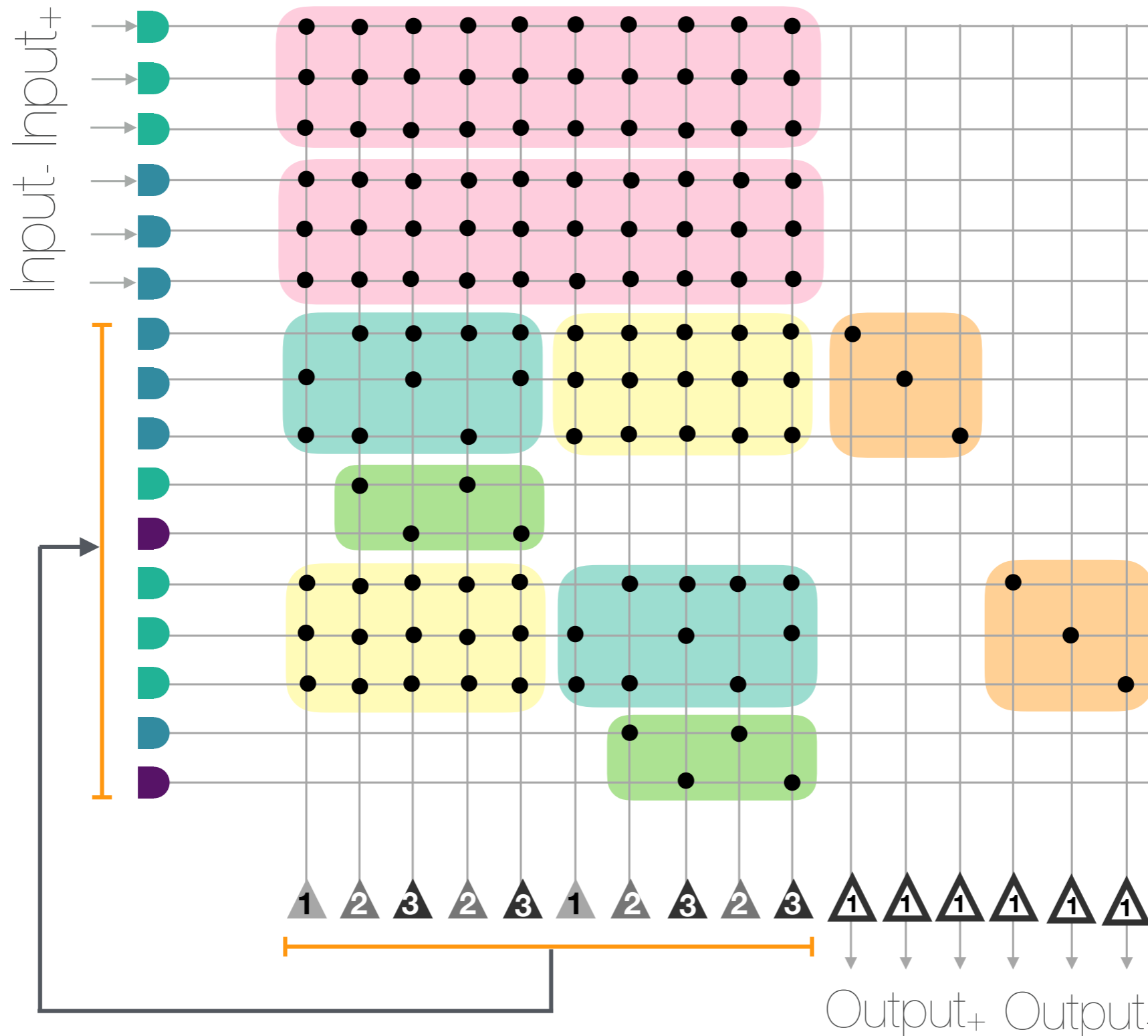
Effect of word size on MSE between TN KF and ideal KF for 1D projectile



A compromise must be struck between accuracy and speedup: necessary because of the chips design and the way information is encoded



Subtitle



- axon label 0
- axon label 1
- axon label 2
- {1,-1,0}
- {1,-1,1}
- {1,-1,2}
- {0,1,0}

- $-\alpha$  reset synapses
- $+(n-1)\alpha$  reset synapses
- output synapses
- multiplication synapses
- Pos/neg balance reset



# ITK & FE65-P2

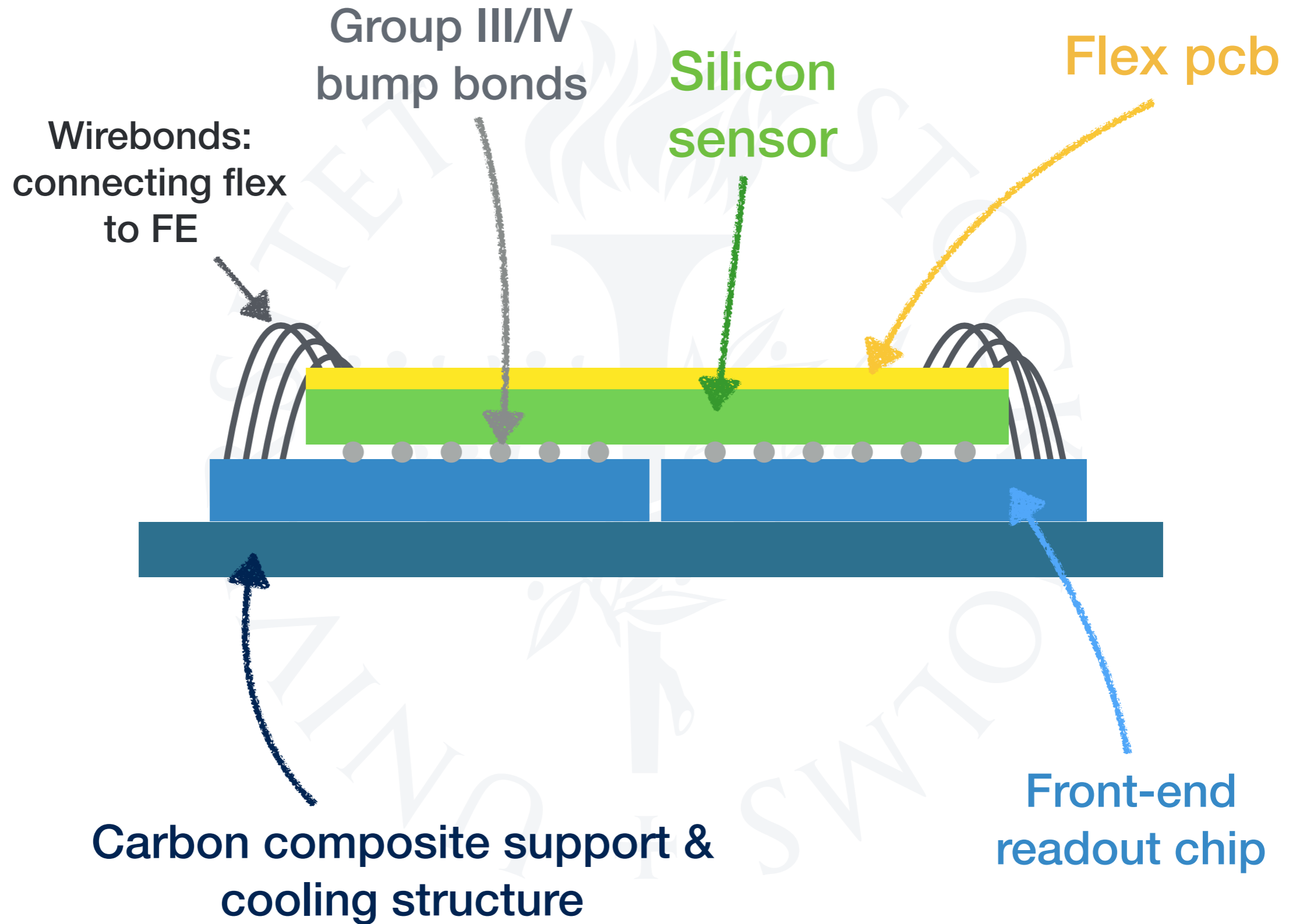


Conditions for ITk will be challenging...

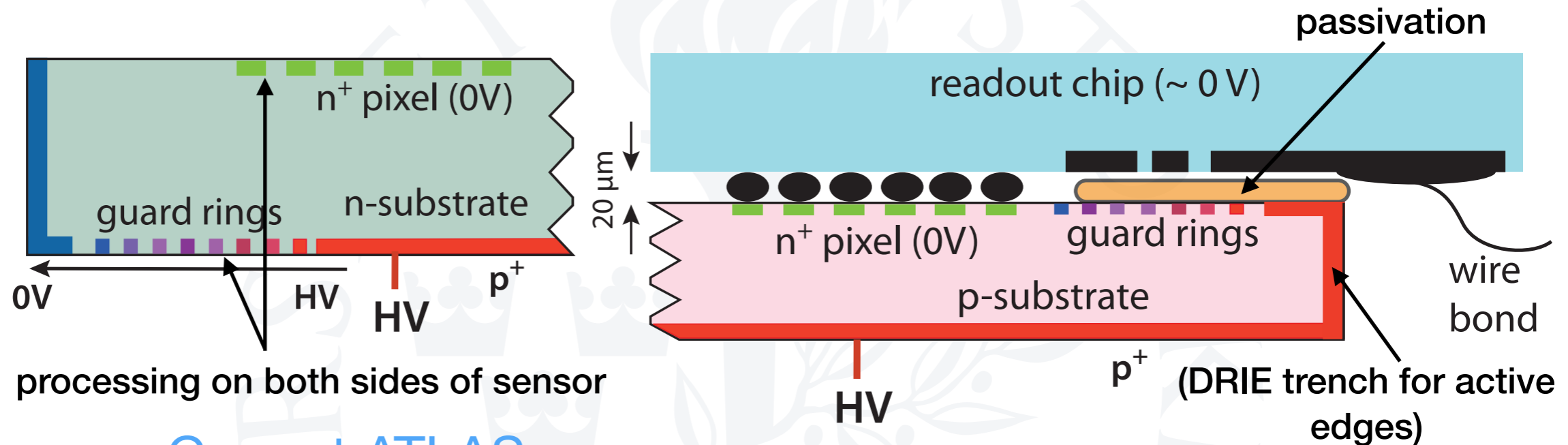
	ITk Pixel: innermost layer (flat modules)	Run 2: IBL	Difference
Lifetime target integrated lumi.	2000 [fb <sup>-1</sup> ]*	300 [fb <sup>-1</sup> ]	x7
Lifetime fluence	1.3e16 [neq/cm <sup>2</sup> ]*	5e15 [neq/cm <sup>2</sup> ]	x2.6
TID	7 [MGy]*	2.5 [MGy]	x2.8
<Hits/chip/bc at target PU>	223 @ PU 200	27 @ PU 75	x8
<Density of hits>	0.6 [mm <sup>-2</sup> ]	0.08 [mm <sup>-2</sup> ]	x8

\*assuming replacement after 2000 fb<sup>-1</sup>





- **n-in-p:**
  - ◉ single-sided processing: easier/cheaper to produce
  - ◉ needs passivation between FE chip and sensor



Current ATLAS  
Pixel sensors

ITk Pixel Sensors

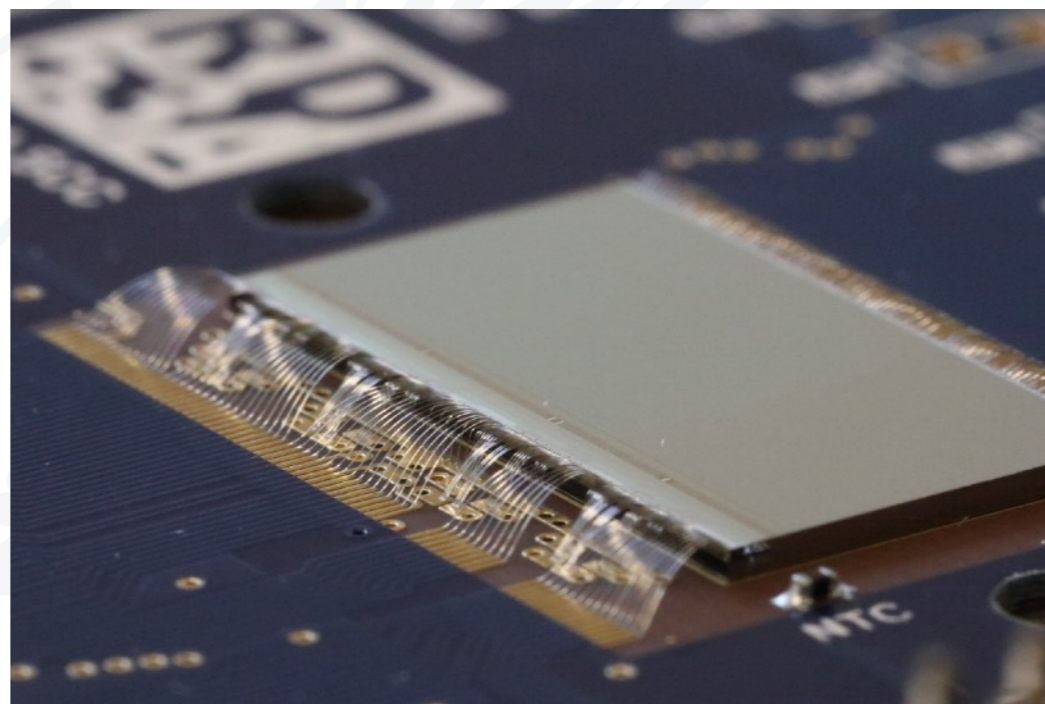
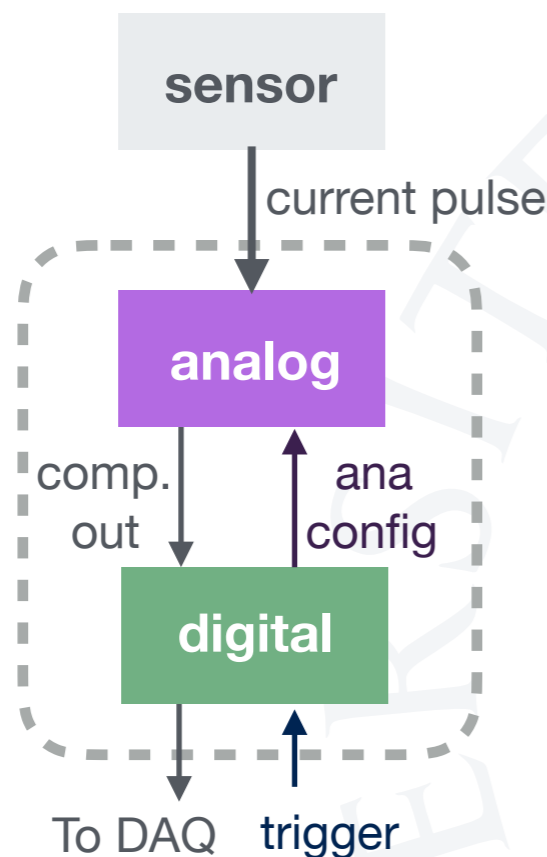
- **Reduced thickness:**
  - ◉ better rad-hardness,
  - ◉ difficult to handle, > cost
  - ◉ current Pixel sensors: 200-250 μm

ITk Layer	Sensor thickness [μm]
0	3D sensor
1	100
2	100-150
3	150
4	150-300
EC	200





- analogue portion: integrates collected charge, amplifies
- digital portion: digitizes signal, stores till trigger, sends hit info to DAQ



RD53A

ITk FE  
(RD53B)  
Coming soon

- Complex requirements & high hit rate = **smaller CMOS**.
- Equivalent rad-hardness, logic density @ 65nm is 3x of 130nm.
- Small test-chips to understand 65nm produced in 2015-2016.
- RD53A produced at end of 2017. Tests finalizing ana-FE and digital core used in final ITk chip are ongoing. **Current under review.**

<https://twiki.cern.ch/twiki/bin/view/RD53/RD53APublicPlots>



<Hits/chip/bc>  
TID  
# pixels/chip

223 @ PU 200  
7 [MGy]  
153,600

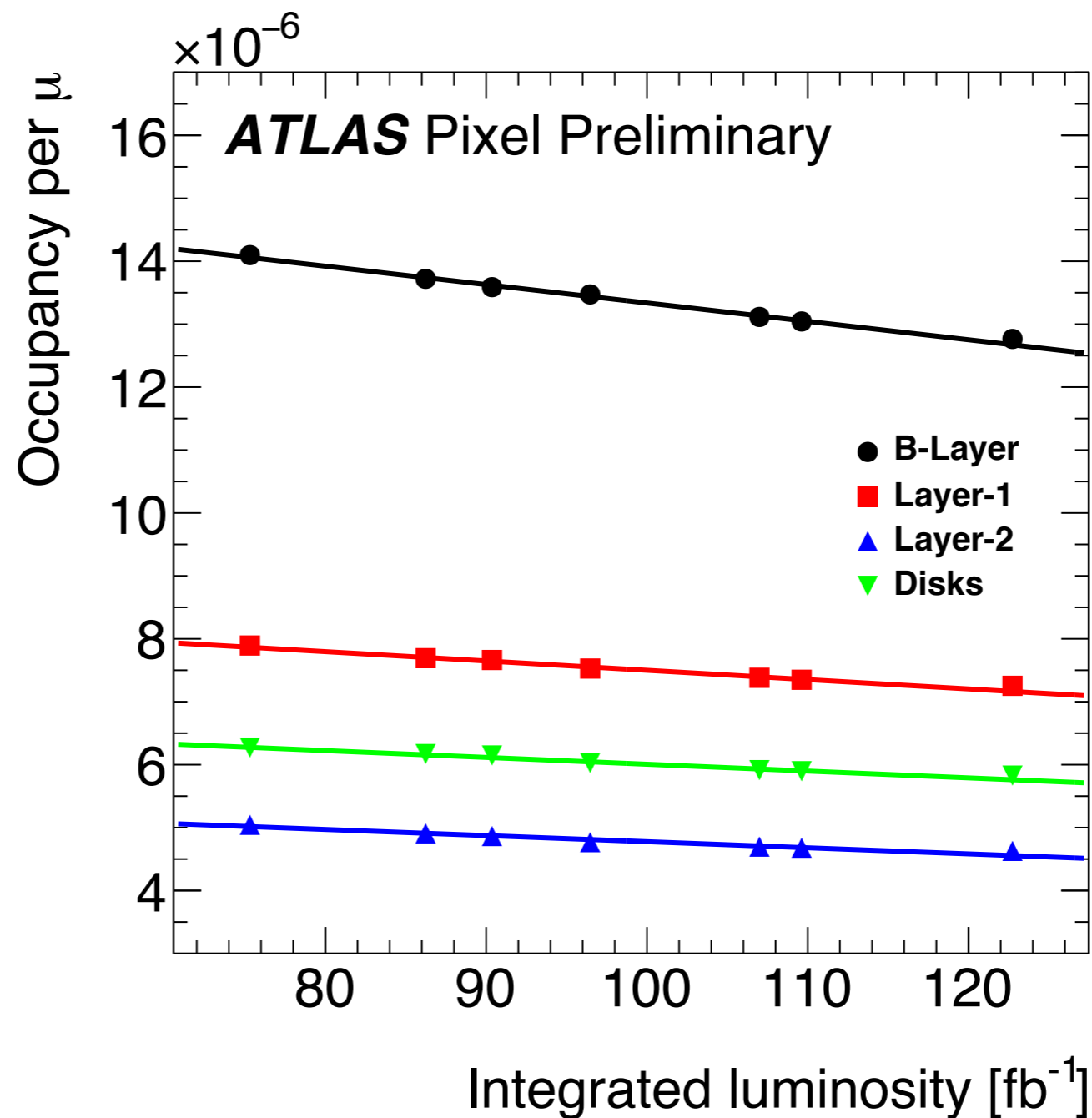
8 x Run 2  
2.8 x Run 2  
5.7 x Run 2

# Radiation Damage Simulation





Less hits per pixel module as detector ages

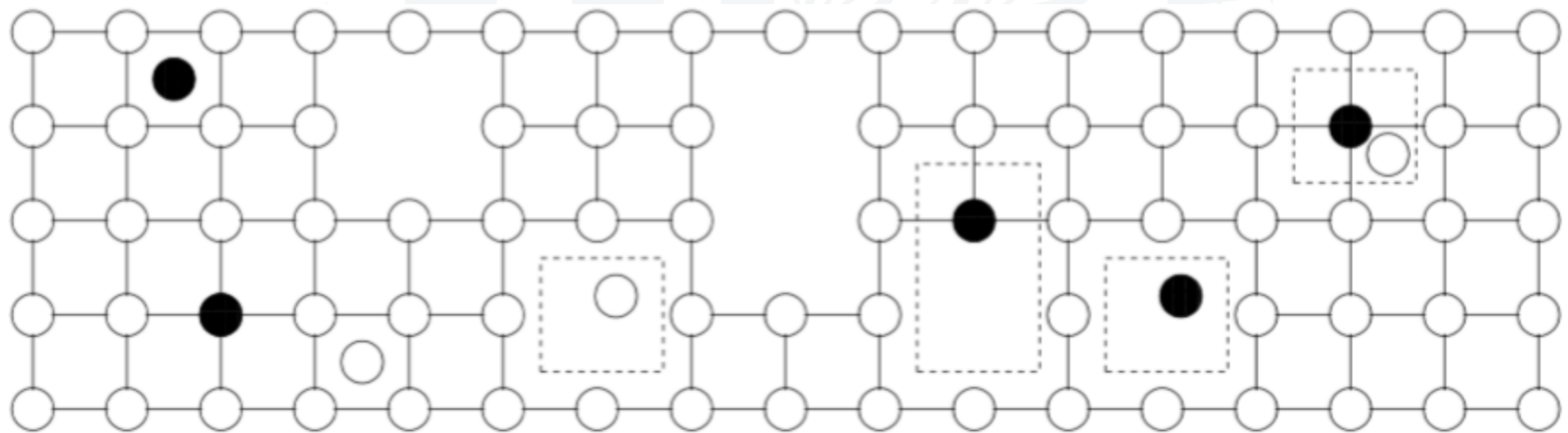


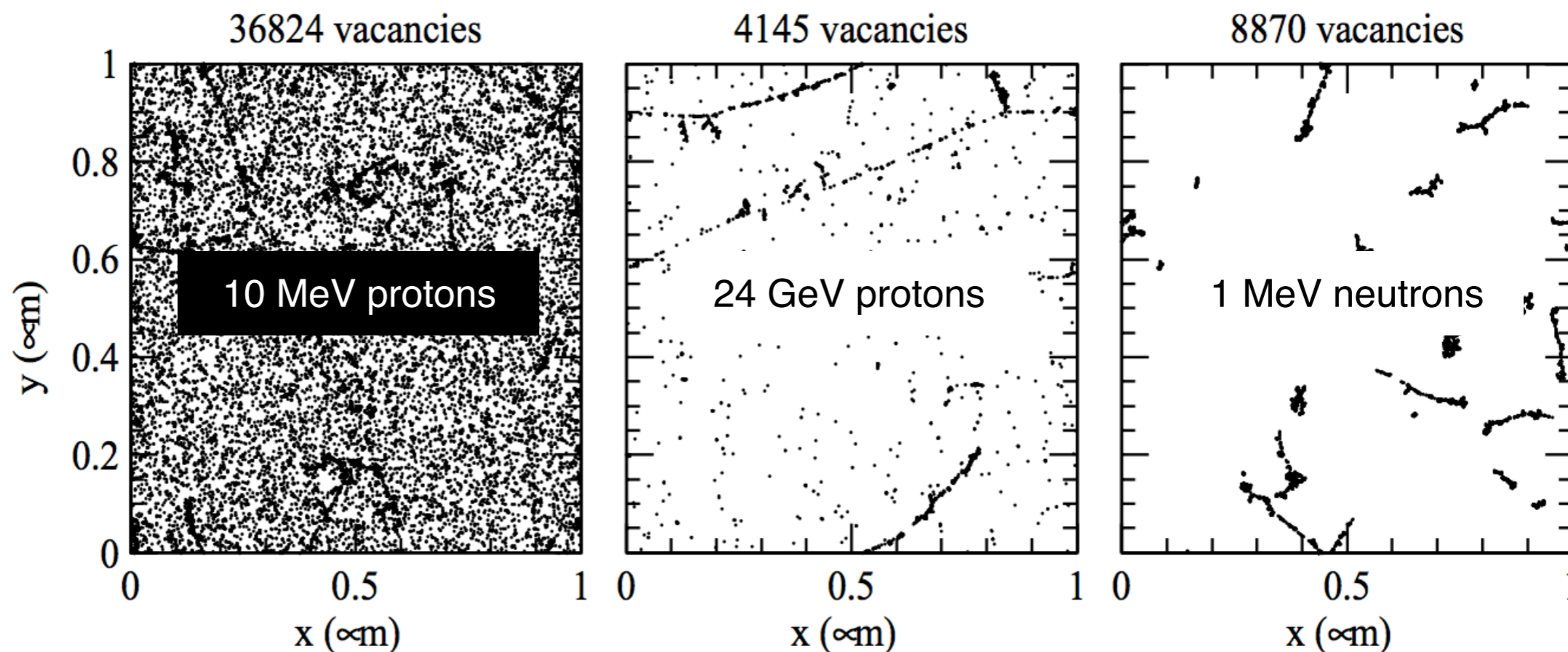
<https://atlas.web.cern.ch/Atlas/GROUPS/PHYSICS/PLOTS/PIX-2017-008/>

Pixel sensor occupancy decreasing with integrated lumi.  
Detector performance changes with time (fixed threshold)  
Need to understand these effects and how they change our reconstruction efficiency



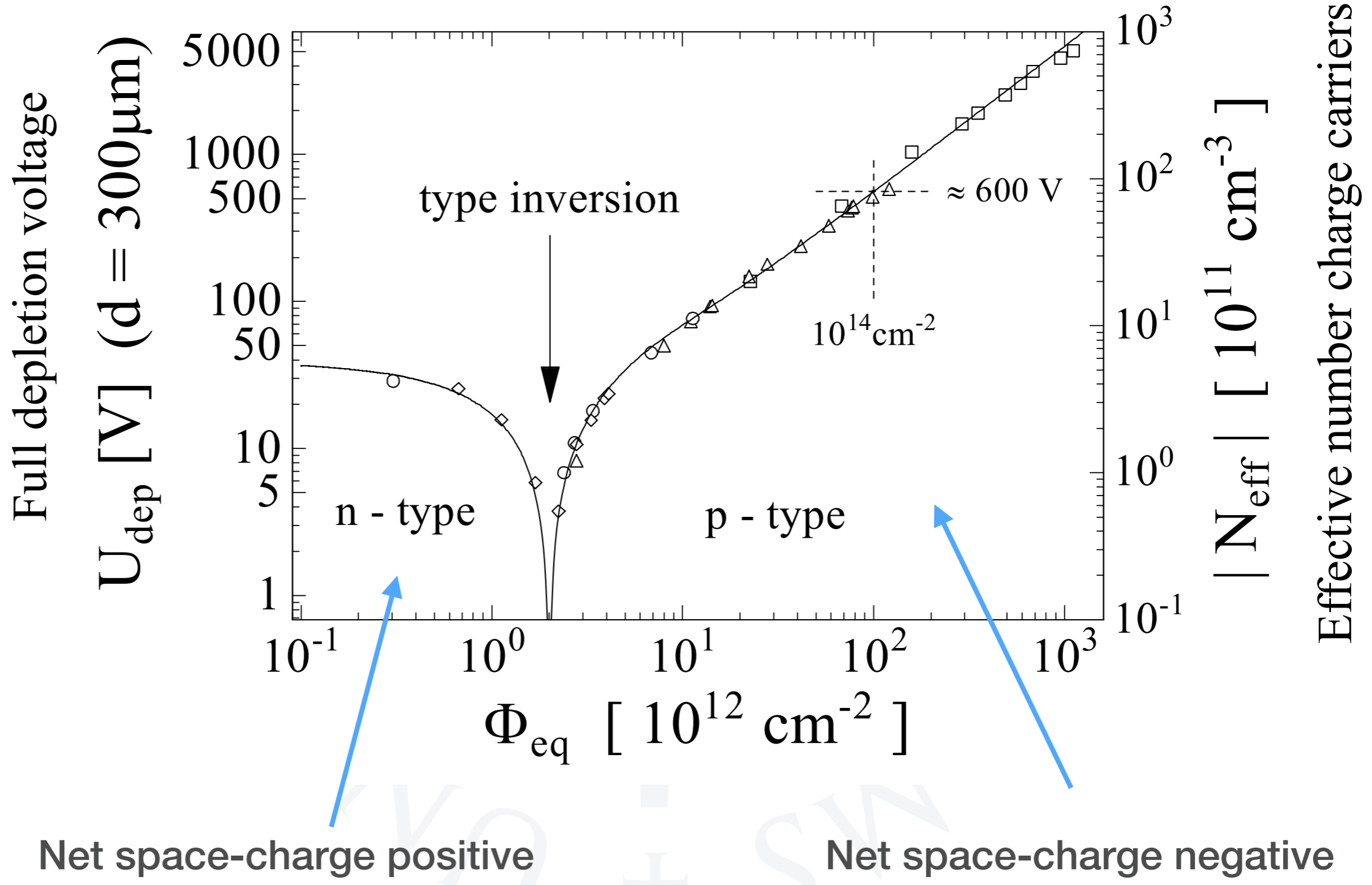
- As particles pass through the detector material they can interact with it.
- Non-Ionizing Energy Loss (NIEL) damages sensor bulk
- Deformation of lattice introduces energy levels to silicon band-gap.
- Some of these levels can act as traps





- The cluster deformations introduced by 24 GeV protons are very different to those introduced by 10 MeV protons.
- 24 GeV protons make comparable damage to 1 MeV neutrons.
- As such they have a hardness factor of 24.

## Space-charge sign inversion

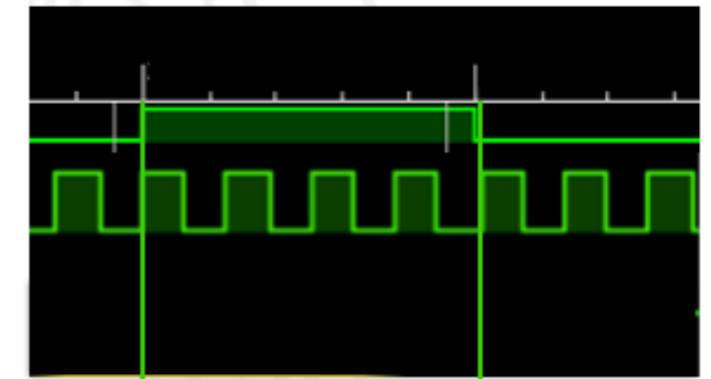
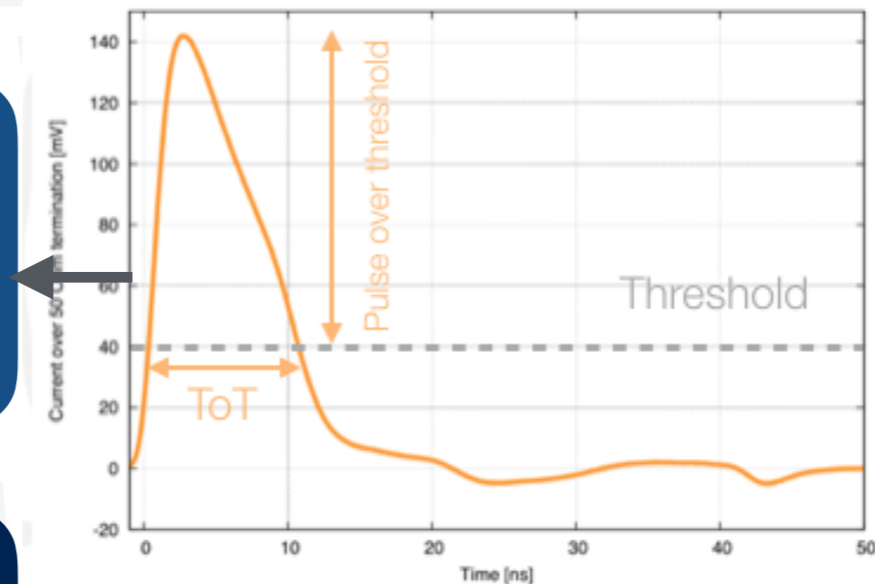
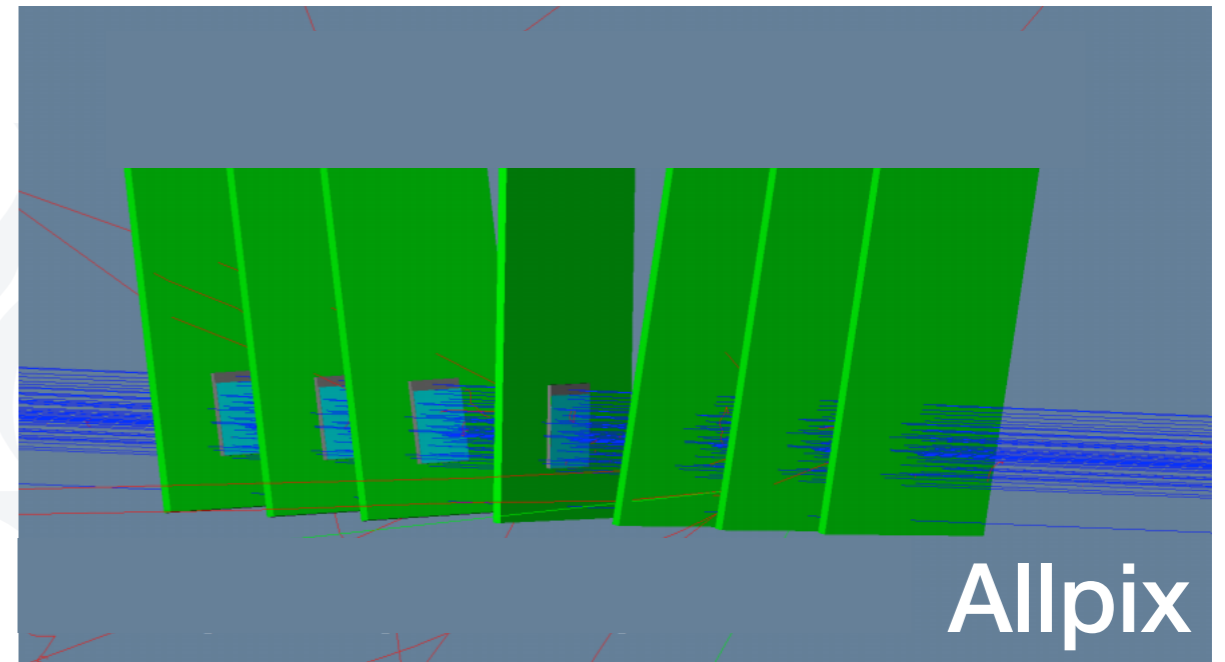
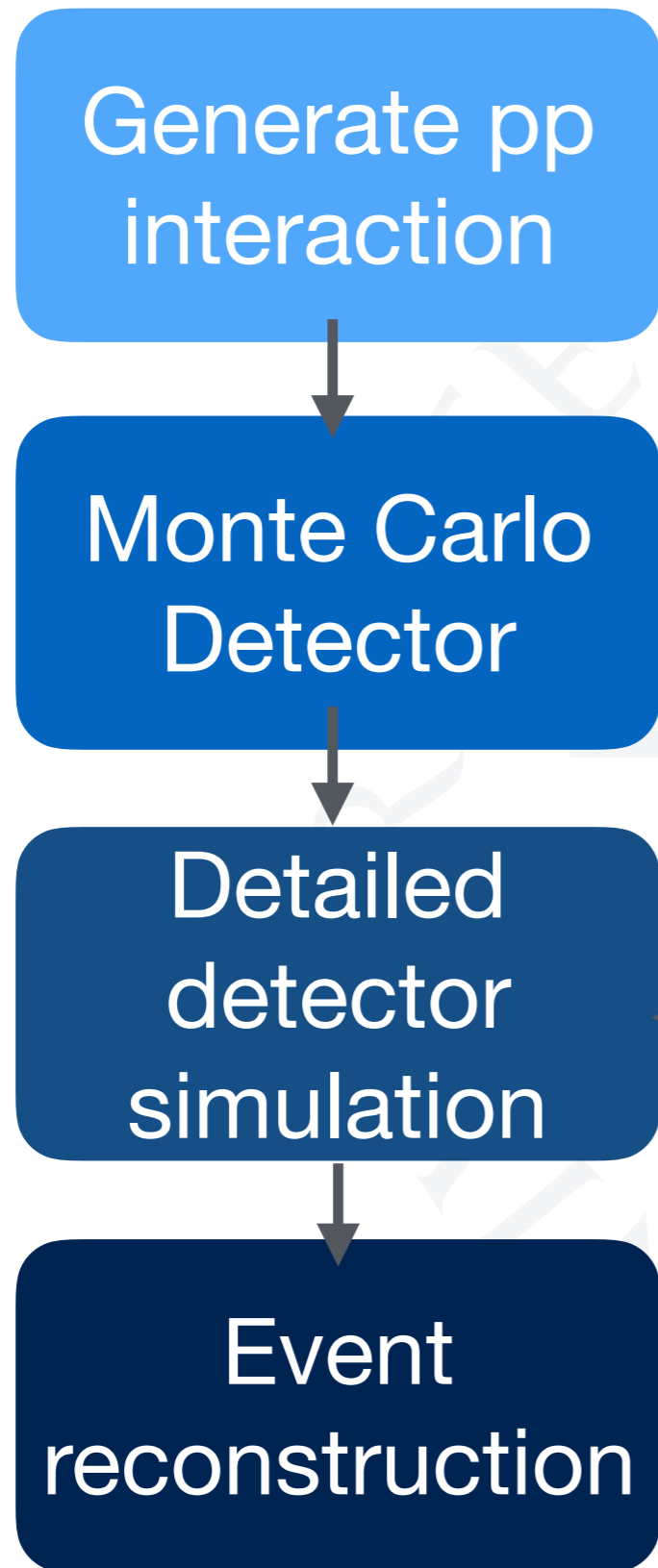


Net space-charge positive

Net space-charge negative



Alpix → Athena

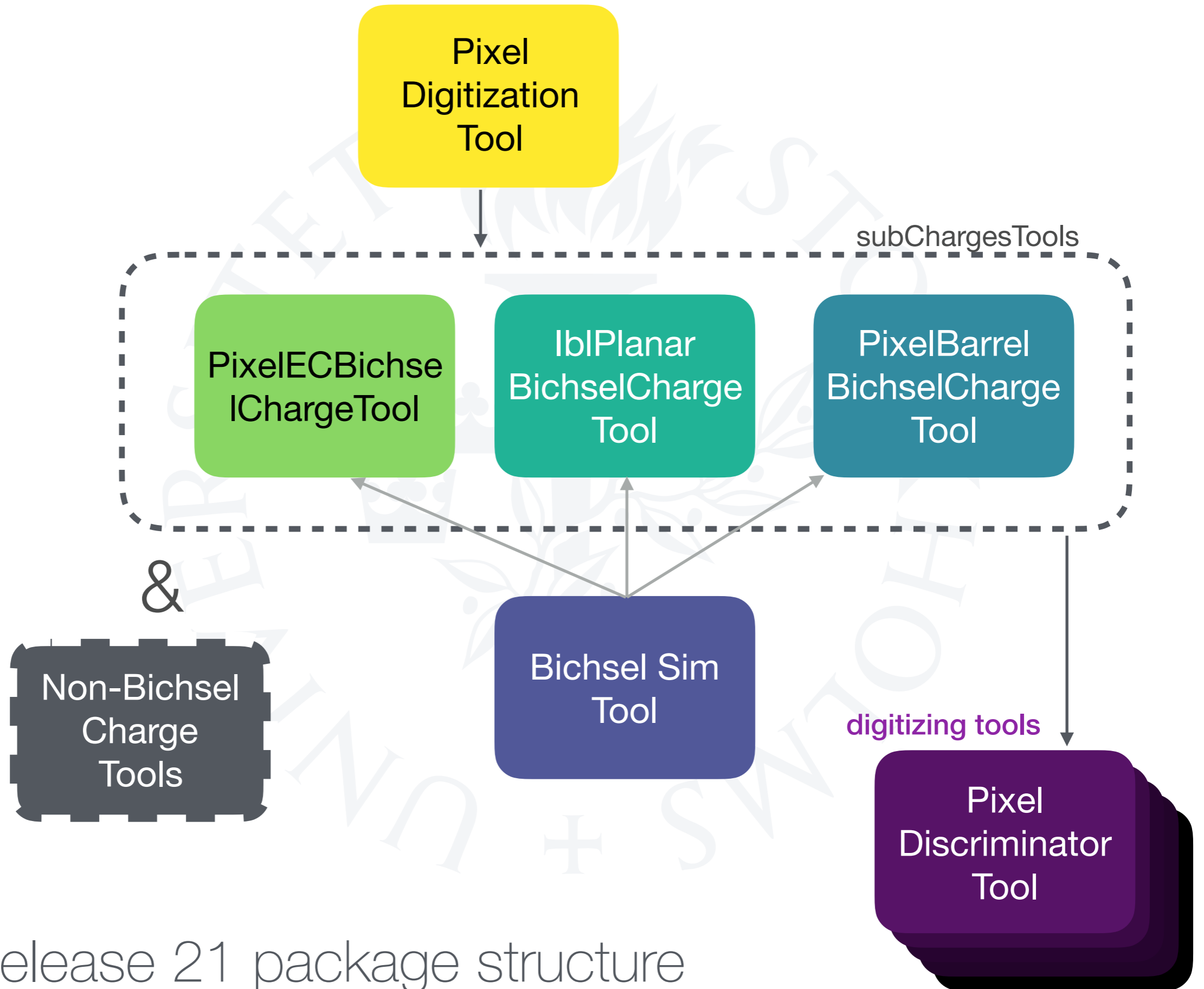


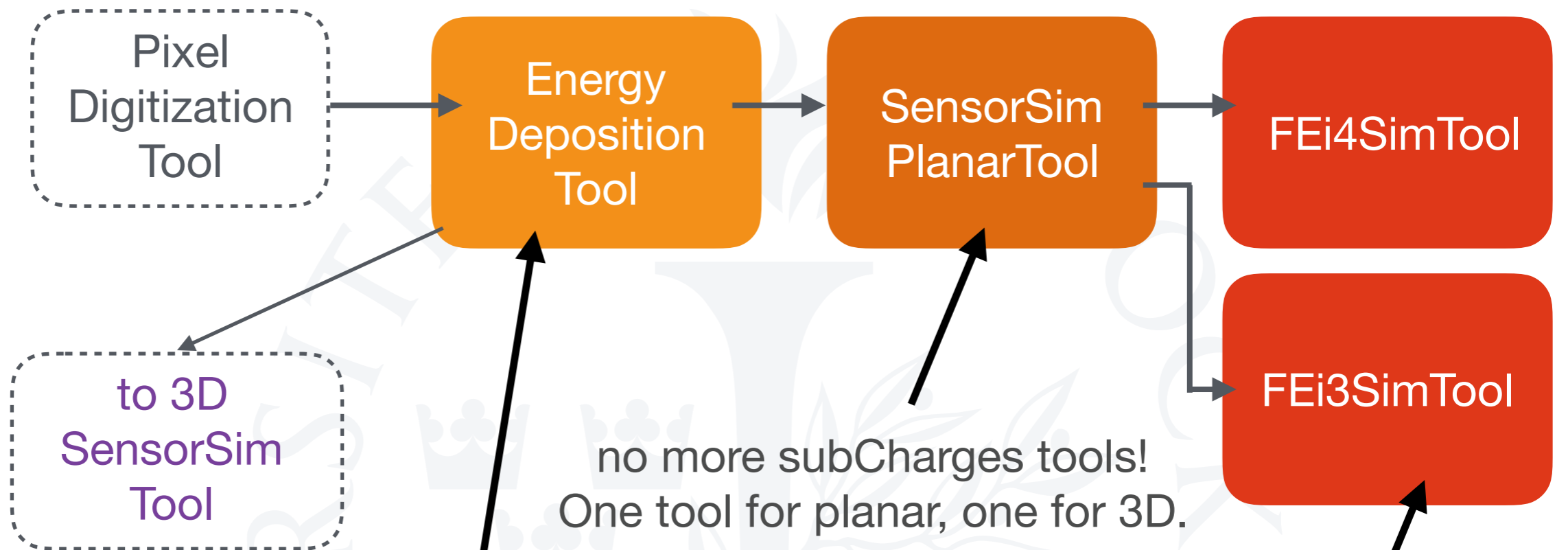
Analogue

Digital

**My task:** implementing simulation in ATLAS framework



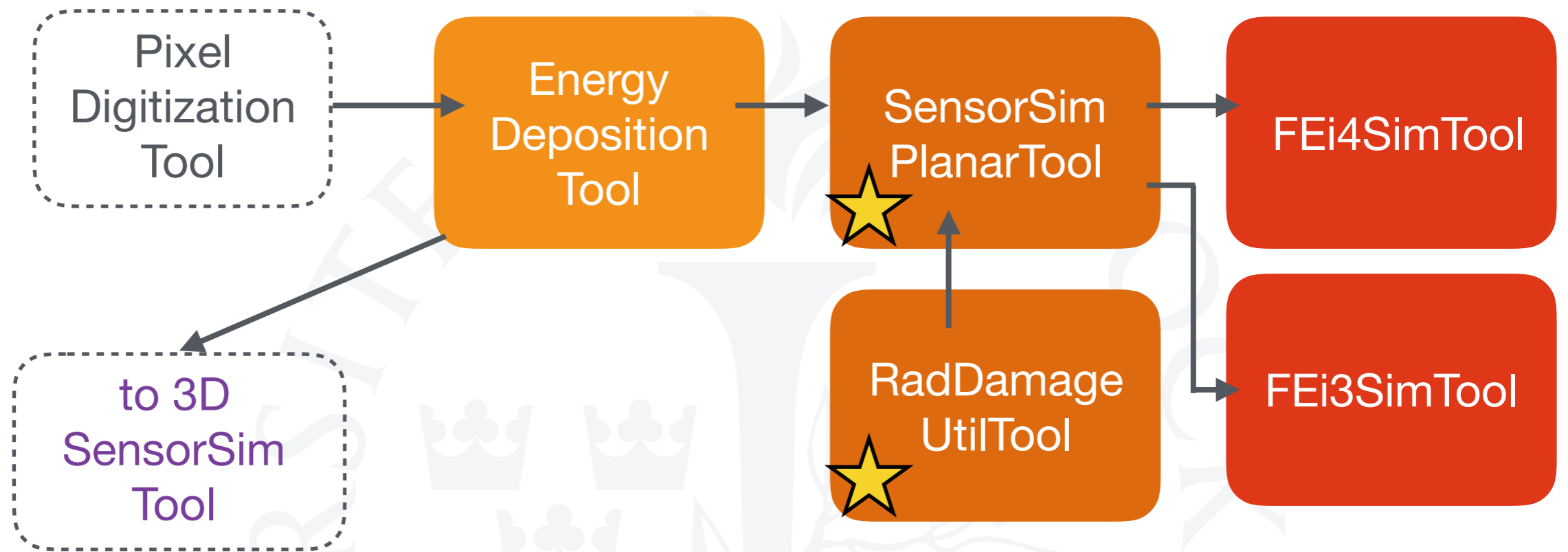




all Bichsel simulation and depositing energy across sensor bulk now done here

collection of 'digitizing tools' has become one class per FE!



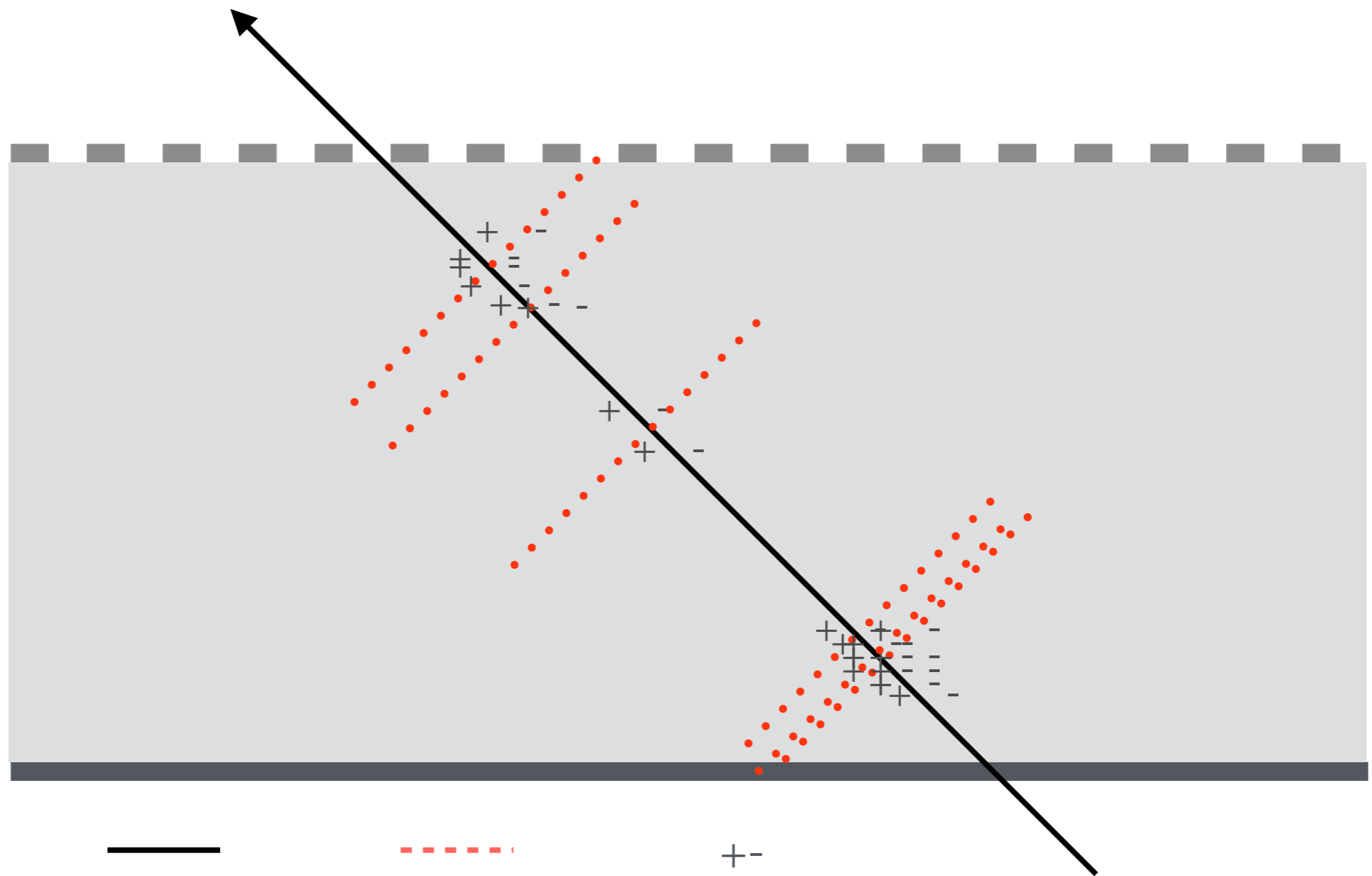


★ new things

- One tool added (for neatness), sensorSim loop modified for trapping simulation
- Details of simulation strategy in paper: <https://cds.cern.ch/record/2255825>



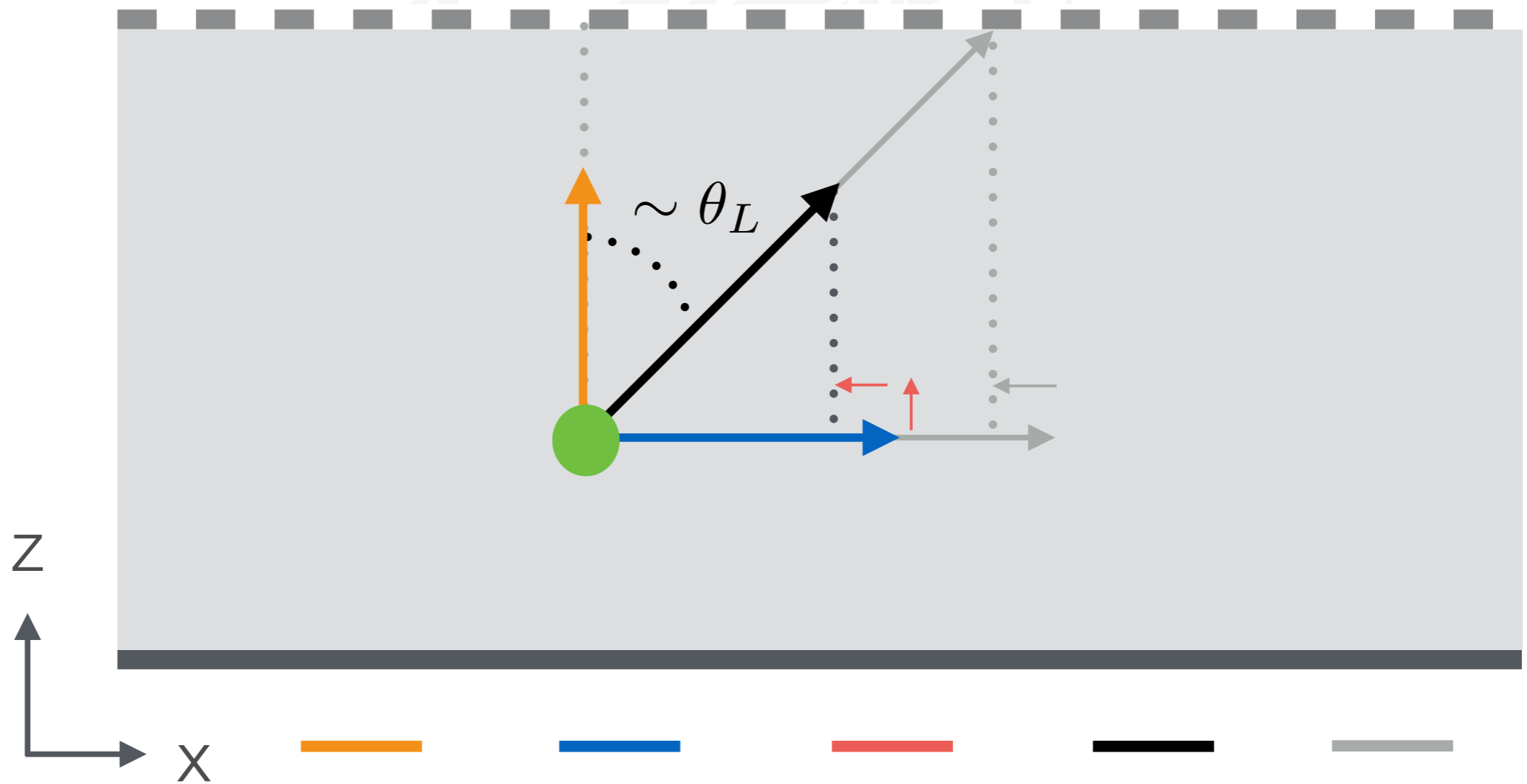
charged particle (betaGamma from G4)



— Particle trajectory  
- - - Collisions  
+- Charge carrier pairs

**Pre-existing:** energy deposition, random collisions

(1) Is the charge trapped? (2) Where is trap? (3) What charge is induced?



				
Drift component	Lorentz component	Diffusion component	Overall Movement	Without trapping

**New functionality:** charge trapping, electric field shape

Fraction of charge carriers NOT trapped after time 't'

$$\frac{N(t)}{N(0)} = e^{-\frac{t}{\tau}} = e^{-\beta\Phi t}$$

Characteristic lifetime

Beta is the 'damage parameter', Phi is the neq fluence

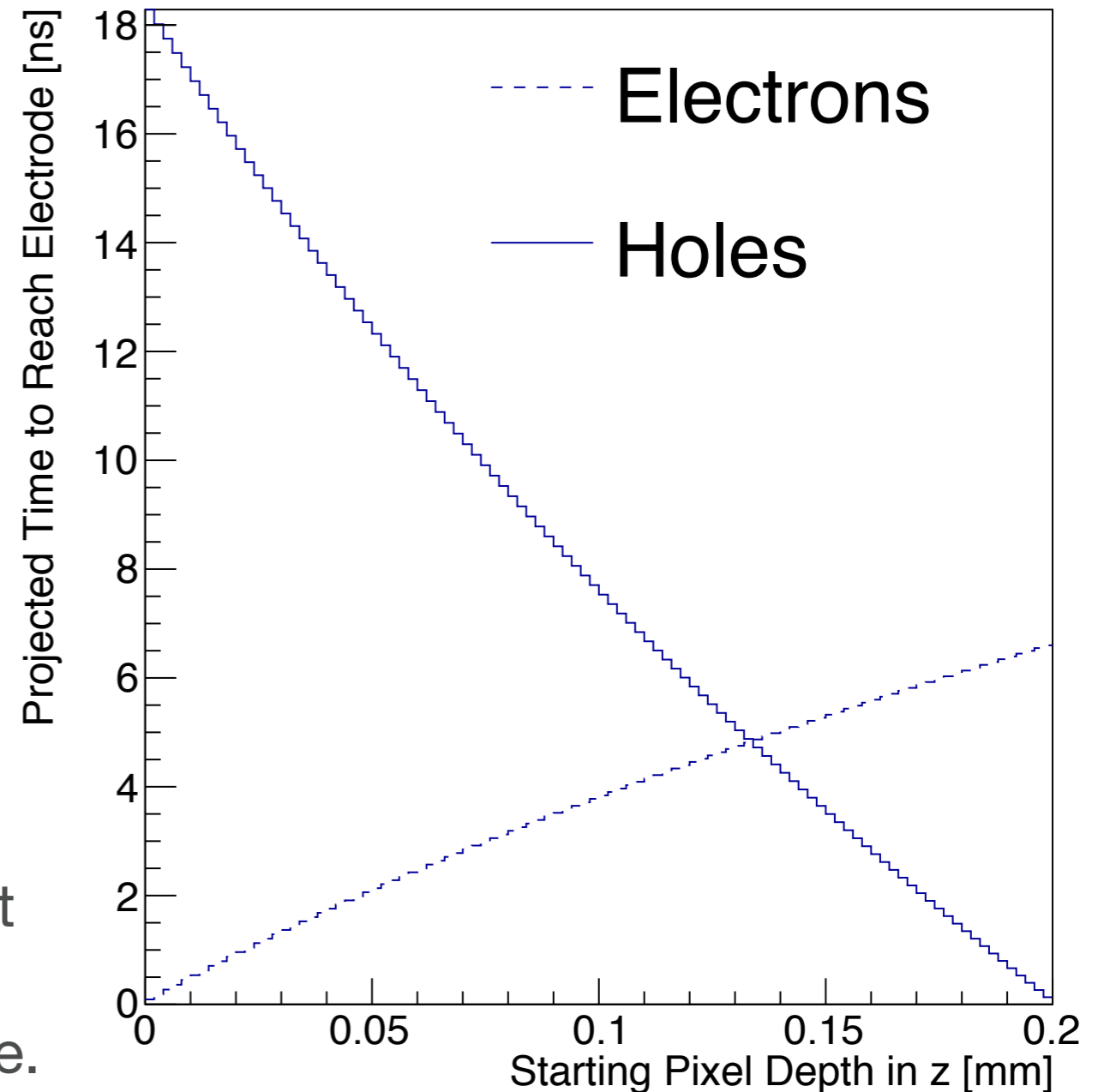
Is it trapped?

Random number thrown from a uniform distr.

$$t = -\frac{1}{\beta\Phi} \ln(u)$$

Trapping time

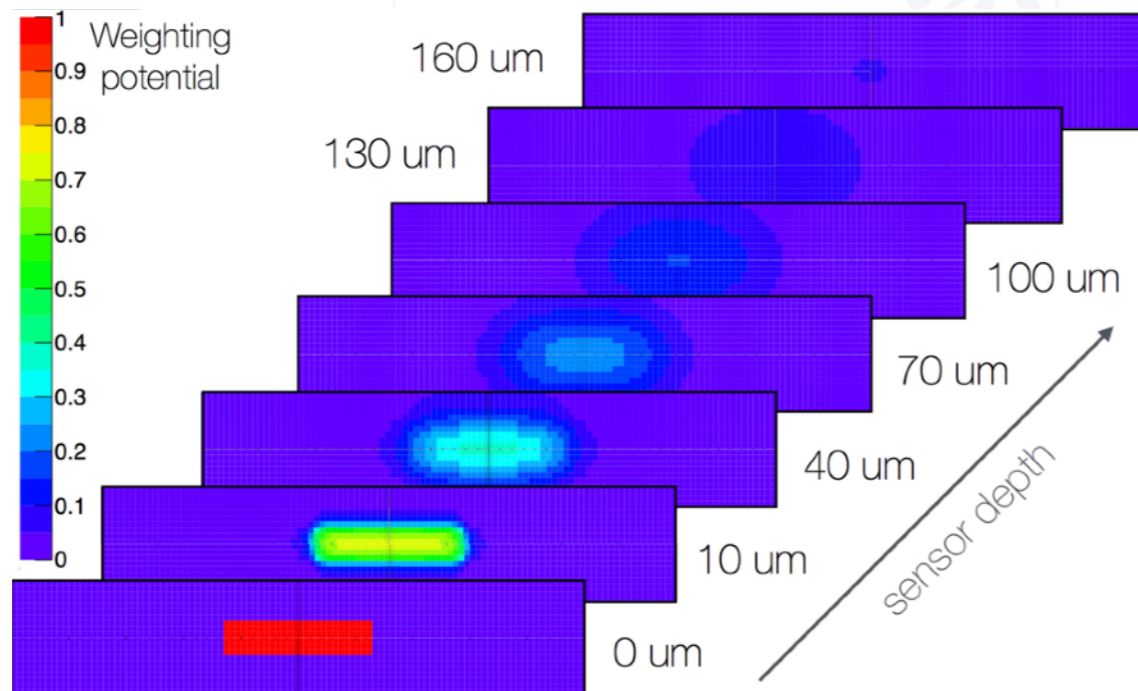
- o t is dep. on fluence
- o Throw rand. number to get trapping time.
- o Compare to collection time.



Find trap depth in bulk

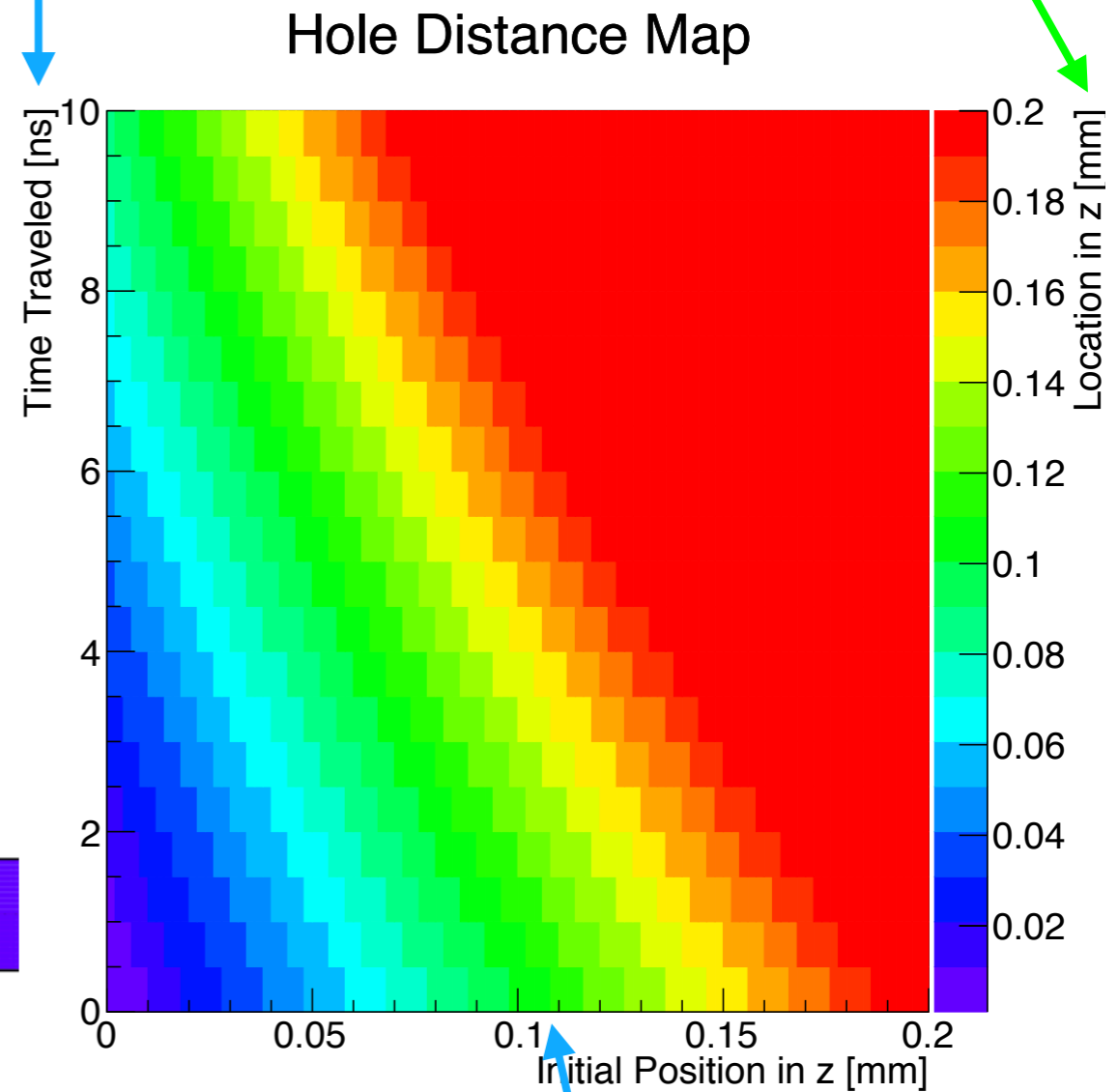
- 1) Look up trap position
- 2) Look up weight. pot. at trap position & starting position
- 3) Use difference to find charge induced on collecting electrode!

$$Q = q \cdot \Delta\phi(z)$$

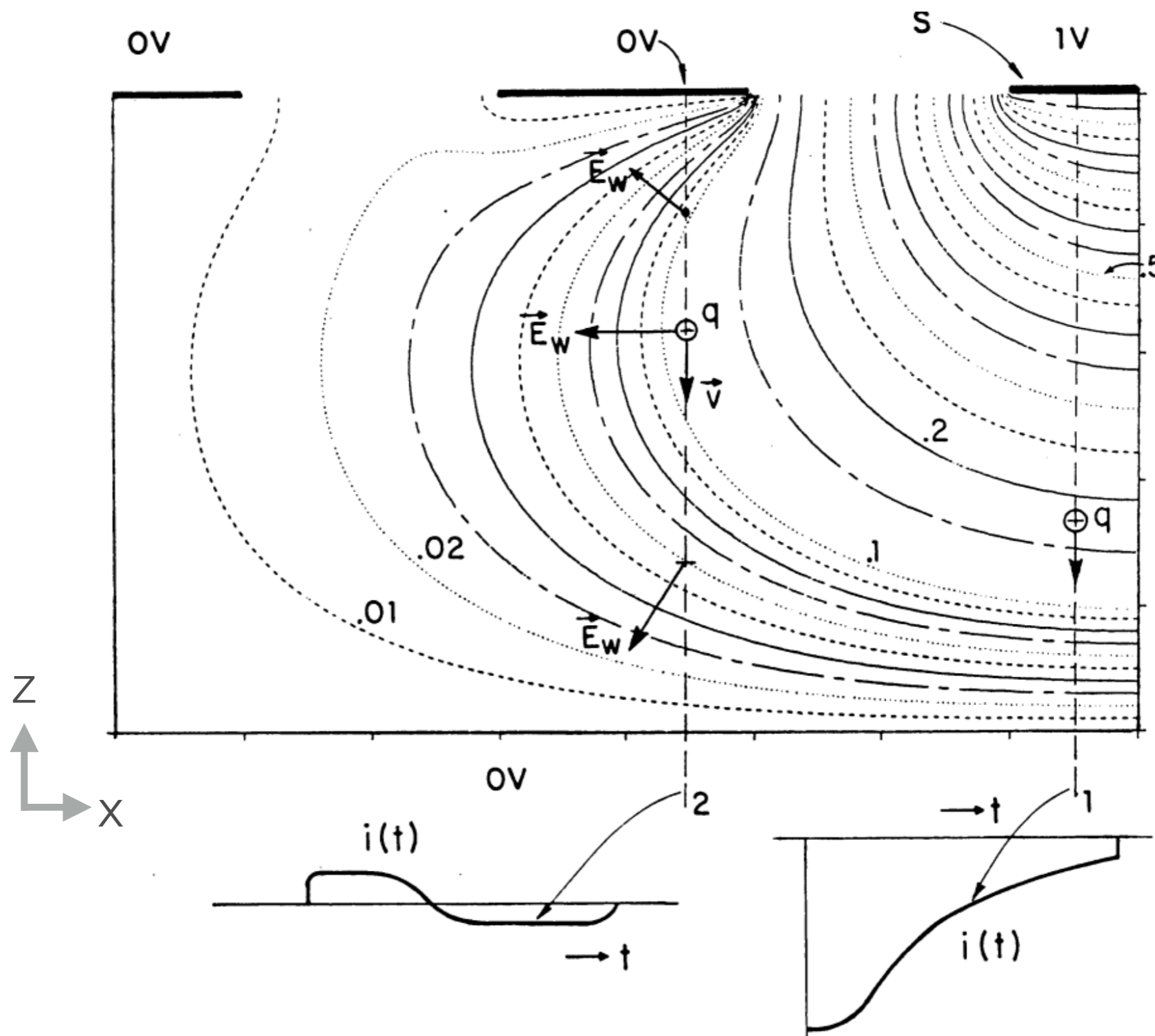


Input: time traveled till trapped

Output: trap position



Input: creation of charge carrier pair by ionization



Adjacent bipolar crosstalk signal

Signal at collection electrode