Contribution ID: **29**   Type: **not specified**

New FPGA platform to upgrade the Cavity BPM Systems at Fermi@Elettra

Wednesday 5 June 2019 08:30 (25 minutes)

A new digital FMC carrier board with Altera/Intel Arria 10 FPGA and 10 GbE capability has been developed to support various applications of Elettra-Sincrotrone Trieste laboratories, such as eBPM, PhBPM, Diamond detectors, TDC, etc…
One of these applications is to replace our old uTCA digitizer cards called ADA and ADO that are used in the Cavity BPM system for Fermi@Elettra Free Electron Laser facility. We will describe the new carrier board and the new system topology that plans to replace a complete CBPM uTCA crate.

Authors: DE MONTE, Raffaele (Elettra Sincrotrone Trieste SCpA); BRAJNIK, Gabriele (Elettra - Sincrotrone Trieste); Mr GIURESSI, Dario (Elettra - Sincrotrone Trieste)

Presenter: DE MONTE, Raffaele (Elettra Sincrotrone Trieste SCpA)

Track Classification: BPM electronics development