

Prototype Characterization of a Charge-integrating Pixel Detector Readout Chip with In-pixel A/D conversion

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The HYLITE (High dYnamic range Laser Imaging deTEctor) is a new hybrid pixel detector readout chip, which is designed for advanced light sources such as X-ray Free Electron Laser (XFEL) and diffraction-limited storage rings. These X-ray sources produce beams with high intensities, high repetition frequency and good coherence. In order to make use of these excellent properties, the detector system with higher dynamic range and readout speed is needed. HYLITE is a charge-integrating readout chip which has three gains for different dynamic ranges and automatic gain switching function. The full dynamic range covered by HYLITE is $1\sim 10000$ photons at 12keV for each pixel in every shot. In-pixel ADC is designed to achieve front-end digitalization and 10 kHz continuous frame rate.

HYLITE 0.1 is the first prototype chip designed using CMOS 0.13 μm technology to verify basic pixel functions. The pixel array consists of 6×12 pixels. The size of each pixel is $200\mu\text{m}\times 200\mu\text{m}$ and will be improved to $100\mu\text{m}\times 100\mu\text{m}$ in the future. Six different design variations of pixels with different integrating capacitance and structures were designed to help optimize between area and performance. The test results showed that the non-linearity of the pre-amplifier is 0.16%, 0.51%, and 1.11% in high, medium and low gain mode, respectively. A 10bits Wilkinson ADC is integrated in each pixel to digitalize the output of pre-amplifier. The linear feedback shift register (LFSR) is used as the ADC clock counter as well as the readout shifter for compacting the layout area. By front-end digitalizing, analog signal transmission of long distance is avoided and frame rate of 10 kHz was also tested and proved.

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