

**Development and applications  
of fast-timing semiconductor  
devices for US-Japan  
collaboration**

**Report of Contributions**

Contribution ID: 1

Type: **not specified**

## **LGAD Sensor measurement in Japan**

*Thursday 21 February 2019 09:00 (1 hour)*

**Presenter:** ONARU, Kyoji (University of Tsukuba (JP))

**Session Classification:** Low Gain Avalanche Detector

Contribution ID: 2

Type: **not specified**

## **CMS LGAD Detector & Readout**

*Thursday 21 February 2019 10:00 (1 hour)*

**Presenter:** HELLER, Ryan (Fermi National Accelerator Lab. (US))

**Session Classification:** Low Gain Avalanche Detector

Contribution ID: 3

Type: **not specified**

## **LGAD ASIC prospect**

*Thursday 21 February 2019 11:00 (30 minutes)*

**Presenter:** APRESYAN, Artur (Fermi National Accelerator Lab. (US))

**Session Classification:** Low Gain Avalanche Detector

Contribution ID: 4

Type: **not specified**

## **ATLAS ITK Pixel development**

*Friday 22 February 2019 09:00 (1 hour)*

**Presenter:** HARADA, Daigo (University of Tsukuba (JP))

**Session Classification:** Other Experiment for FTBF testbeam

Contribution ID: 5

Type: **not specified**

## **SOI CMOS development in Japan**

*Friday 22 February 2019 10:00 (1 hour)*

**Presenters:** HARA, Kazuhiko (University of Tsukuba (JP)); Dr YAMADA, Miho (KEK); HITOSHI, Murayama (Tsukuba); IWANAMI, Shikie (Tsukuba); Mr ONO, Shun (KEK)

**Session Classification:** Other Experiment for FTBF testbeam