LHCC Poster Session – February 27th 2019 Characterization of front-end electronics for the High Granularity Timing Detector in ATLAS

The High Granularity Timing Detector (HGTD) [1] is a proposed silicon detector for the forward region of the ATLAS detector in the High Luminosity LHC. It's high granularity and excellent timing resolution open new possibilities in the particle reconstruction, physics analysis and luminosity measurement at the HL-LHC.



Motivation: Pile-up Mitigation



HGTD:

- Coverage: 2.4 $< |\eta| <$ 4.0
- z position = 3.5 m
- $\sigma_t = 30 \text{ps/MIP}$ (pre-irrad.)

HL-LHC:

- 200 interactions/bunch crossing
- $\sigma_z = 45$ mm (150 ps)
- $oldsymbol{\sigma}_t = 175$ ps
- on average: 1.6 vertices/mm
- **ITk:** $\mathbf{z}_0 > 1 \mbox{ mm for } \boldsymbol{\eta} > 3$, $\mathbf{p}_T < 1.5 \mbox{ GeV}$



n-on-p Si detector with extra

• Expected time resolution

• x20 Gain and increased S/N ratio

before irradiation = 30 ps

0.6

0.8

-Sum

···· Primary e+ h

a)

1.0

Time [ns]

– Gain e+ h

doped p-layer

HGTD Front-End Electronics

 $\ensuremath{\mathsf{ALTIROC}}$: 225 channels - 2x2cm^2 ASIC for measurement and digitization of LGAD signal



 Resolve merged-in-space vertices

The HGTD has to maintain an excellent timing resolution under very high radiation doses = $5.1 \times 10^{15} n_{eq}/cm^2 + 4.7$ MGy TID!

LGAD:

Sensor Technology: Low Gain Avalanche Detector



Timing resolution:

$$\sigma_{det}^2 = \sigma_{Landau}^2 + \sigma_{electronics}^2$$

Dimensions to optimize σ_{det}^2 :

- \circ thin sensors 50 μ m
- small area $1.3 \times 1.3 \text{ mm}^2$ pads

Electronics contributions to the time resolution

$$\sigma_t^2 = \sigma_{jitter}^2 + \sigma_{TimeWalk}^2 + \sigma_{TDC}^2 + \sigma_{Clock}^2$$

Current [µA]

-3

0.0

0.2

0.4

• Jitter: Noise contribution to the signal - fast signals, small detector capacitance

Requirements:

- Excellent time resolution
- radiation hardness
- trigger rates of HL-LHC
- Preamplifier
- TOT Discriminator time walk correction
- 2 TDCs digitization of time measurements
- Memory information storage until trigger

Testbeam measurements with ALTIROC prototype

ALTIROC0:

- 8-channel prototype
- only analog (preamp + discri)
- 4-pad unirradiated sensor bump-bonded



Testbeam with ALTIROC0:

- @CERN SPS North Area
- 120 GeV pions
- tracking from AIDA Telescope
- SiPM + quartz bar system for time reference

Results

Time Resolution:

improvement

• time resolution of the SiPM subtracted

• best achieved $\sigma_t = 35$ ps

• time walk correction $\rightarrow 30\%$



•	100 _E -							<u> </u>
5	90	90 <i>HGTD</i> Preliminary 80 Testbeam October 2018					Before TimeWalk Correction	
	80						After TimeWalk Correction	
	70							
	60						+	
	50	±	Ŧ		٠	ŧ	•	
	40	-	•	•		•	•	
	30	•	•		•			

2140 2150 2160 2170 2180 2190 2200

20E

- **Time Walk:** large signals cross a constant threshold faster than small ones **Time-Over-Threshold** measurement
- binning of the **Time-to-Digital Converter** (TDC) **fine binning**
- \bullet Clock contribution \rightarrow online calibration



[1] Technical Proposal: A High-Granularity Timing Detector for the ATLAS Phase-II Upgrade, CERN-LHCC-2018-023. LHCC-P-012, CERN, 2018

