

# **The Engineering, Production and Quality Assurance of the Inner Barrel Staves for the Upgrade of the ALICE Inner Tracking System.**

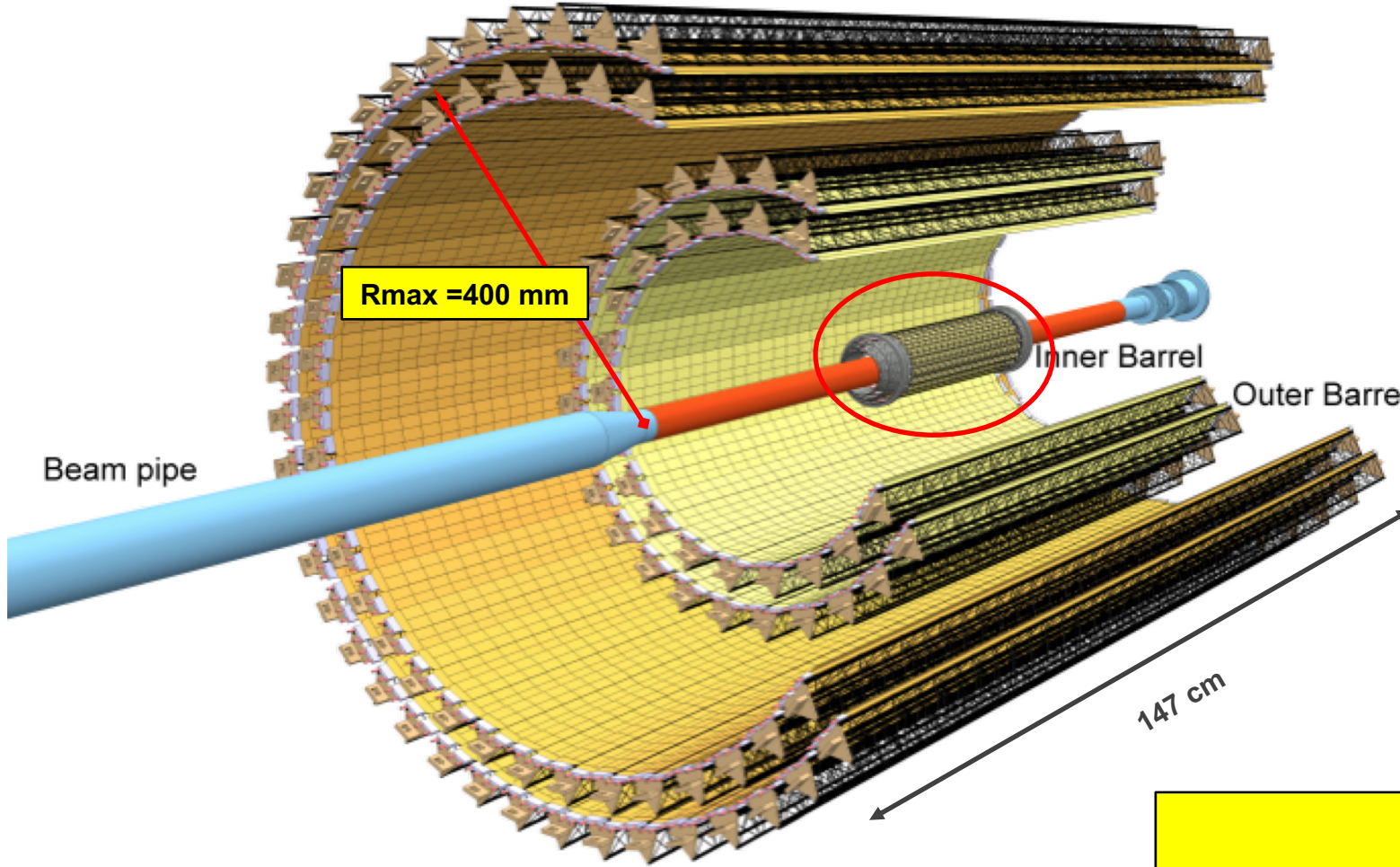
**Antoine JUNIQUE (CERN)**

**on behalf of the ALICE Collaboration**

**06.09.2019**

**TWEPP 2019**

- **ALICE Inner Tracking System layout**
- **Manufacturing of:**
  - **Inner Barrel Flex Printed Circuit**
  - **Hybrid Integrate Circuit (HIC)**
  - **Stave**
- **Challenges**
- **Conclusion**



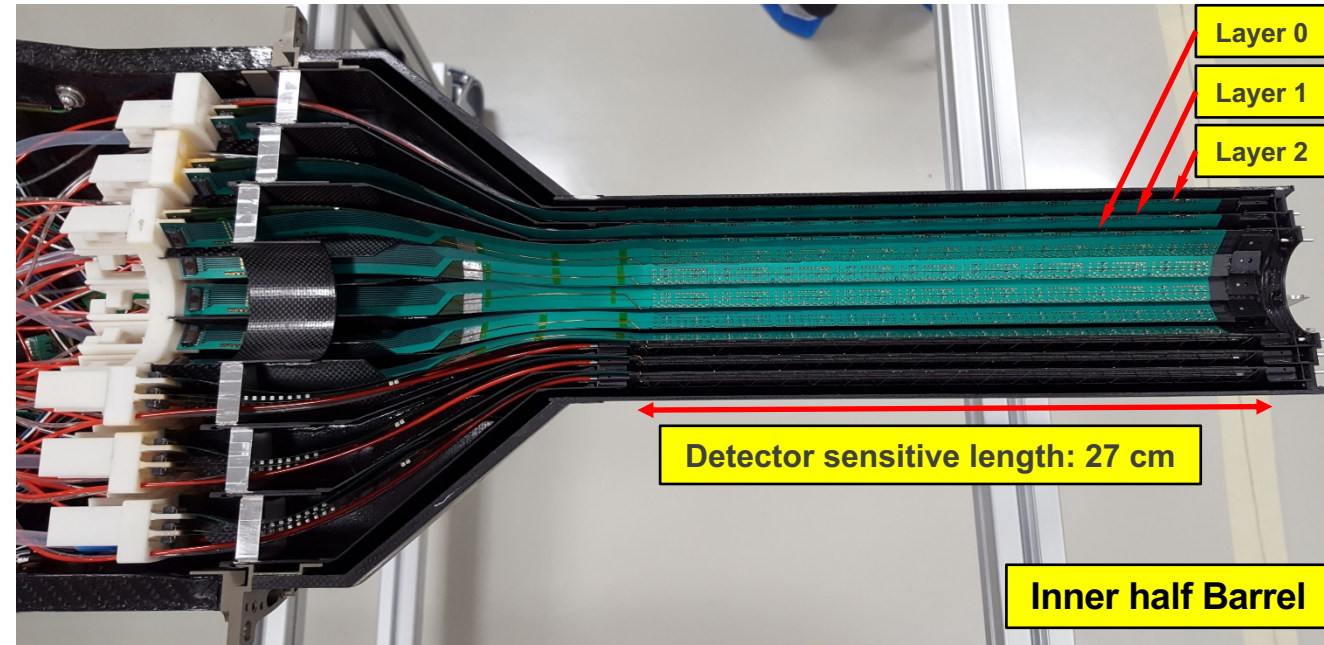
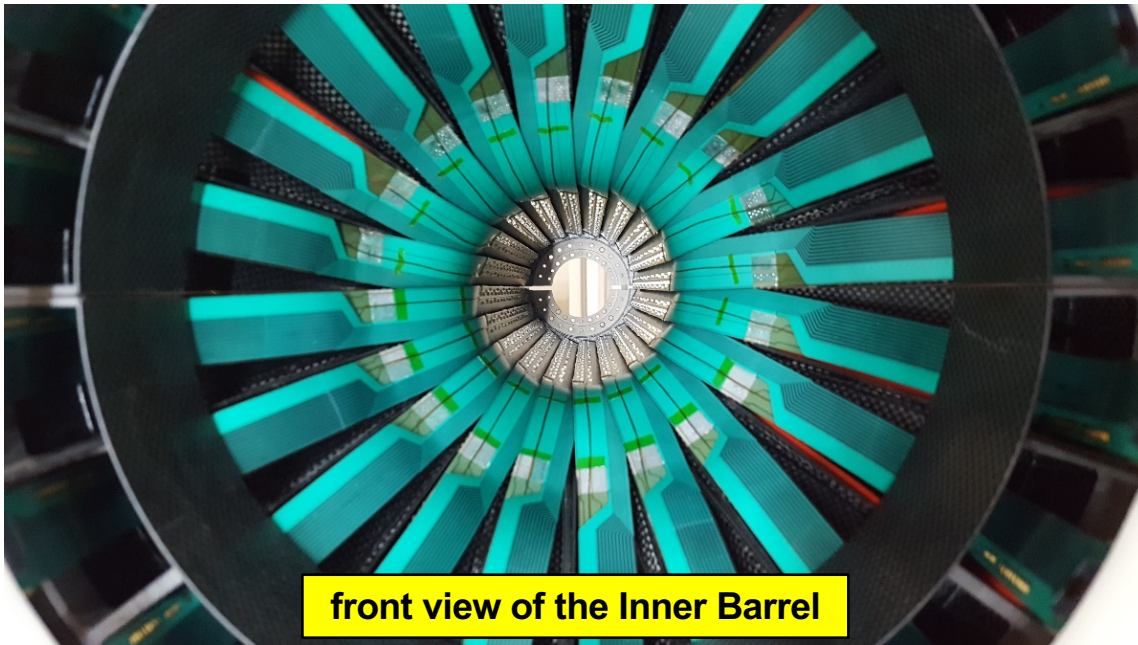
## Motivations and goals

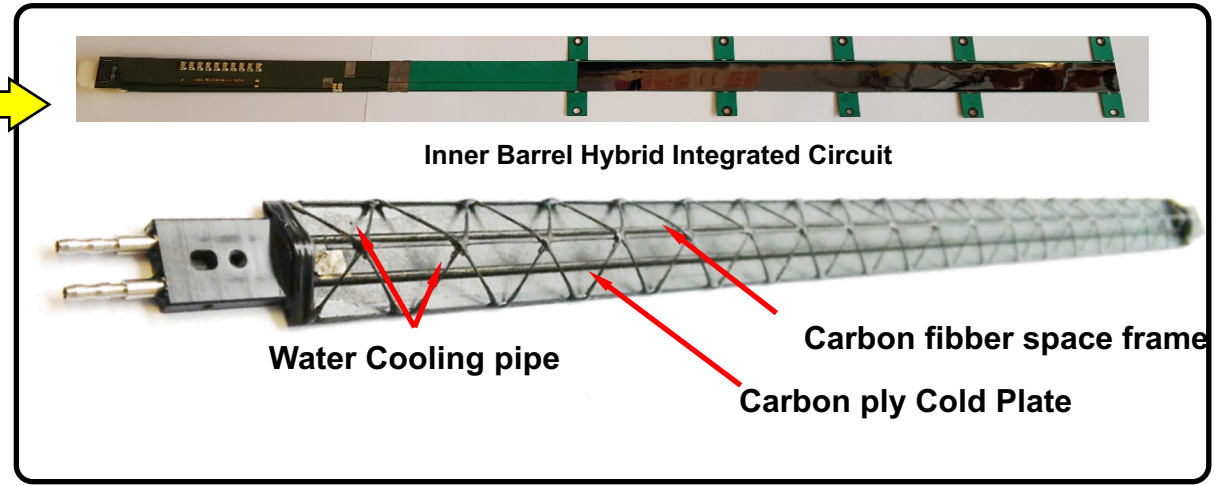
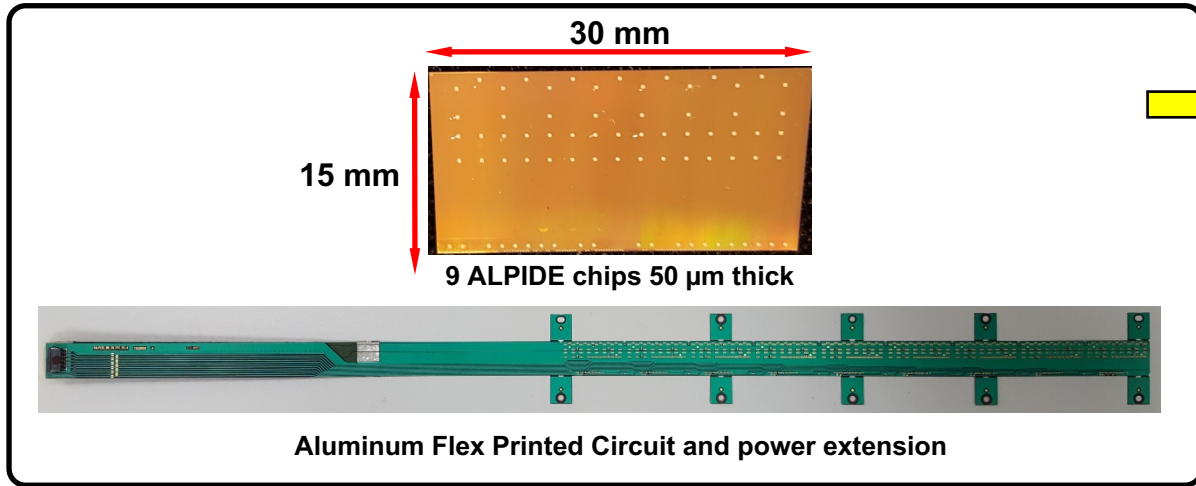
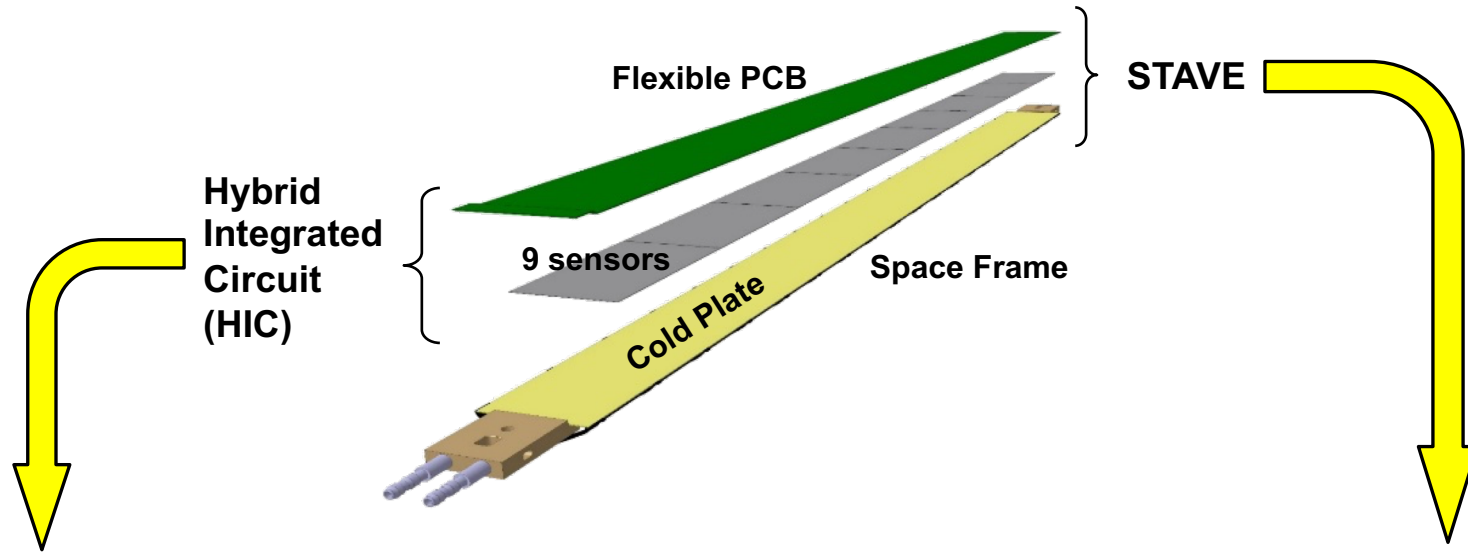
- Close to Interaction point
- Small pixel (29 x 27  $\mu\text{m}$ )
- Spatial resolution:  $\sim 5 \mu\text{m}$
- Low material budget : 0.38 %/X0
- Readout collision up to 100 kHz Pb-Pb and 400 kHz pp

10 m<sup>2</sup> active silicon area  
 ~ 24000 MAPS (Monolithic Active Pixel Sensor chips)  
 12.5 G-pixels

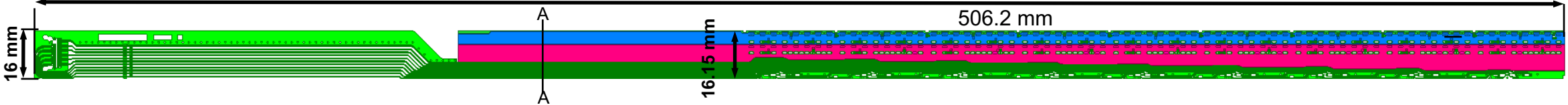
## IB ITS upgrade in numbers (main components)

- IB staves: 48 (9 chips per Stave)
- Pixel sensor chip (ALPIDE 50  $\mu\text{m}$  thick): 432

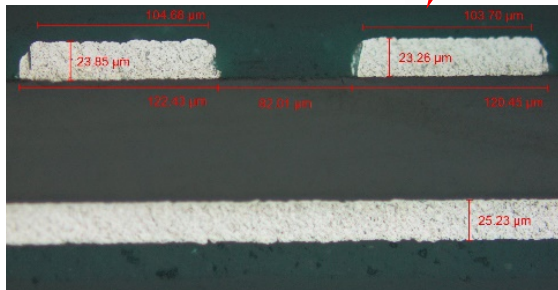
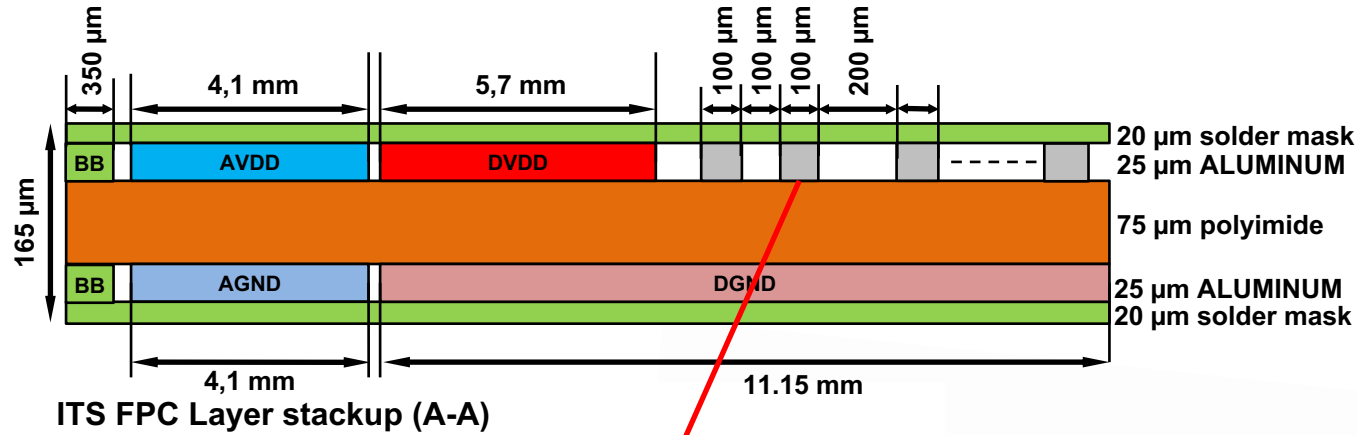




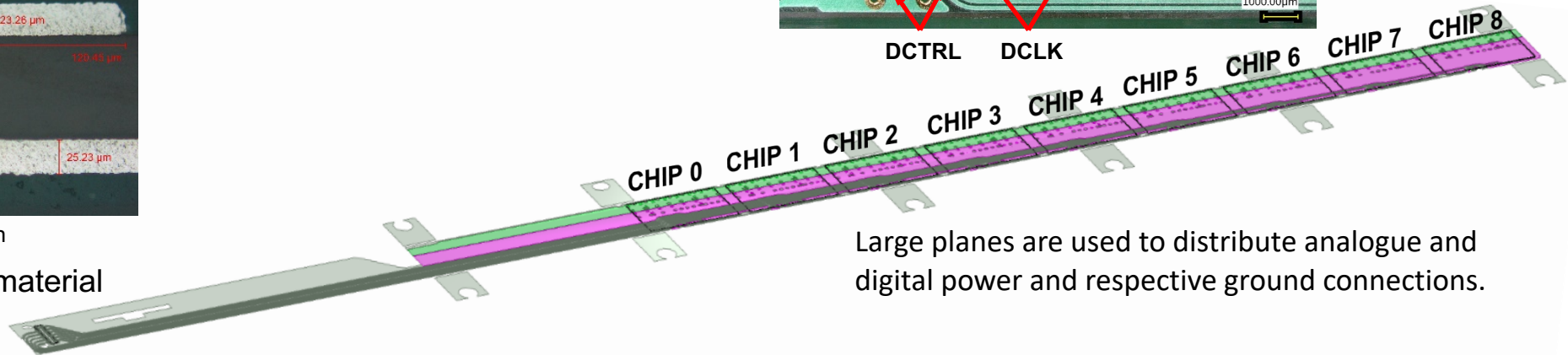
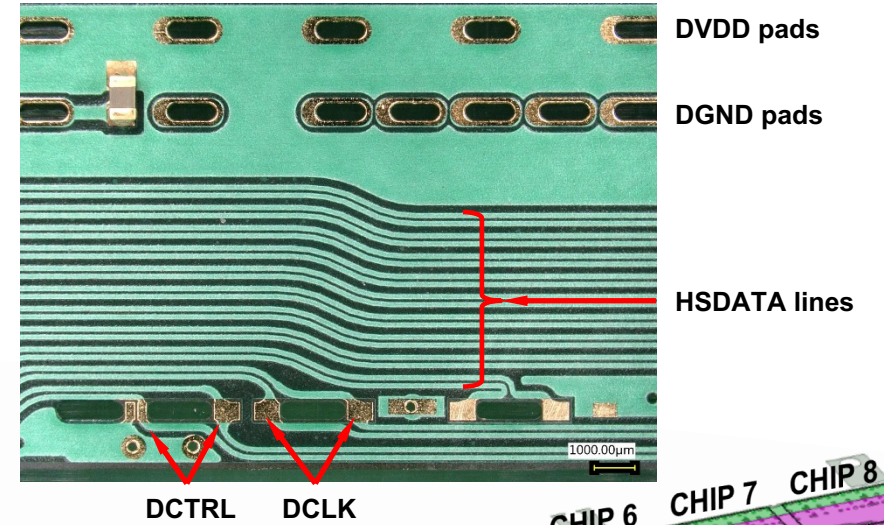
# Inner Barrel Flex Printed Circuit



The 9 silicon chips are read out in parallel: each chip sends its data stream to the end of Stave, at 1.2Gb/s, via a dedicated differential pair, 100  $\mu\text{m}$  wide. Two additional differential pairs distribute the clock and configuration signals at 40 MHz.



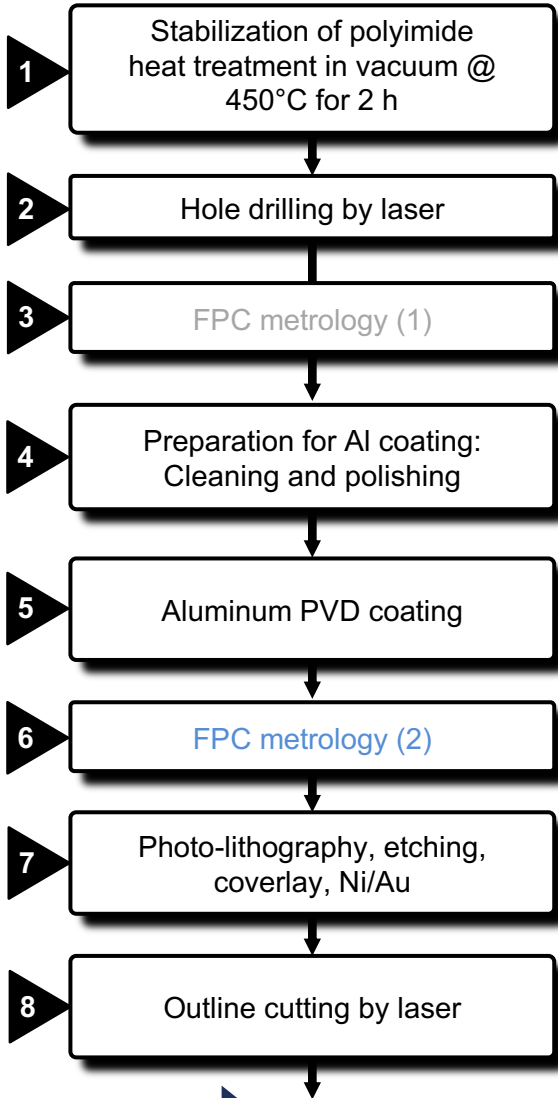
HSDATA cross section



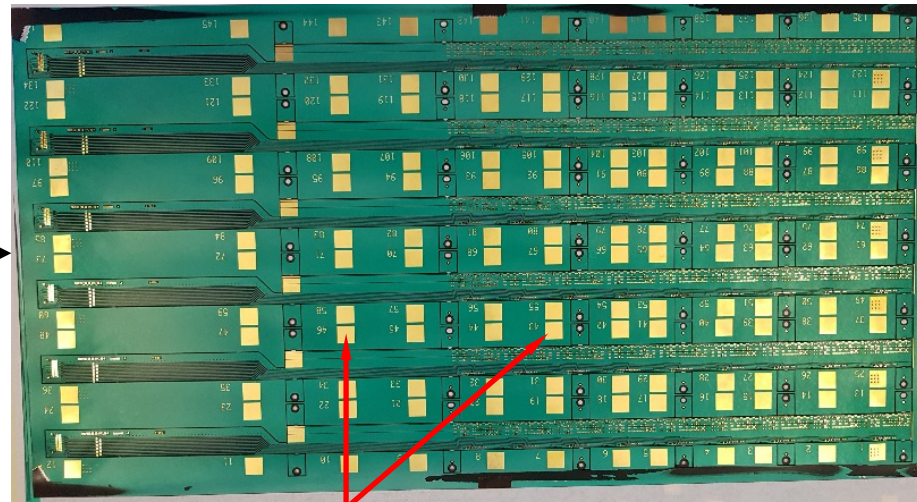
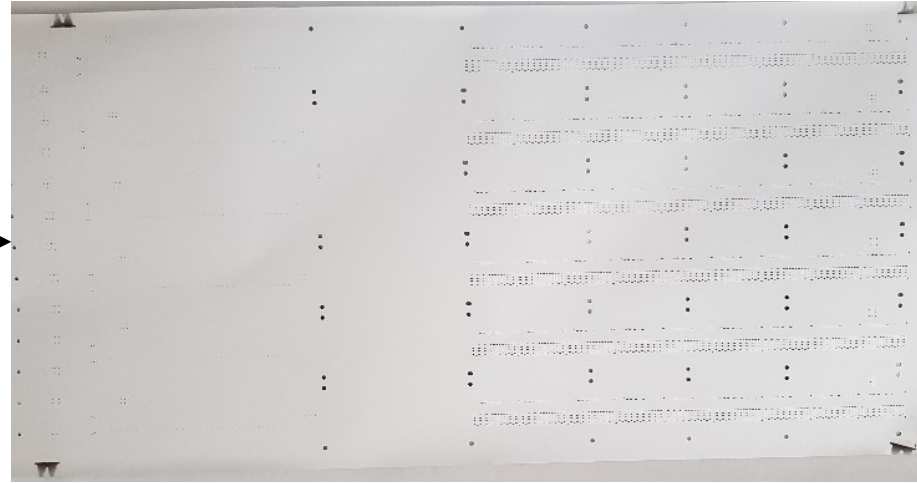
Large planes are used to distribute analogue and digital power and respective ground connections.

Aluminum on polyimide to reduce the material budget

## Main production steps

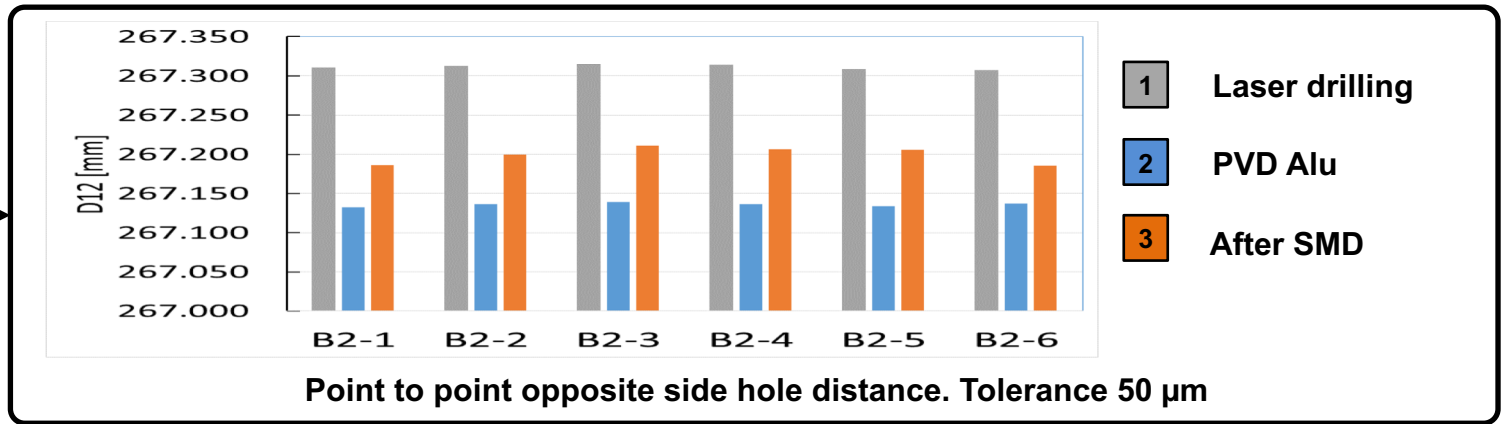
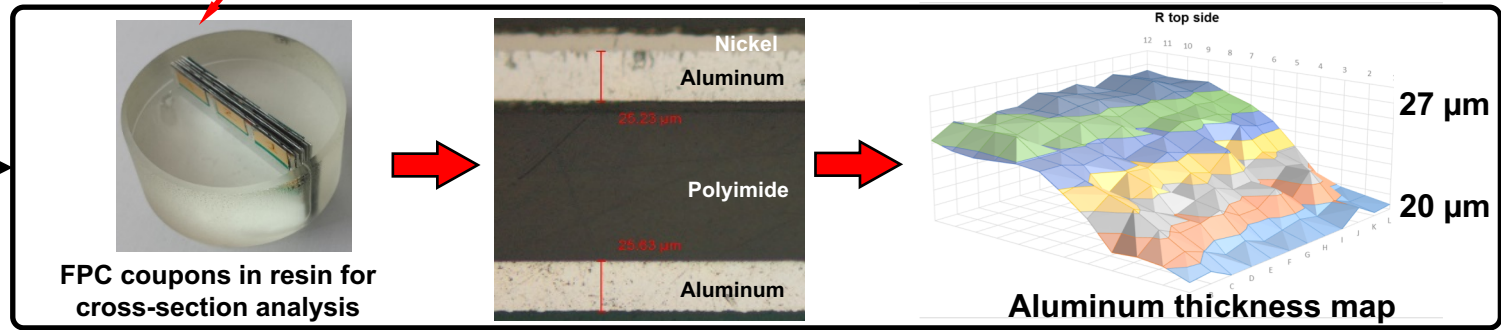
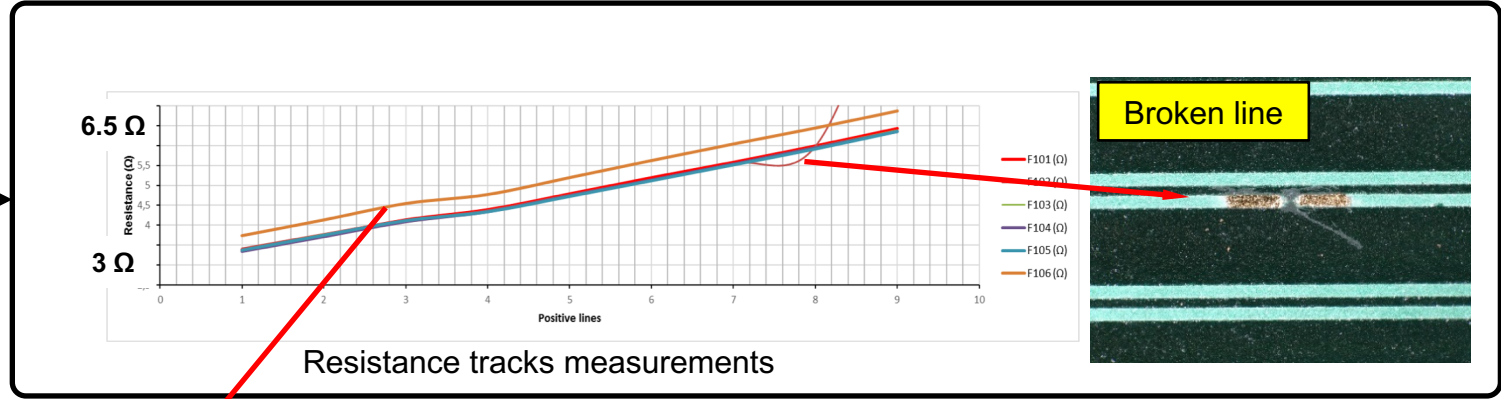
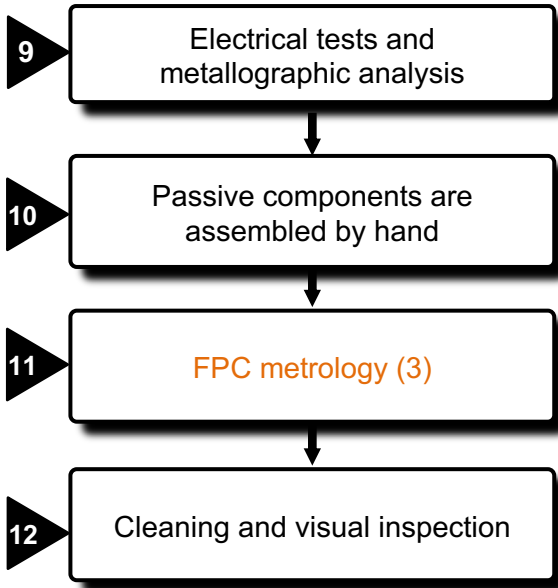


The inner barrel FPCs are mostly manufactured at CERN PCB workshop.



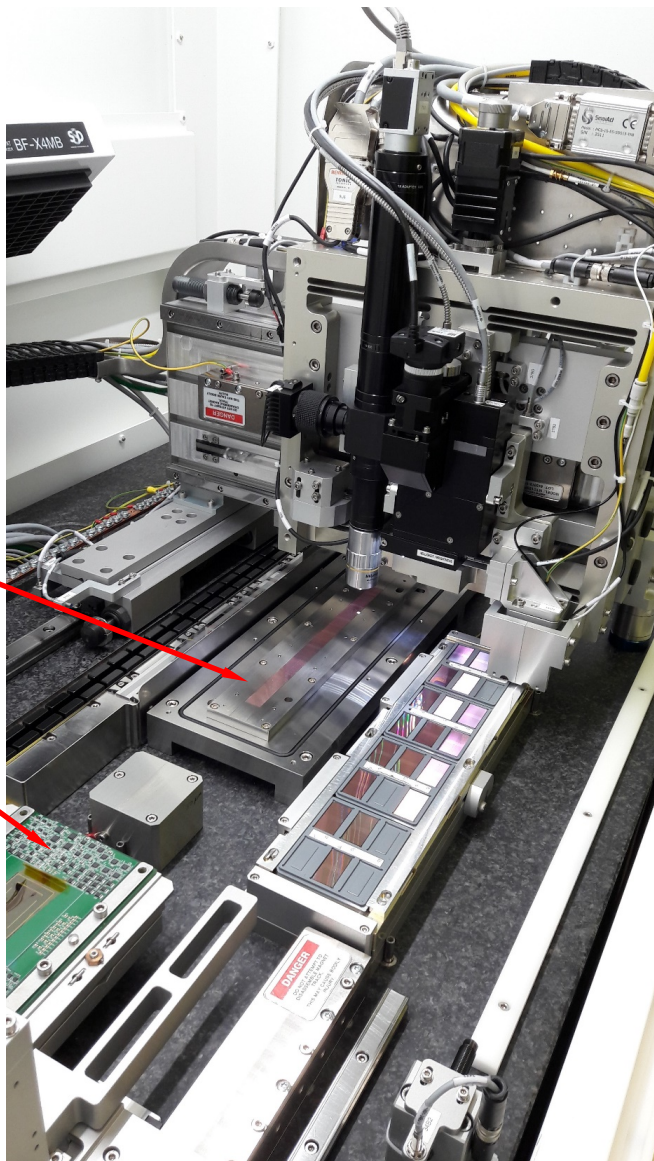
Test coupons

- 8 batches of 24 IB FPCs, 192 FPCs in total
- 8 batches delivered (85 % yield)
  - 35 NOK, related to manufacture issues
  - 8 NOK, related to damages during handling or SMD mounting



**All the metrology is executed at CERN**





Alignment table

Probe card

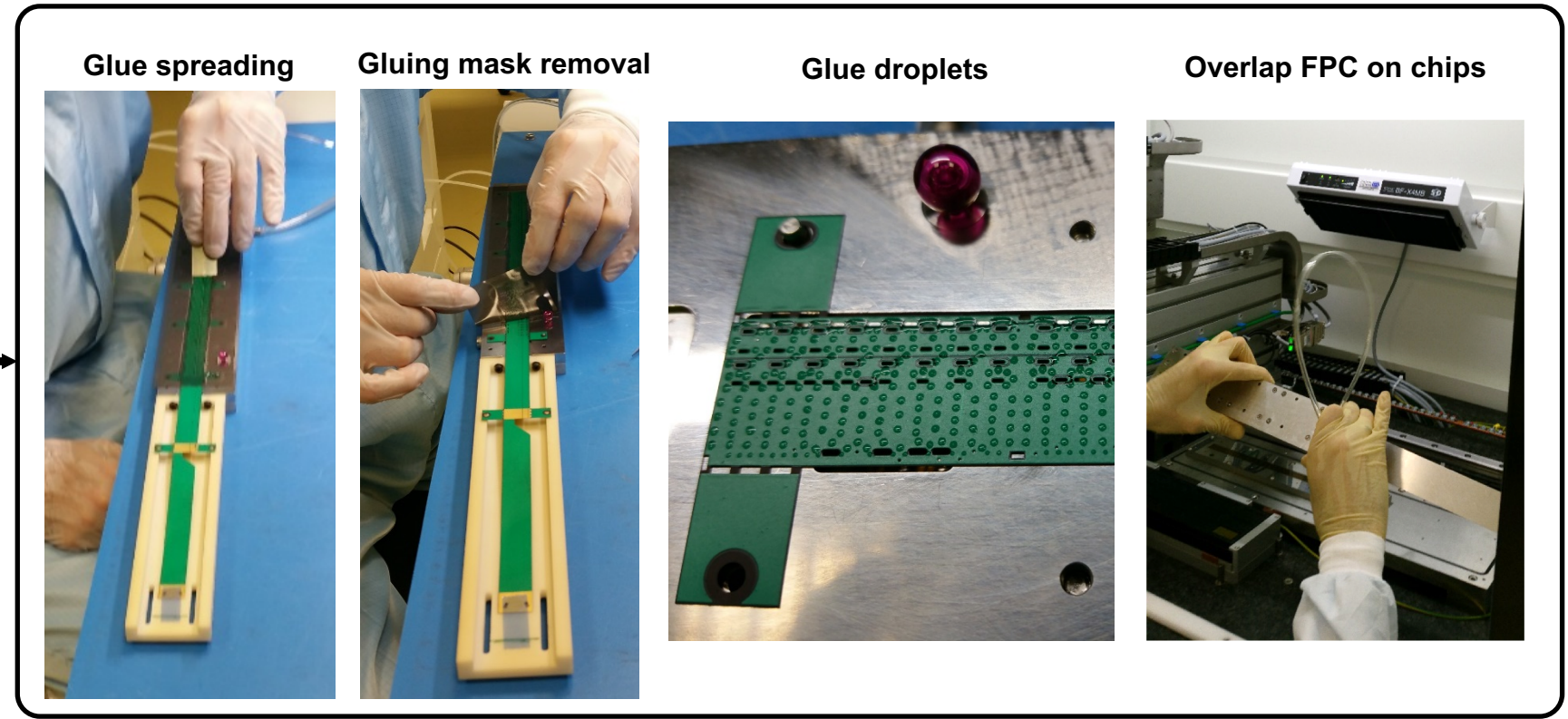
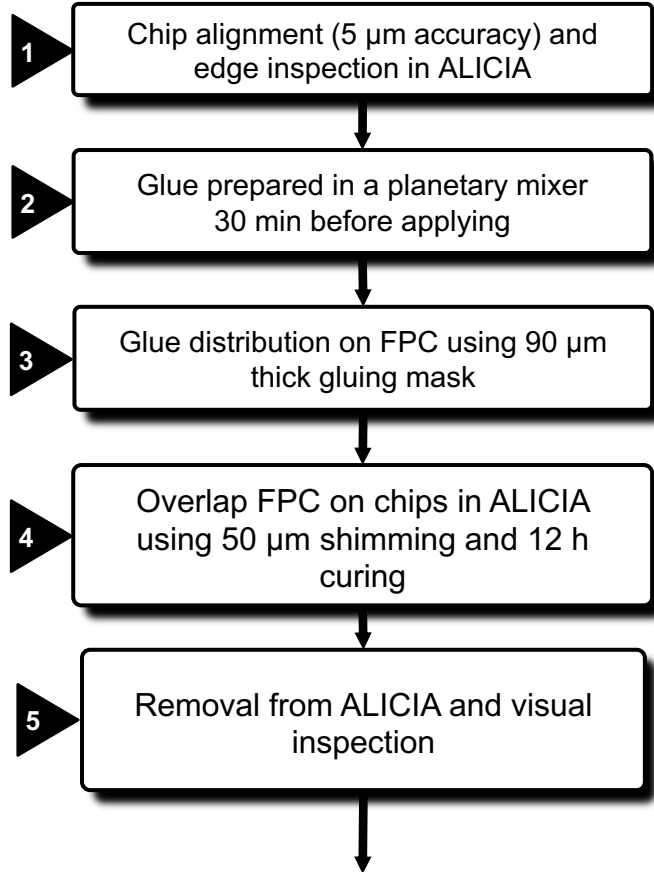
Custom developed assembly and test machine for the ALICE ITS: This machine was designed and produced, with CERN specifications, by the IBS company based at Eindhoven, Netherlands.

- Electrical test with a probe card
- Automated pick and place of pixel chips (50  $\mu\text{m}$  , 100  $\mu\text{m}$  thick)
- Automated visual inspection and control of the chip
- Alignment of 1x9 or 2x7 pixel chips for HIC assembly (<5  $\mu\text{m}$  alignment precision)

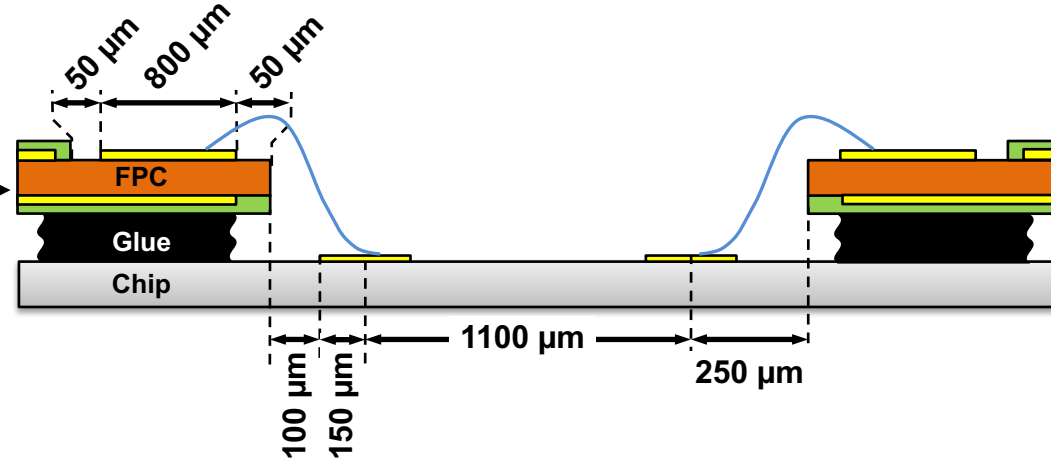
6 ALICIA machines were produced and installed in the ALICE ITS production centers.

## Components preparation

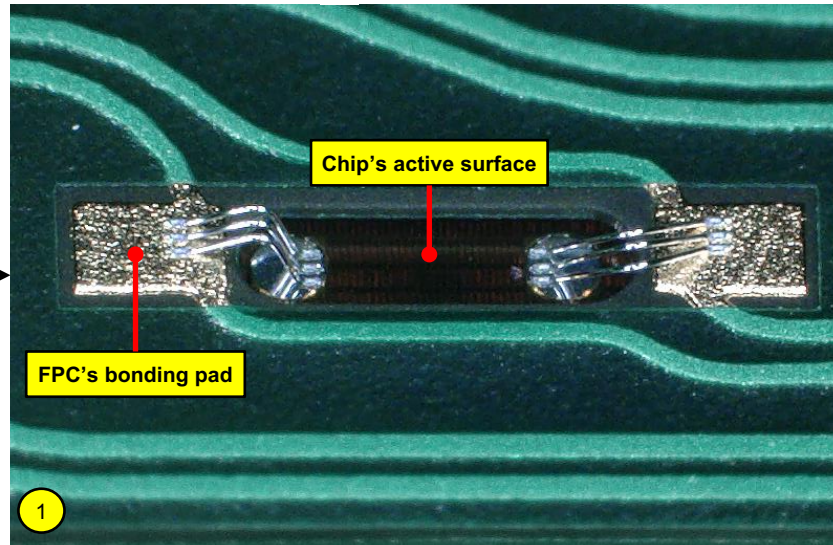
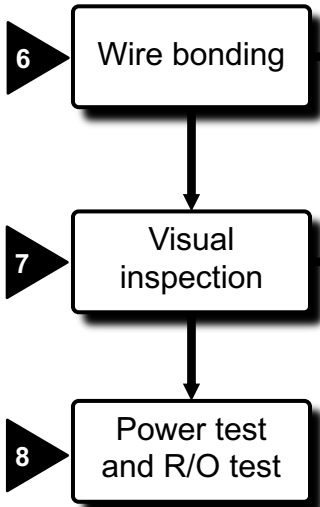
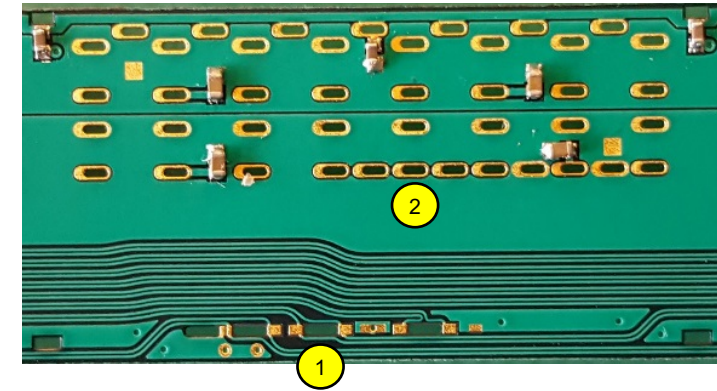
- Chips: visual inspection, selected "GOLD" (less than 50 dead pixels over 500 000) from electrical test.
- FPC: electrical characterisation, metrology, cleaning, visual inspection
- Gluing mask : visual inspection and cleaning



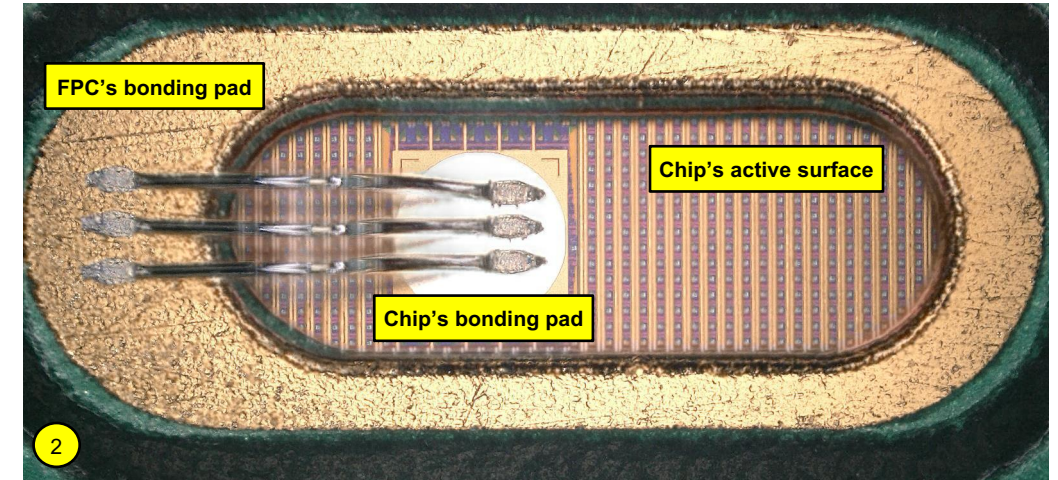
Schematic view of the FPC-Chip interconnection by wire bonding technique



Wire bonding FPC layout

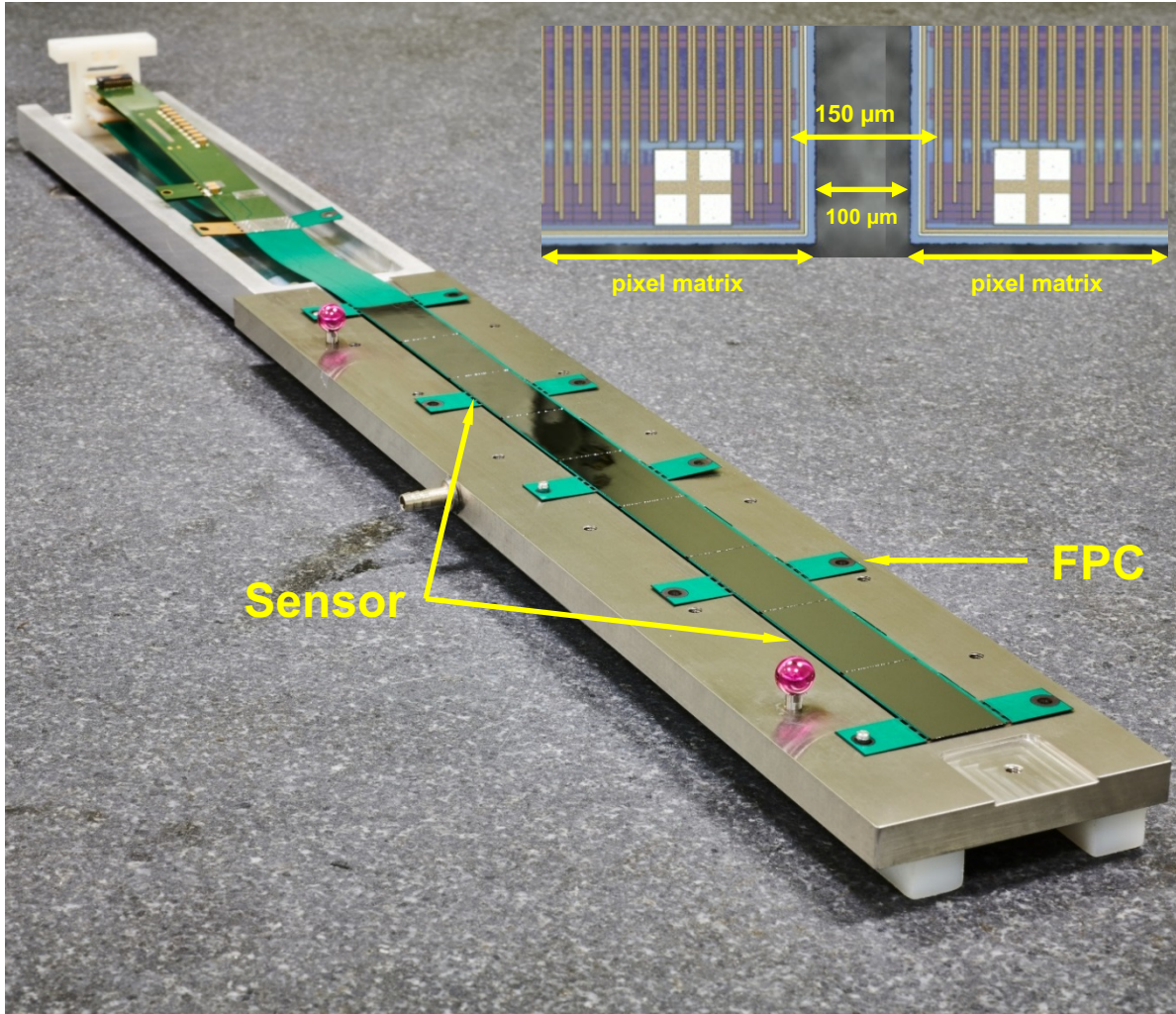


Signal connection

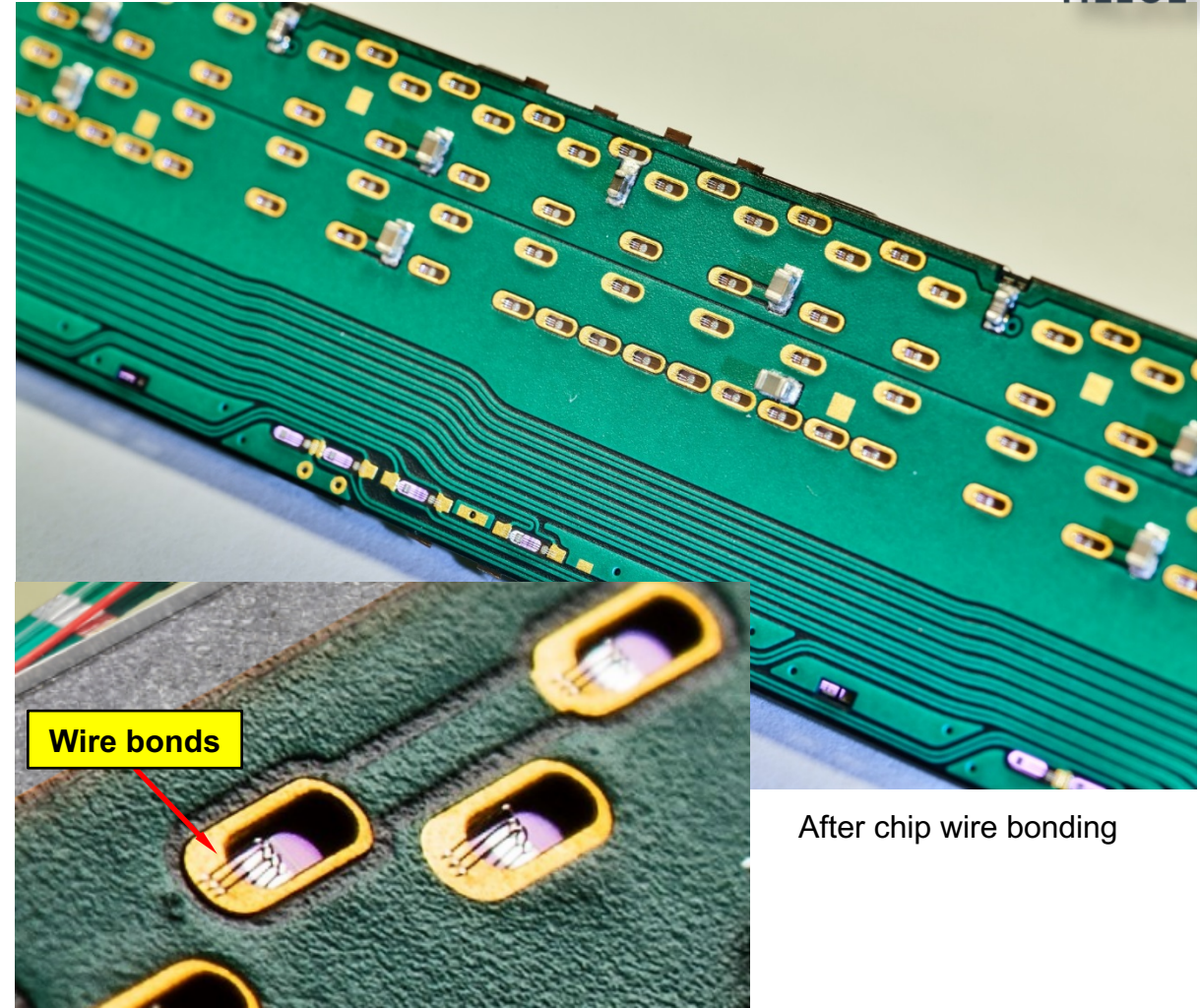


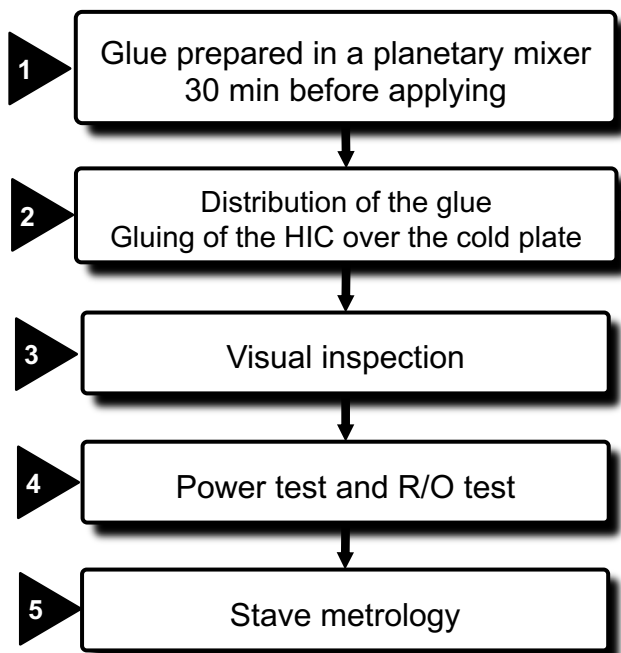
Power connection

All the wire bonding is executed at CERN



After chip gluing





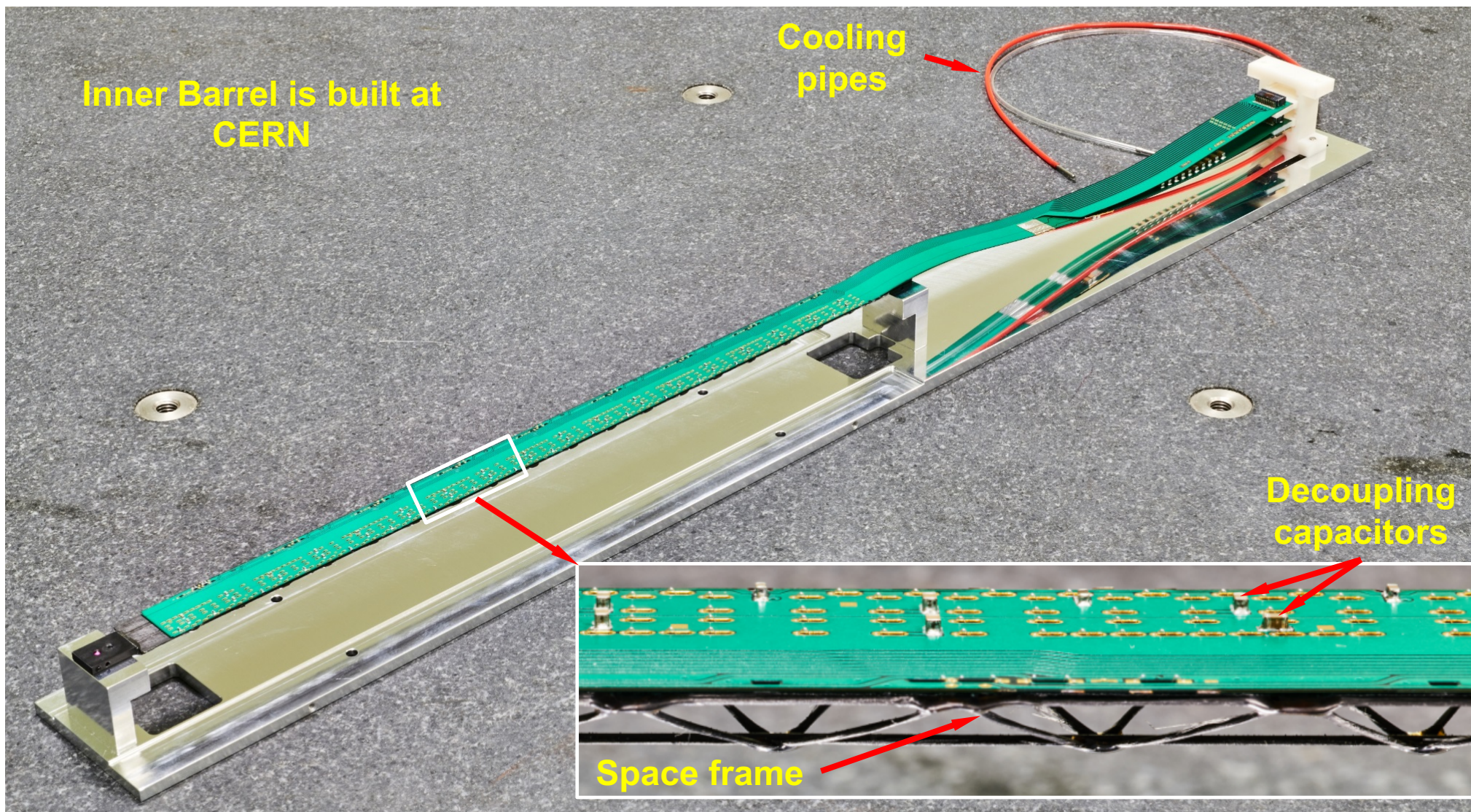
Gluing mask application

Araldite spreading

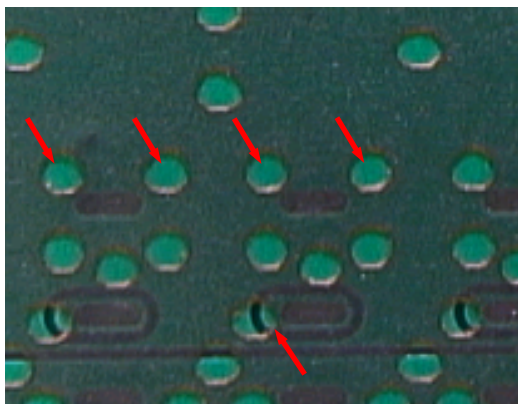
Gluing mask removal

Space frame

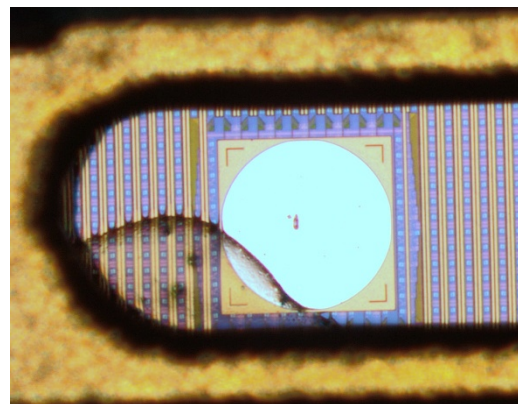
The detailed assembly process is shown in a large frame. It includes: 'Gluing mask application' showing a metal frame with a mask; 'Araldite spreading' showing a person in a blue lab coat spreading glue; 'Gluing mask removal' showing the mask being peeled off; 'Space frame' showing the assembly with a red arrow pointing to a specific part. A large red arrow on the right points from the 'Space frame' image to a smaller inset image showing a person handling a component.



## - Gluing mask alignment and glue seepage



Gluing mask misalignment

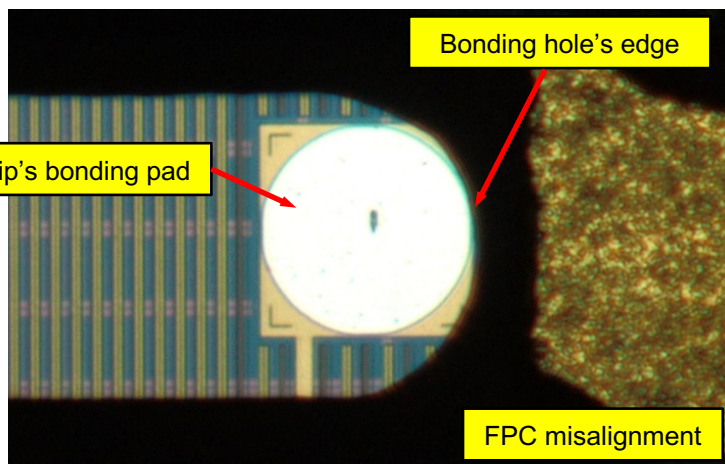


Bonding pad covered by glue

### - Solutions:

- Improvement of the glue mask design
  - Hole diameter 600  $\mu\text{m}$
  - Thickness 90  $\mu\text{m}$
- Using vacuum jig to align the glue mask
- Glue mask manufacturing improvement
- Start polymerization of the glue before applying it

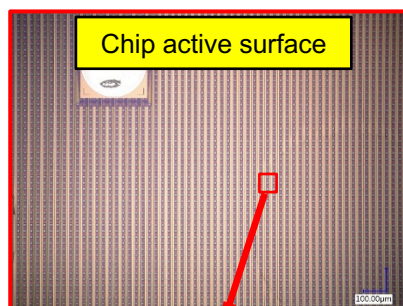
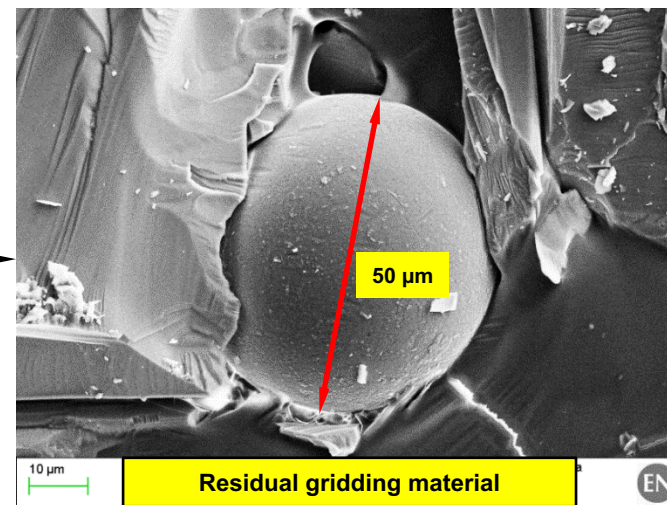
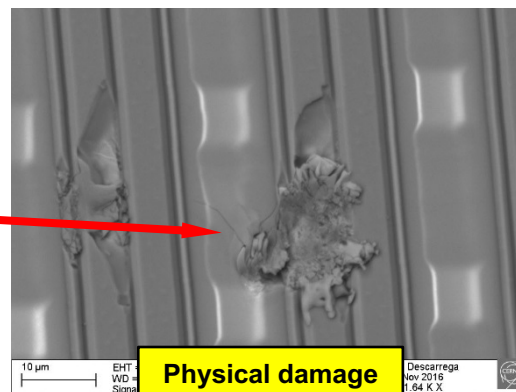
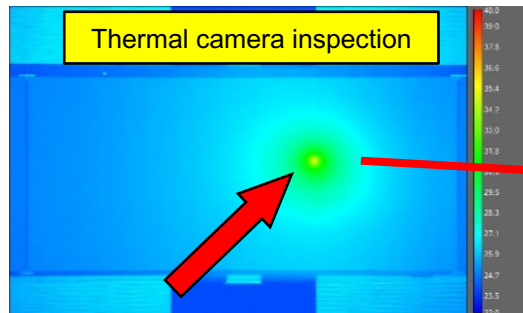
## - FPC dimensional stability



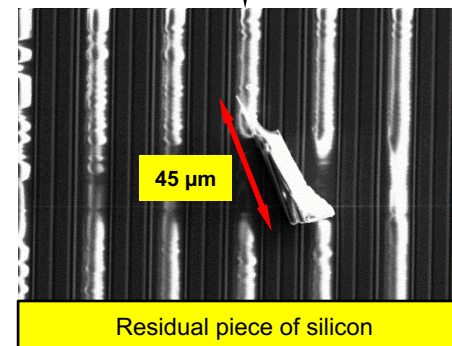
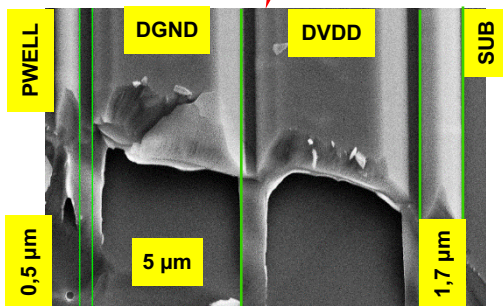
### - Solutions:

- Thermal treatment to stabilize the polyimide foil
- Redesign the FPC layout with an expansion coefficient
- Assembling the passive components by hand to prevent thermal stress.

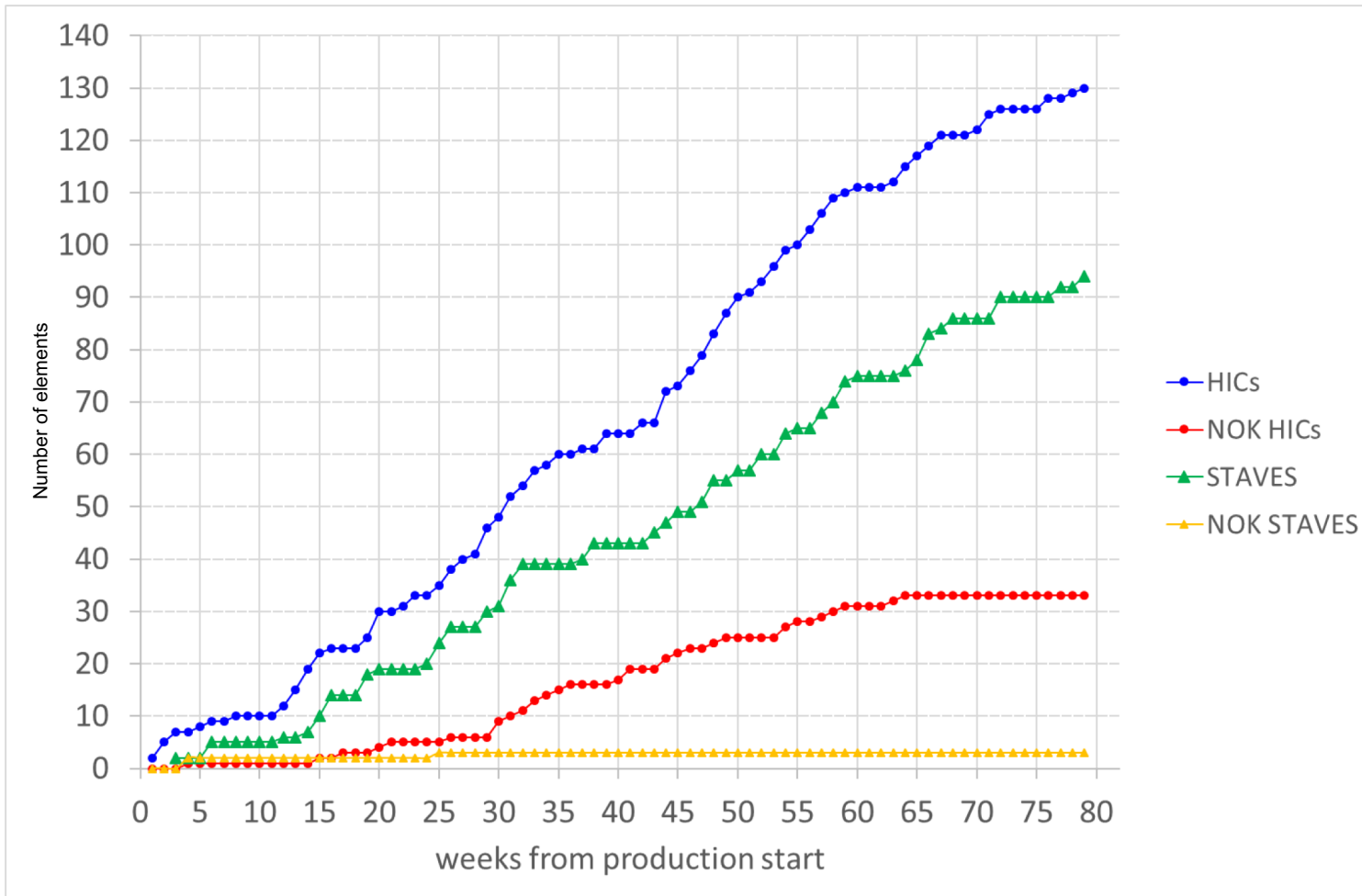
## - Prevent hard particle contamination



- Solutions:**
- Visual inspection steps added
  - Cleaning procedure improvement
  - Glue mask manufacturing improvement
  - Glue replacement, without filler particles
  - Glue layer thickness optimization

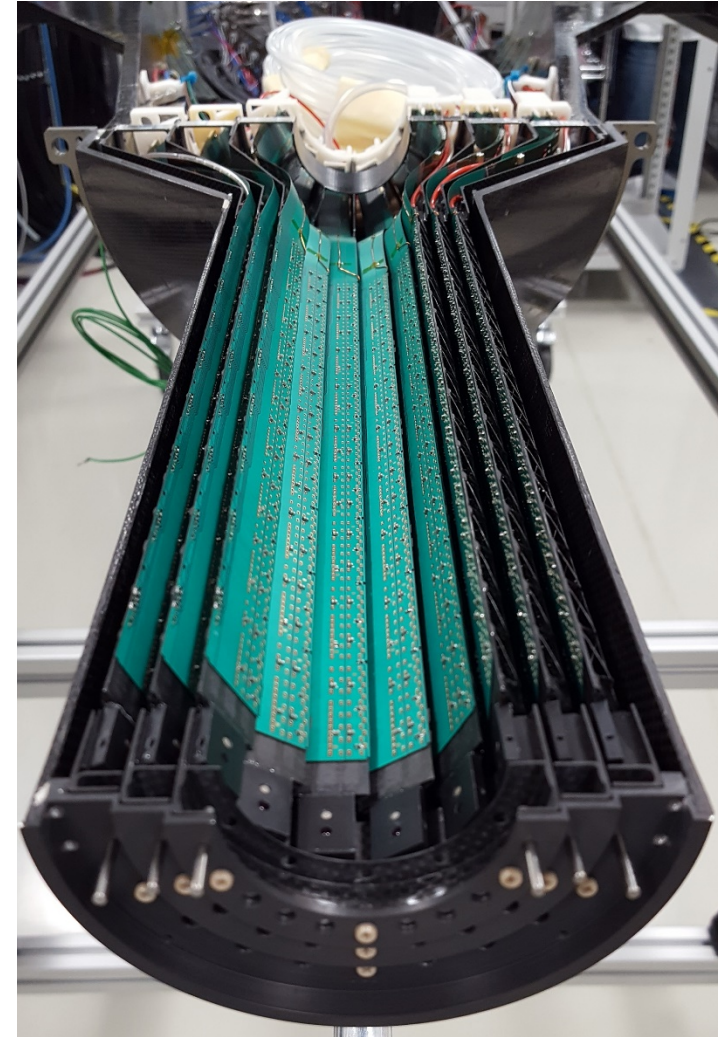






- **130 HICs were constructed**
- **27 HICs were discarded mostly due to electrical problems**
- **96 staves were constructed**
- **1 Inner Barrel was assembled with 48 Staves**

- ALICE ITS Inner Barrel was constructed
  - ✓ With low material, 0.38 %/X<sub>0</sub>
  - ✓ Conductive layer from aluminum
  - ✓ Signal lines routed without vias
  - ✓ Wire bonding through FPC
- Lessons learned
  - ✓ Intensive quality assurance
  - ✓ Importance of the metrology
  - ✓ Electrical tests
  - ✓ Cross section analysis
  - ✓ Prevention of contamination
  - ✓ Long training and specialize team
- Commissioning started and will be completed by April 2020, installation in the cavern in summer 2020

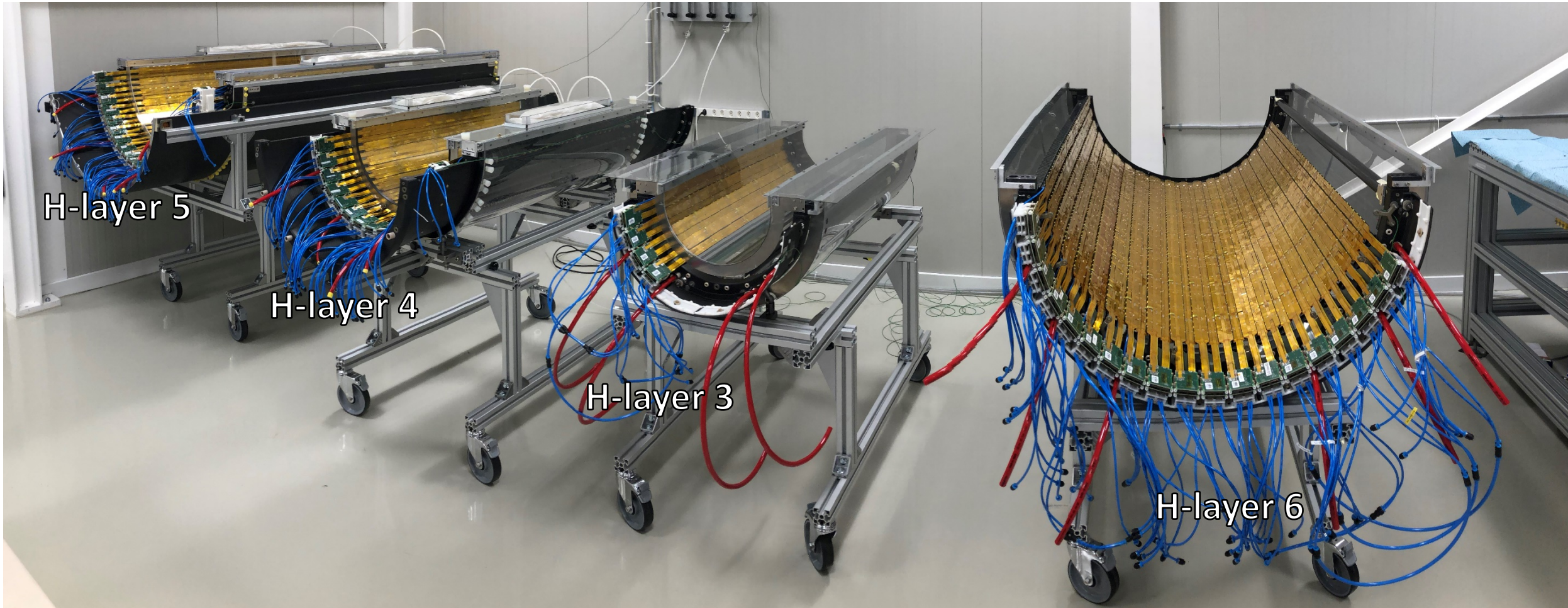


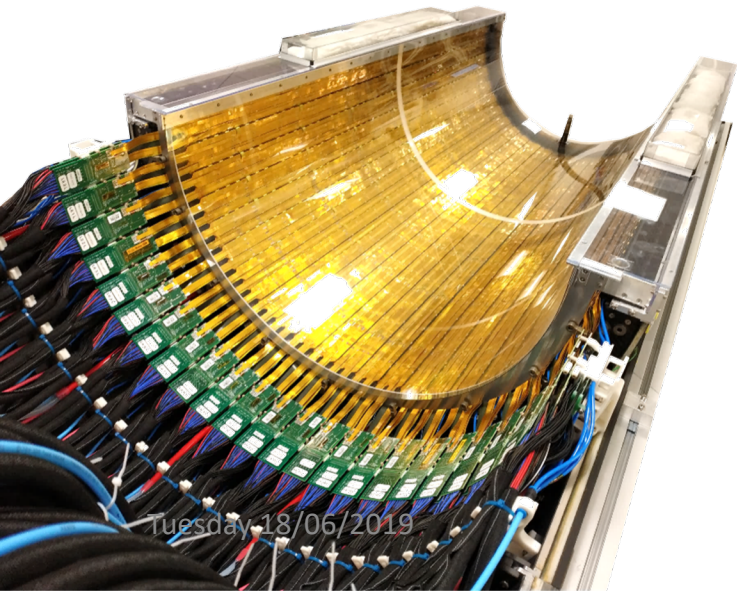
**Inner half Barrel**

**Thank you for your attention!**

# Backup slides

## Half-barrel completed





Tuesday, 18/06/2019

- **Layer 6** and **Layer 2** being commissioned with final electronics and cooling



