TWEPP 2019 Topical Workshop on Electronics for Particle Physics



Contribution ID: 178 Type: Oral

Focal plane processing in standard CMOS technologies

Monday 2 September 2019 16:05 (40 minutes)

The new paradigm of Analog to Information Conversion (AIC) aims at extracting information from the environment rather than simply massive amounts of raw data. Focal plane processing is a way of doing so by means of processing the sensed data at the acquisition stage, efficiently reducing bandwidth and power consumption. The use of standard CMOS technologies favours the development of low cost solutions.

Primary author: LÓPEZ MARTÍNEZ, Paula (University of Santiago de Compostela (ES))

Presenter: LÓPEZ MARTÍNEZ, Paula (University of Santiago de Compostela (ES))

Session Classification: Invited