

TWEPP 2019 Topical Workshop on Electronics for Particle Physics

Tuesday, 3 September 2019

ASIC - Aula magna (09:00 - 10:15)

-Conveners: Grzegorz Deptuch

time	[id] title	presenter
09:00	[75] Performance of LAUROC1, ATLAS Liquid Argon Upgrade ReadOut Chip	DE LA TAILLE, Christophe
09:25	[55] A 4-Channel 10-Gbps/ch CMOS VCSEL Array Driver with on-chip Charge Pumps	Prof. YE, Jingbo
09:50	[70] Electronics readout for the CGEM - Inner Tracker: TIGER ASIC and electronics chain	BORTONE, Alberto

ASIC - Aula magna (11:30 - 12:20)

-Conveners: Grzegorz Deptuch

time	[id] title	presenter
11:30	[138] A Clock and Data Recovery Circuit for the ALTA/CMS HL-LHC Pixel Front End Chip in 65 nm CMOS Technology	MOUSTAKAS, Konstantinos
11:55	[91] The IpGBT: a radiation tolerant ASIC for Data, Timing, Trigger and Control Applications in HL-LHC	MOREIRA, Paulo

ASIC - Aula magna (14:00 - 15:15)

-Conveners: Angelo Rivetti

time	[id] title	presenter
14:00	[38] Development of RD50-MPW2: a high-speed monolithic HV-CMOS prototype chip within the CERN-RD50 collaboration	ZHANG, Chenfan
14:25	[158] Continuous Integration of FPGA Designs and Automation of the Development Environment	GLEIN, Robert
14:50	[115] The analog readout channel for the Si(Li) tracker of the GAPS experiment	RICEPUTI, Elisa

Wednesday, 4 September 2019

ASIC - Aula magna (09:00 - 10:15)

-Conveners: Angelo Rivetti

time	[id] title	presenter
09:00	[84] picoTDC: A 3ps bin bize 64 channel TDC for HEP experiments	HORSTMANN, Moritz
09:25	[77] ALTIROC1, a 25 pico-second time resolution ASIC for the ATLAS High Granularity Timing Detector (HGTD)	SEGUIN-MOREAU, Nathalie
09:50	[13] First experimental results on TOFHIR readout ASIC of the CMS Barrel Timing Layer	NIKNEJAD, Tahereh Sadat

ASIC - Aula magna (14:00 - 15:15)

-Conveners: Christine Guo Hu

time	[id] title	presenter
14:00	[86] A Reconfigurable Monolithic Active Pixel Sensor for Digital Electromagnetic Calorimetry	Mr BENHAMMADI, Seddik
14:25	[42] CLICTD: A monolithic HR-CMOS sensor chip for the CLIC silicon tracker	KREMASTIOTIS, Iraklis
14:50	[98] Depleted monolithic active pixel prototypes for MIP detection and photon counting	GRINSTEIN, Sebastian

Thursday, 5 September 2019

ASIC - Aula magna (10:15 - 11:30)

-Conveners: Ping Gui

time	[id] title	presenter
10:15	[78] Development of ultra-low power 10-bit SAR ADC in 65 nm CMOS technology	MORON, Jakub
10:40	[76] SALT, a 128-channel readout ASIC for Upstream Tracker in the LHCb Upgrade	IDZIK, Marek
11:05	[34] Downlink Equalization and Eye Opening Monitor in the lpGBT	Dr SUN, Quan

Friday, 6 September 2019

ASIC - Aula magna (09:00 - 10:15)

-Conveners: Christine Guo Hu

time	[id] title	presenter
09:00	[12] The lpGBT PLL and CDR Architecture, Performance and SEE Robustness	BIEREIGEL, Stefan
09:25	[124] The lpGBTIA, a 2.5 Gbps Radiation-Tolerant Optical Receiver for InGaAs photodiodes	MENOUNI, Mohsine DE OLIVEIRA FRANCISCO, Rui TROSKA, Jan RODRIGUES SIMOES MOREIRA, Paulo
09:50	[46] An lpGBT sub-system for environmental monitoring and control of experiments	FIRLEJ, Miroslaw

ASIC - Aula magna (11:30 - 11:55)

-Conveners: Christine Guo Hu

time	[id] title	presenter
11:30	[48] A multi-channel multi-data rate circuit for phase alignment of data in the lpGBT	KULIS, Szymon