

Session Program

Sep 2 - 6, 2019



TWEPP 2019 Topical Workshop on Electronics for Particle Physics

ASIC

Tue, September 3

9:00 AM

ASIC

Session | **Location:** Aula magna | **Convener:** Grzegorz Deptuch

09:00 - 09:25

Performance of LAUROC1, ATLAS Liquid Argon Upgrade ReadOut Chip

Speaker

Christophe De La Taille

09:25 - 09:50

A 4-Channel 10-Gbps/ch CMOS VCSEL Array Driver with on-chip Charge Pumps

Speaker

Prof. Jingbo Ye

09:50 - 10:15

Electronics readout for the CGEM - Inner Tracker: TIGER ASIC and electronics chain

Speaker

Alberto Bortone

10:15 AM

11:30 AM

ASIC

Session | **Location:** Aula magna | **Convener:** Grzegorz Deptuch

11:30 - 11:55

A Clock and Data Recovery Circuit for the ALTAS/CMS HL-LHC Pixel Front End Chip in 65 nm CMOS Technology

Speaker

Konstantinos Moustakas

11:55 - 12:20

The IpGBT: a radiation tolerant ASIC for Data, Timing, Trigger and Control Applications in HL-LHC

Speaker

Paulo Moreira

12:20 PM

2:00 PM

ASIC

Session | **Location:** Aula magna | **Convener:** Angelo Rivetti

14:00 - 14:25

Development of RD50-MPW2: a high-speed monolithic HV-CMOS prototype chip within the CERN-RD50 collaboration

Speaker

Chenfan Zhang

14:25 - 14:50

Continuous Integration of FPGA Designs and Automation of the Development Environment

Speaker

Robert Glein

14:50 - 15:15

The analog readout channel for the Si(Li) tracker of the GAPS experiment

Speaker

Elisa Riceputi

3:15 PM

Wed, September 4

9:00 AM

ASIC

Session | **Location:** Aula magna | **Convener:** Angelo Rivetti

09:00 - 09:25 **picoTDC: A 3ps bin bize 64 channel TDC for HEP experiments**

Speaker

Moritz Horstmann

09:25 - 09:50

ALTIROC1, a 25 pico-second time resolution ASIC for the ATLAS High Granularity Timing Detector (HGTD)

Speaker

Nathalie Seguin-Moreau

09:50 - 10:15

First experimental results on TOFHIR readout ASIC of the CMS Barrel Timing Layer

Speaker

Tahereh Sadat Niknejad

10:15 AM

2:00 PM

ASIC

Session | **Location:** Aula magna | **Convener:** Christine Guo Hu

14:00 - 14:25

A Reconfigurable Monolithic Active Pixel Sensor for Digital Electromagnetic Calorimetry

Speaker

Mr Seddik Benhammadi

14:25 - 14:50

CLICTD: A monolithic HR-CMOS sensor chip for the CLIC silicon tracker

Speaker

Iraklis Kremastiotis

14:50 - 15:15

Depleted monolithic active pixel prototypes for MIP detection and photon counting

Speaker

Sebastian Grinstein

3:15 PM

Thu, September 5

10:15 AM

ASIC
Session | **Location:** Aula magna | **Convener:** Ping Gui

10:15 - 10:40
Development of ultra-low power 10-bit SAR ADC in 65 nm CMOS technology
Speaker
Jakub Moron

10:40 - 11:05
SALT, a 128-channel readout ASIC for Upstream Tracker in the LHCb Upgrade
Speaker
Marek Idzik

11:05 - 11:30 **Downlink Equalization and Eye Opening Monitor in the IpGBT**
Speaker
Dr Quan Sun

11:30 AM

Fri, September 6

9:00 AM

ASIC

Session | **Location:** Aula magna | **Convener:** Christine Guo Hu

09:00 - 09:25

The IpGBT PLL and CDR Architecture, Performance and SEE Robustness

Speaker

Stefan Biereigel

09:25 - 09:50

The IpGBTIA, a 2.5 Gbps Radiation-Tolerant Optical Receiver for InGaAs photodiodes

Speakers

Mohsine Menouni, Rui De Oliveira Francisco, Jan Troska, Paulo Rodrigues Simoes Moreira

09:50 - 10:15

An IpGBT sub-system for environmental monitoring and control of experiments

Speaker

Mirosław Firlej

10:15 AM

11:30 AM

ASIC

Session | **Location:** Aula magna | **Convener:** Christine Guo Hu

11:30 - 11:55

A multi-channel multi-data rate circuit for phase alignment of data in the IpGBT

Speaker

Szymon Kulis

11:55 AM