

## **Session Program**

**Sep 2 - 6, 2019**



## **TWEPP 2019 Topical Workshop on Electronics for Particle Physics**

***Programmable Logic, Design Tools and  
Methods***

# Thu, September 5

11:30 AM

## Programmable Logic, Design Tools and Methods

Session | Location: Aula de bioloxía | Convener: Marcus Julian French

11:30 - 11:55

### Implementation of a RD53A readout chain using FELIX system and the PiLUP board

**Speaker**  
Nico Giangiacomi

11:55 - 12:20

### Highly-linear FPGA-based Data Acquisition System for Multi-channel SiPM Readout

**Speaker**  
Todd Townsend

12:20 PM

2:00 PM

## Programmable Logic, Design Tools and Methods

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14:00 - 14:25

### Multi-channel time-tagging module for fast-timing Resistive Plate Chamber detectors

**Speaker**  
Mr Xiushan CHEN

14:25 - 14:50

### Development of the Compact Processing Module for the ATLAS Tile Calorimeter Phase-II Upgrad

**Speaker**  
Fernando Carrio Argos

14:50 - 15:15

### Versatile firmware for the Common Readout Unit (CRU) of the LHC ALICE experiment.

**Speaker**  
Olivier Bourrion

3:15 PM

4:30 PM

## Programmable Logic, Design Tools and Methods

Session | Location: Aula de bioloxía | Convener: Marcus Julian French

16:30 - 16:55

### Development of Readout Electronics for a Digital Tracking Calorimeter

**Speaker**  
Ola Slettevoll Groettvik

4:55 PM