

TWEPP 2019 Topical Workshop on Electronics for Particle Physics

Thursday 5 September 2019

Programmable Logic, Design Tools and Methods - Aula de biología (11:30 - 12:20)

-Conveners: Marcus Julian French

time	[id] title	presenter
11:30	[23] Implementation of a RD53A readout chain using FELIX system and the PiLUP board	GIANGIACOMI, Nico
11:55	[155] Highly-linear FPGA-based Data Acquisition System for Multi-channel SiPM Readout	TOWNSEND , Todd

Programmable Logic, Design Tools and Methods - Aula de biología (14:00 - 15:15)

-Conveners: Marcus Julian French

time	[id] title	presenter
14:00	[132] Multi-channel time-tagging module for fast-timing Resistive Plate Chamber detectors	Mr CHEN, Xiushan
14:25	[96] Development of the Compact Processing Module for the ATLAS Tile Calorimeter Phase-II Upgrad	CARRIO ARGOS, Fernando
14:50	[16] Versatile firmware for the Common Readout Unit (CRU) of the LHC ALICE experiment.	BOURRION, Olivier

Programmable Logic, Design Tools and Methods - Aula de biología (16:30 - 16:55)

-Conveners: Marcus Julian French

time	[id] title	presenter
16:30	[7] Development of Readout Electronics for a Digital Tracking Calorimeter	GROETTVIK, Ola Slettevoll