12th International "Hiroshima" Symposium on the Development and Application of Semiconductor Tracking Detectors (HSTD12) at Hiroshima,

Japan

Contribution ID: 267

Type: ORAL

The Phase 2 upgrade of CMS Inner Tracker

Sunday 15 December 2019 10:20 (20 minutes)

A new Silicon Tracker will be built for the Phase 2 Upgrade of the CMS experiment to fully exploit the increased luminosity delivered by HL-LHC. The innermost part, called the Inner Tracker, will be exposed to extreme conditions such as unprecedented radiation levels of 1.2 Grad and 2E16 neq/cm2 and hit rates of 3.2 GHz/cm2. The new Inner Tracker relies on many novel solutions and technologies that allow for a design of a light and radiation-hard pixel detector of high performance. The hybrid pixel modules will be composed of pixel sensors with pixel size of 2500 um2 and new ASIC, designed in 65 nm CMOS technology, developed by the RD53 collaboration. A novel scheme of serial powering will be deployed to power the pixel modules and new technologies will be used for a high bandwidth readout system. The mechanics will be lightweight, based on carbon-fibre material and two-phase CO2 cooling. In this contribution, the design of the CMS Inner Tracker system will be presented along with the prospective design choices.

Submission declaration

Original and unpublished

Author: ORFANELLI, Stella (CERN)
Presenter: ORFANELLI, Stella (CERN)

Session Classification: Session1

Track Classification: Large scale applications