BE-ABP: Beam Dynamics on the GPU

SixTrackLib + PyHEADTAIL: Summary of Day 1

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Goal

Starting point:

- two existing codes:
 SixTrackLib (templated C) and PyHEADTAIL (Python + PyCUDA)
- merge functionality: single-particle tracking + multi-particle dynamics
 - \bullet abstracted PyHEADTAIL in CuPy: jupyter notebook \angle on github \angle

Goals:

- extend this python script based on PyHEADTAIL to
- done: prepare the accelerator optics in SixTrackLib in chunks (not SixTrackLib's usual one-turn behaviour)
- done: share the macro-particle coordinates / memory from PyHEADTAIL with SixTrackl ib
- o.t.w.: transferring jupyter notebook into PyCUDA (some kernels missing)
 todo: call to SixTrackLib to track through chunks of the optics lattice
 before returning to a PyHEADTAIL multi-particle interaction module
- optimisation of performance and architecture support (single implementation for both multi-core CPU + GPU)

What Did we Do?

Steps to unite codes:

- take memory pointer (and array length) and construct PyCUDA memory to communicate between PyHEADTAIL and SixTrackLib based on the SixTrackLib memory structure
 - → did not manage to do the same CuPy
 - → on the way to implement PyHEADTAIL notebook in PyCUDA (so far exists only in CuPy)
- ② initiate SixTrackLib trackjobs with only parts of optics lattice (changed track function of SixTrackLib) → still need to provide python functions for this

Open questions for optimisation focus on the way:

- improvement of embedding strategy in high-level language (Python):
 PyCUDA vs. CuPy vs. arrayfire vs. numba
- code redundancy vs. multi-hardware support (multi-core CPU, GPU)
- code structuring (kernel size)
- low-level optimisation: register pressure etc.

PyHEADTAIL's Context Management

usual script code:

```
bunch = (...)
one_turn_map = (...)

for turn in range(n_turns):
    for m in one_turn_map:
        m.track(bunch)
```

PyHEADTAIL's Context Management

extended script code:

- → wrap "with GPU(bunch) as cmg:" around simulation code
- \implies PyHEADTAIL takes care of managing CPU RAM and GPU RAM