IXPUG

INTEL EXTREME PERFORMANCE USERS GROUP

IXPUG Annual Conference 2019

September 24–27, 2019

Globe of Science and Innovation at CERN

Geneva, Switzerland

https://www.ixpug.org/events

@IXPUG1

Image courtesy of CERN

Organization: The Core Team

Intel

Shari Lawrence

CERN openlab

Alberto Di Meglio

Kristina Gunne

Andrew Robert Purcell

IXPUG Steering Comm.

Glenn Brook

Estela Suarez

Nalini Kumar

Thomas Steinke

Image courtesy of CERN

About IXPUG

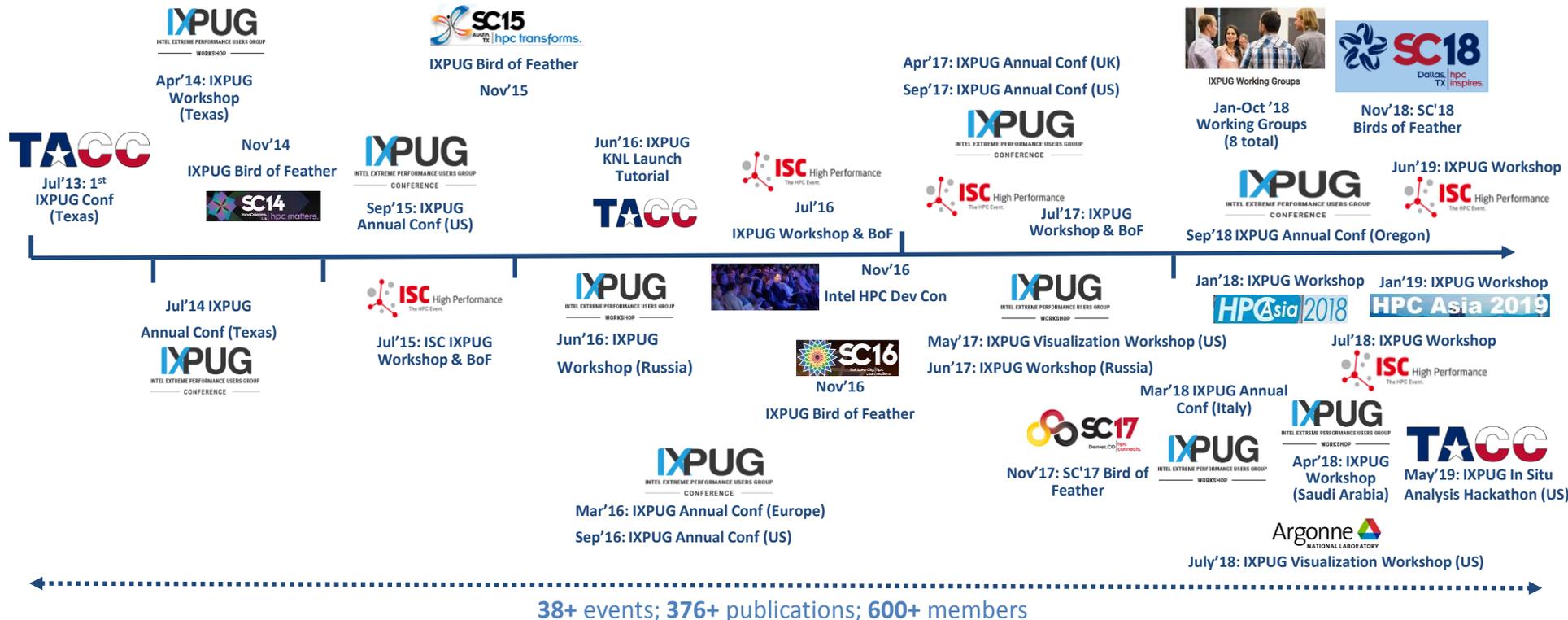## IXPUG = Intel eXtreme Performance Users Group

- independent users group
- provide a forum for the free exchange of information
- enhancing the usability and efficiency of HPC & AI/DL workloads
- computing systems using Intel® architecture

- Fosters technical **collaboration**...
  - System architecture beyond the processor (memory, interconnect)
  - Software tools and programming models
  - New workloads (HPC, data analytics, AI, visualization, etc.)
- Freely exchanging **best practices**, experiences worldwide (open to the public)
- Strong technical support from **Intel experts**

www.ixpug.org

IXPUG Event Momentum

Jul'13: 1st IXPUG Conf (Texas)

Jul'14 IXPUG Annual Conf (Texas)

Apr'14: IXPUG Workshop (Texas)

Nov'14 IXPUG Bird of Feather

Sep'15: IXPUG Annual Conf (US)

Jul'15: ISC IXPUG Workshop & BoF

IXPUG Bird of Feather Nov'15

Mar'16: IXPUG Annual Conf (Europe)

Sep'16: IXPUG Annual Conf (US)

Jun'16: IXPUG Workshop (Russia)

Jun'16: IXPUG KNL Launch Tutorial

Nov'16 IXPUG Bird of Feather

Nov'16 Intel HPC Dev Con

Jul'16 IXPUG Workshop & BoF

May'17: IXPUG Visualization Workshop (US)

Jun'17: IXPUG Workshop (Russia)

Nov'17: SC'17 Bird of Feather

Apr'17: IXPUG Annual Conf (UK)

Sep'17: IXPUG Annual Conf (US)

Jul'17: IXPUG Workshop & BoF

Jan-Oct '18 Working Groups (8 total)

Nov'18: SC'18 Birds of Feather

IXPUG Working Groups

Jan'18: IXPUG Workshop

Mar'18 IXPUG Annual Conf (Italy)

Apr'18: IXPUG Workshop (Saudi Arabia)

July'18: IXPUG Visualization Workshop (US)

Jul'18: IXPUG Workshop

Sep'18 IXPUG Annual Conf (Oregon)

Jan'19: IXPUG Workshop

Jun'19: IXPUG Workshop

May'19: IXPUG In Situ Analysis Hackathon (US)

38+ events; **376+** publications; **600+** members

All technical presentations and recordings are posted on www.ixpug.org

How to Get Involved

Join, Engage and Share:

- Connect with us on Twitter **@IXPUG1**

- Become a member and encourage others to join, by registering at www.ixpug.org

- Contribute by sharing your learnings and experiences in using Intel architecture

- Attend an IXPUG Conferences, Workshops, Birds-of-Feather, ~monthly Working Group, etc. meeting

- Post a question, share a technique, best practices, etc. on the "Discussion" board at

   https://www.ixpug.org/discussion

Additional Opportunities:

- Join the IXPUG Steering Committee

- Volunteer as a "Reviewer" by sending an email to info@ixpug.org

www.ixpug.org

Image courtesy of CERN

This Years Topics

Machine Learning / AI

Applications

Visualization

Programming Models

Memory Hierarchy

New Technologies

CPU

FPGA

NVRAM, HBM

Quantum Computing

Image courtesy of CERN

HPC Asia 2020

International Conference on
High Performance Computing in Asia-Pacific Region
Fukuoka, Japan, Jan. 15 – 17, 2020

IXPUG
INTEL EXTREME PERFORMANCE USERS GROUP

IXPUG Workshop at HPC Asia 2020
International Conference on HPC in Asia-Pacific Region

IXPUG Workshop at HPC Asia 2020 is an open workshop on high performance computing applications, systems, and architecture with Intel technologies. The workshop aims to bring together software developers and technology experts to share challenges, experiences and best-practice methods for the optimization of HPC, Machine Learning, and Data Analytics workloads on Intel architecture.

Conference Date: January 17, 2020 (Day-3 of HPC Asia 2020)
Location: Fukuoka, Japan
Call for Papers: Coming Soon!