

# TUTORIAL: AIDA-TLU/EUDAQ2

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DESY

*Thanks to P. Schütze and F. Diegritz for their help preparing the tutorial  
And Tamar for helping with the shipping*

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# THAT'S WHAT YOU SIGNED UP FOR

- Listening to my (short) introduction
- Getting and installing all the software pieces
- Starting, initializing and configuring with EUDAQ2
- Using the TLU
- How to trigger with the AIDA-TLU
- Testing the different DUT interfaces
- A short coffee break :)

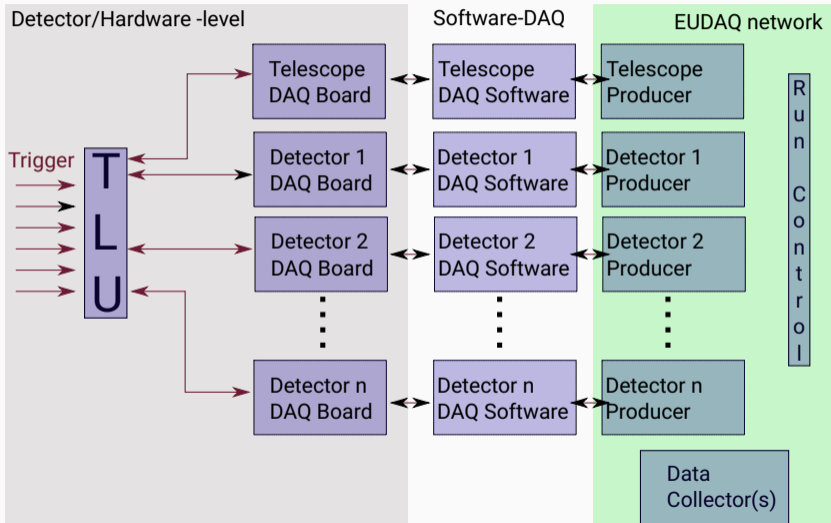
## A FEW COMMENTS ON THE TUTORIAL

- All scripts provided should be executed from the top folder of `bttb8_daq_tutorial`
- You need to have a qt5 development version installed
- If you encounter trouble installing carefully check the cmake outputs
- For the sake of simplicity please do not change any install paths and install ipbus to `/opt/cactus/`

- Successful test beams requires best possible knowledge about the DAQ system that will be used.
- Last years [tutorial](#) from which I took quite some stuff
- I think (hope) I've prepared too much material - if we will not finish within the time, that's nothing to worry about. If we finish faster - we can discuss more details.
- **The tutorial relies on you: Ask whatever you want to ask!**

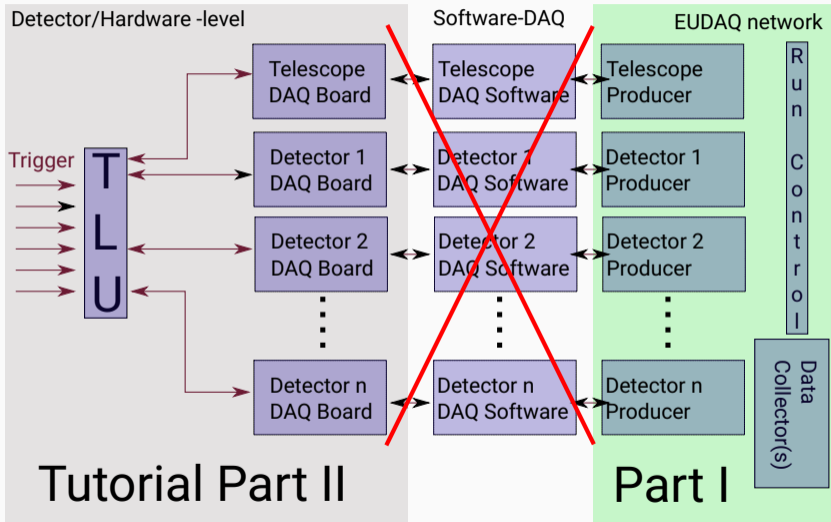
# A TYPICAL TEST BEAM DAQ LAYOUT

- Reference telescope
- Triggering logic
- DAQ network
- N-DUTs



# A TYPICAL TEST BEAM DAQ LAYOUT

- Reference telescope
- Triggering logic
- DAQ network
- N-DUTs
- We will ignore all device specific DAQ software
- We will assume that the DAQ Boards have an interface to the TLU

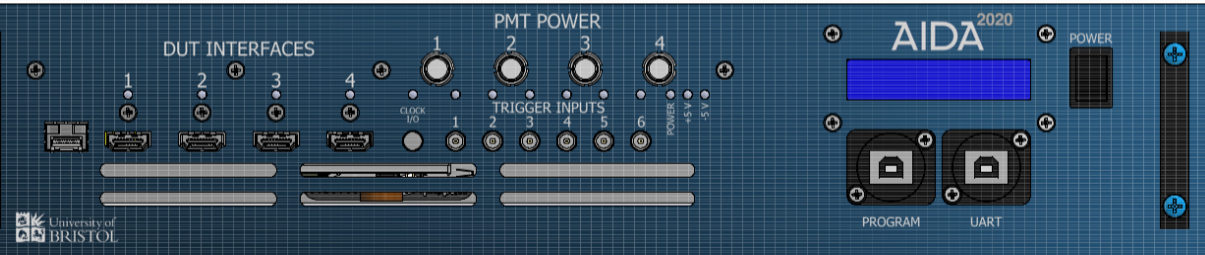


# AIDA-TLU - SYNCHRONIZING DIFFERENT DETECTORS

- TLU = TriggerLogicUnit
- 6 configurable inputs to create a trigger
- 4 HDMI-differential DUT interfaces

## DUT interface modes

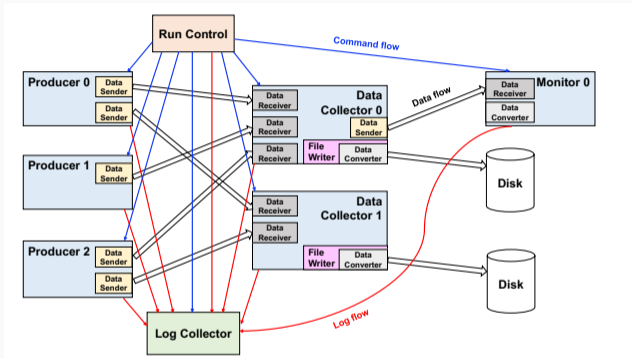
- Trigger, ID (and veto)
- Trigger, clock and  $T_0$
- Trigger (and veto)



# EUDAQ2 - THE TELESCOPE DAQ SOFTWARE

Modular DAQ framework with components running in the same network

- One RUNCONTROL
- One (optional) LOGCOLLECTOR
- One PRODUCER per hardware device
- One to many DATACOLLECTORS to store data
  - Event-id sorted
  - Trigger-id sorted
  - Unsorted
- Optional MONITOR
  - User: MYFANCYSENSORPRODUCER and MYFANCYSENSORRAW2STDEVENTCONVERTER



EUDAQ2 paper



# Part I: EUDAQ2 in a nutshell

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# INSTALLING THE SOFTWARE

```
1 # if not yet done clone the repository for the tutorial
2 git clone https://github.com/lhuth/bttb8_daq_tutorial.git
3 cd bttb8_daq_tutorial
4
5 # driver to talk to the TLU
6 git clone https://github.com/ipbus/ipbus-software.git
7 #Follow instructions on https://ipbus.web.cern.ch/ipbus/doc/user/html/software
   /install/compile.html
8 # source the download_eudaq script
9 cd $PATH_TO_BTTB_DAQ_TUTORIAL
10 source download_eudaq.sh
11 cd eudaq && mkdir build && cd build
12 cmake -DUSER_TLU_BUILD=on ..
13 make install -j4
14
15 cd ../..
16 source scripts/setup.sh
```

# GETTING STARTED

```
1 cd $PATH_to_EUDAQ/bin
2 ./euRun &
3
4 ./euLog &
5 ./euCliProducer -n AidaTluProducer -t aida_tlu &
6 # Any other device can be added later on via
7 # ./euCliProducer -n <ModuleName> -t <givenName> -r <IP>:<port>
```

# INITIALIZE THE SYSTEM

Take a look at the configs/example.init, load it and click init

```
1 [RunControl]
2 #Nothing to be done here
3
4 [LogCollector.log]
5 EULOG_GUI_LOG_FILE_PATTERN = myexample_$(date +%Y%m%d).log
6
7 [Producer.my_pd0]
8 EXO_DEV_LOCK_PATH = /tmp/mydev0.lock
9
10 [Producer.my_pd1]
11 EXO_DEV_LOCK_PATH = /tmp/mydev1.lock
```

## CONFIGURE THE SYSTEM

Take a look at the configs/example.conf, load it and click conf

```
1 [RunControl]
2 EXO_STOP_RUN_AFTER_N_SECONDS = 60
3 EUDAQ_CTRL_PRODUCER_LAST_START = my_pd0
4 EUDAQ_CTRL_PRODUCER_FIRST_STOP = my_pd0
5
6 [Producer.my_pd0]
7 EUDAQ_DC = my_dc # used data collector
8 EXO_PLANE_ID = 0
9 EXO_DURATION_BUSY_MS = 10
10 EXO_ENABLE_TRIGERNUMBER = 1
11 EXO_DEV_LOCK_PATH = mylock0
12
13 [DataCollector.my_dc]
14 EUDAQ_MN = my_mon #monitor that receives data
15 EUDAQ_FW = native
16 EUDAQ_FW_PATTERN = run$3R_-$12D$X
17 EUDAQ_DATACOL_SEND_MONITOR_FRACTION = 10
```

# START/STOP A RUN

eudaq Run Control v2.4.2-11-g73b0feb6

State:  
**Current State: Running**

Control

Init file:

Config file:

Next RunN:

Log:    LogConfigs

ScanFile

Run Number: 91      my\_dc:DataCollector: 198 Events  
my\_mon:Monitor: 19 Events      my\_pd1:Producer: 176 Events  
my\_pd0:Producer: 1731 Events

**Connections**

type	name	state	connection	message	information
LogCollector	log	RUNNING	tcp://127.0...	Started	<_SERVER> tcp://39789
DataCollector	my_dc	RUNNING	tcp://127.0...	Started	<EventN> 198 <MonitorEventN> 19.000000 <_SERVER> tcp://34419
Monitor	my_mon	RUNNING	tcp://127.0...	Started	<EventN> 19 <_SERVER> tcp://37771
Producer	my_pd1	RUNNING	tcp://127.0...	Started	<EventN> 176
Producer	my_pd0	RUNNING	tcp://127.0...	Started	<EventN> 1731

# SCANNING - AUTOMATED DATA TAKING

```
1 [global]
2 repeatScans = 1
3 allowNested = 0 # overwrites local nested arguments
4 #configPrefix = "path/to/folder/scanned" // optional
5 timeBasedScan = 1 # 1 = true :)
6 timePerStep = 10 #in second
7 nEventsPerStep = 200
8
9 [0]
10 default = 0
11 start = 1
12 stop = 2
13 step = 1
14 name = Producer.my_pd0
15 #eventCounter = Producer.my_pd0
16 parameter = EXO_PLANE_ID
17
18 [4]
19 nested = 1
20 default = 0
```

# **Part II: The AIDA Trigger Logic Unit**

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# GETTING STARTED

- We need be two groups from now on
- And we need one laptop that connects to the TLU network
- Each team will have one TLU
- TLU-IP: 192.168.200.30
- Laptop-IP: 192.168.200.1
- Try to ping the TLU
- Start EUDAQ+TLU

```
cd $PATH_to_EUDAQ
./startup_tlu.sh
```



# INITIALIZE THE TLU

```
1 [Producer.aida_tlu]
2 # you can use this to track your changes, e.g. using the date
3 initid = 20180925
4 TLUmod= "1e"
5 # Path on the PC with TLU Producer and relative path is starting path euRun!
6 ConnectionFile = "file:///home/lhuth/bttb8_daq_tutorial/eudaq/user/eudet/misc/
   hw_conf/aida_tlu/aida_tlu_connection.xml"
7 # ControlHub is recommended for Ubuntu, the name is the name in the connction
   file
8 DeviceName = "aida_tlu.controlhub"
9 #DeviceName = "aida_tlu.udp"
10
11
12 # Set CONFLOCK to 1 to configure clock, which is necessary after a power
   cycle
13 CONFLOCK = 1
14 # Path to clock file
15 CLOCK_CFG_FILE = "/home/lhuth/bttb8_daq_tutorial/hw_conf/aida_tlu/
   aida_tlu_clk_config.txt"
16 skipini = 0 # Set skipini to 1, if you want to skip the init-step
```

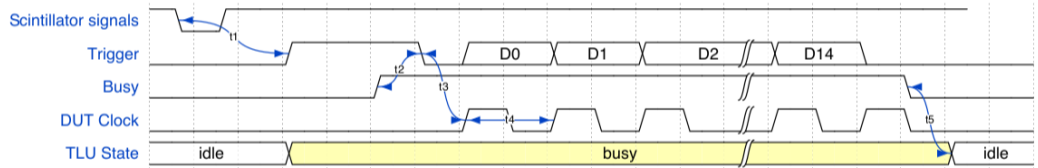
# CONFIGURE THE TLU

```
1
2 [Producer.aida_tlu]
3 verbose = 0
4 confid = 20181002
5 skipconf = 0
6
7 # delay start in ms
8 delayStart = 0
9
10 #####
11 # DUT IN/OUTPUT
12
13 # Mask: 0 CONT, 1 SPARE, 2 TRIG, 3 BUSY (1 = driven by TLU, 0 = driven by DUT)
14 # EUDET mode: 7
15 HDMI1_set = 0x7
16 HDMI2_set = 0x7
17 HDMI3_set = 0x7
18 HDMI4_set = 0x7
19
20 # same as above for the clock line, 1 = AIDA mode, 2 = FPGA
```

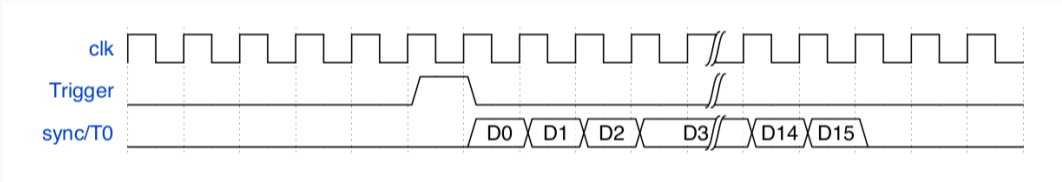
# DUT-Interface modes

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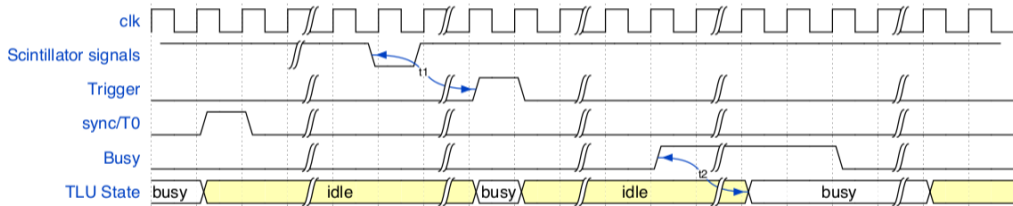
# CLASSICAL EUDET-MODE



# MIXED-MODE



# FULL AIDA-MODE



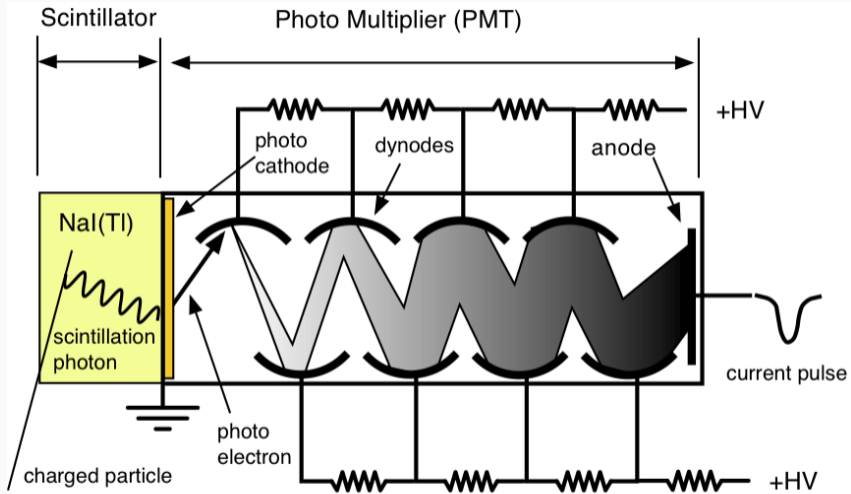
”Simply” connect the TLU to the signal, configure auto-triggers and study the impact on the oscilloscope. We have two little HDMI to LEMO converters that you can use



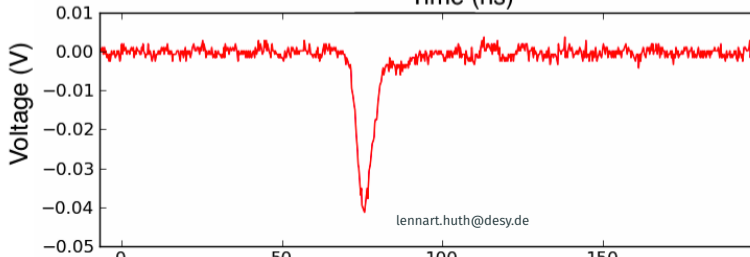
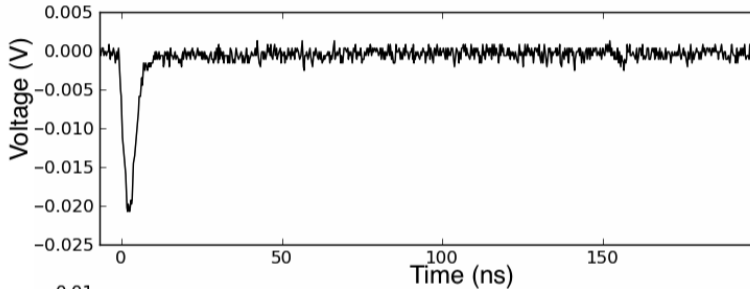
# **Part II-a: Setting trigger thresholds and control voltages**

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# SETTING UP SCINTILLATORS



# SETTING UP SCINTILLATORS



# CONFIGURING THE PMTs

- PMTX\_V to define the control voltage - ranges from 0-1V and should be 0.8V as a default
- DACThresholdX to set the threshold in V, typically 0.04V

```
1 PMT1_V =0.8
2 PMT2_V =0.8
3 PMT3_V =0.8
4 PMT4_V =0.8
5
6 DACThreshold0 = -0.04
7 DACThreshold1 = -0.04
8 DACThreshold2 = -0.04
9 DACThreshold3 = -0.04
10 DACThreshold4 = -0.20
11 DACThreshold5 = -0.20
```

## SETTING VALID TRIGGER INPUTS

The TLU has 6 inputs, resulting in  $2^6 = 64$  potential trigger combinations  $\rightarrow$  2 32 bit words for configuration.

Find the configuration to trigger on:

- Only  $I_0 + I_1$
- $I_0 + I_3 + I_5$  OR  $I_1 + I_4$
- $\bar{I}_0 + I_1 + I_3$
- $I_0 + I_3 + I_5$  AND  $I_1 + I_4$

Now let's see what the following combinations will trigger on (only LSBs):

- 0X105
- 0XC

## SETTING VALID TRIGGER INPUTS - SOLUTIONS

- Only  $I_0 + I_1$
- $I_0 + I_3 + I_5$  OR  $I_1 + I_4$
- $\bar{I}_0 + I_1 + I_3$
- $I_0 + I_3 + I_5$  AND  $I_1 + I_4$
  
- 0X105
- 0XC

## **Part II-b: Raspberry Pi as DUT**

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# USING THE AUTO TRIGGER

- connect via: `ssh pi@192.168.200.111(113)`
- execute `sudo ./DutDummy 21 <Busy time> 20 0`



# CHECK THE RATES FOR DIFFERENT MODES WITH DIFFERENT DELAYS ON THE RASPBERRY PI