22nd International Workshop on Radiation Imaging Detectors



Contribution ID: 198

Type: Poster presentation only

Multi-threshold Window Discriminator Based on SAR Logic

The new class of X-ray imaging detectors allows us to capture an image in various energy ranges in one shot [1]. This technique is called X-ray color imaging, and it is becoming a promising method in many applications such as medical imaging, computed tomography, and material testing [2]. To measure the energy spectrum in one shot, discriminant circuits need to be integrated into the pixel front-end electronics. Several solutions of inpixel discriminators exist. However, current designs suffer from a low number of discrimination bins and need to adjust each discrimination threshold separately, leading to relatively complicated calibration procedures [3].

This work will introduce a novel design of a multi-threshold window discriminator based on successive approximation register logic. This circuit realizes in-pixel binning to ten equidistant windows. Two variables are used for tuning the multi-threshold window discriminator: offset of first window and width of windows. Setting these parameters allows the user to fulfill the need for the target application. The results will be presented.

- [1] M. C. Veale, et al., Synchrotron Radiat. News, 31 (2018), 28–32
- [2] J. P. Ronaldson, et al., IEEE Nuclear Science Symposium Conference Record, (2011)
- [3] R. Ballabriga, et al., Journal of Instrumentation, 8 (2013), no. 2,

Authors: JIRSA, Jakub (Faculty of Nuclear Sciences and Physical Engineering, Czech Technical University in Prague, Czech Republic, Faculty of Electrical Engineering Czech, Technical University in Prague, Czech Republic); HAVRANEK, Miroslav (Faculty of Nuclear Sciences and Physical Engineering, Czech Technical University in Prague, Czech Republic, Faculty of Electrical Engineering Czech, Technical University in Prague, Czech Republic); MARCISOVSKY, Michal (Czech Academy of Sciences (CZ), Faculty of Nuclear Sciences and Physical Engineering, Czech Technical University in Prague, Czech Republic, Faculty of Electrical Engineering Czech, Technical University in Prague, Czech Republic); Dr JIRI, Jakovenko (Faculty of Electrical Engineering Czech, Technical University in Prague, Czech Republic)

Presenter: JIRSA, Jakub (Faculty of Nuclear Sciences and Physical Engineering, Czech Technical University in Prague, Czech Republic, Faculty of Electrical Engineering Czech, Technical University in Prague, Czech Republic

Session Classification: Poster session 2

Track Classification: Front end electronics and readout