Contribution ID: **2**   Type: **not specified**

Real Time Server Monitoring and Fixed Point Inference in FPGA

Tuesday, 13 August 2019 14:10 (7 minutes)

The project dealt with real time monitoring of the server hosting the FPGA for Convolution Neural Network inference for the Proto-DUNE Project. I also investigated methods to deal with the numerical overflow of the weights and activation while working with fixed point arithmetic in FPGA.

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