

TWEPP 2010 Topical Workshop on Electronics for Particle Physics

Tuesday, September 21, 2010

ASICs: Parallel Session A1 - Aula (9:50 AM - 10:40 AM)

-Conveners: Luciano Musa

time	[id] title	presenter
9:50 AM	[M6] Wideband (8 GHz GBW) pulse amplifier for the integrated camera of the Cherenkov Telescope Array	Dr GASCON, David
10:15 AM	[M7] Design and Assessment of a Robust Voltage Amplifier with 2.5 GHz GBW and >100 kGy Total Dose Tolerance	Mr VERBEECK, Jens

ASICs: Parallel Session A1 - Aula (11:00 AM - 1:05 PM)

-Conveners: Luciano Musa

time	[id] title	presenter
11:00 AM	[M1] Developments on SPIROC family chips (SiPM Integrated Read-Out Chip): Dedicated very front-end electronics for an ILC prototype hadronic calorimeter with SiPM read-out	Mr RAUX, Ludovic
11:25 AM	[M2] Readout Electronics for Low Dark Count Geiger Mode Avalanche Photodiodes Fabricated in Conventional HV-CMOS Technologies for Future Linear Colliders	Mrs VILELLA, Eva
11:50 AM	[M7] Front end electronics for silicon strip detectors in 90nm CMOS technology; advantages and challenges	Mr KAPLON, Jan
12:15 PM	[M8] A Front-end ASIC for the readout of the PMT in the KM3NeT Detector.	Mr GAJANANA, Deepak
12:40 PM	[M9] A 25 GS/s sampling ASIC in a 130nm CMOS technology.	Mr GENAT, Jean-Francois C.

ASICs: Parallel Session A2 - Aula (3:05 PM - 4:20 PM)

-Conveners: Ken Wyllie

time	[id] title	presenter
3:05 PM	[M1] A Silicon Pixel Readout ASIC with 100 ps time resolution for the NA62 Experiment	Mr MAZZA, Giovanni
3:30 PM	[M7] GOSSIPO-3: Measurements on the Prototype of a Read-Out Pixel Chip for Micro-Pattern Gas Detectors	Dr KRUTH, Andre Konrad
3:55 PM	[M0] A 3D vertically integrated deep n-well CMOS MAPS for the SuperB Layer0	Dr TRAVERSI, Gianluca

ASICs: Parallel Session A2 - Aula (4:45 PM - 6:01 PM)

-Conveners: Ken Wyllie

time	[id] title	presenter
4:45 PM	[M5] Characterisation Of The NA62 GigaTracker End Of Column Readout ASIC	Dr NOY, Matthew
5:10 PM	[M05] The FE-I4 Pixel Readout System-on-Chip for ATLAS Experiment Upgrades	HEMPEREK, Tomasz

5:35 PM	[132] ULTIMATE: a High Resolution CMOS Pixel Sensor for the STAR Vertex Detector Upgrade	Dr HU-GUO, Christine
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Wednesday, September 22, 2010

ASICs: Parallel Session A3 - Aula (9:50 AM - 10:40 AM)

-Conveners: Luciano Musa

time	[id] title	presenter
9:50 AM	A07 [A07] Radiation-hard ASICs for optical data transmission in the first phase of the LHC upgrade	Prof. GAN, K.K.
10:15 AM	A08 [A08] Signal processing for High Granularity Calorimeter: amplification, filtering, memorization and digitalization	ROYER, Laurent

ASICs: Parallel Session A3 - Aula (11:00 AM - 1:05 PM)

-Conveners: Luciano Musa

time	[id] title	presenter
11:00 AM	A09 [A09] A 256 channel 8 bit current digitizer ASIC for the Belle-II PXD	Mr KNOFF, Jochen
11:25 AM	A10 [A10] The GBT-Serdes ASIC prototype: test features and preliminary results	Mr MOREIRA, PAULO
11:50 AM	A16 [A16] The Design for Test Architecture in Digital Section of the ATLAS FE-I4 Chip	Dr ZIVKOVIC, Vladimir
12:15 PM	A15 [A15] A 16:1 Serializer ASIC for Data Transmission at 5 Gbps	GONG, Datao
12:40 PM	A18 [A18] Switched capacitor DC-DC converter ASICs for the upgraded LHC trackers	Mr BOCHENEK, Michal