

# MyBiz

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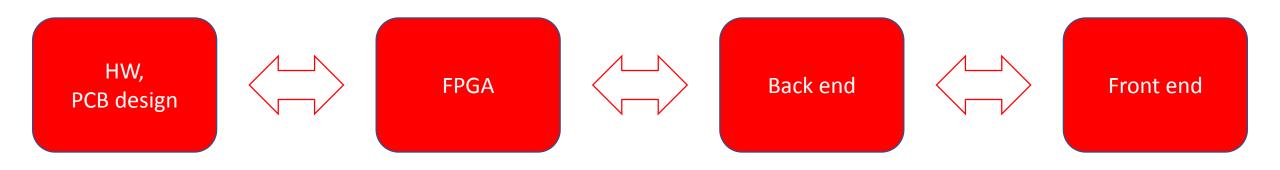






### MyBiz the team

over 100 Years of combined experience



Jozef Stefan Institute
( fellow, Spectroscopy, Patent author )
Instrumentation Technologies
( Senior SW Development Engineer )
HERMES SoftLab
( Software Engineering Consultant )

NASA - JPL (cube satellite)
(principal FPGA Engineer)
GSI - (500 MHz design MRSPEC)
(principal FPGA engineer)
TopIT
( Senior Software Professional)

### MyBiz

#### hardware

**INPUT** 

4 analog channels, 1GS/s, 8 bit 1 digital channel, 1GS/s, 16 bit

**OUTPUT** 

2 analog channels, 250MS/s, 16 bit 1 digital channel, 1GS/s, 16 bit

**FPGA** 

UltraScale+ ZU2 - ZU15 103k - 747k logic cels

**BUFFER** 

2 GByte DDR4

**CONECTIVITY** 

ETH 1 Gbit USB 3.0

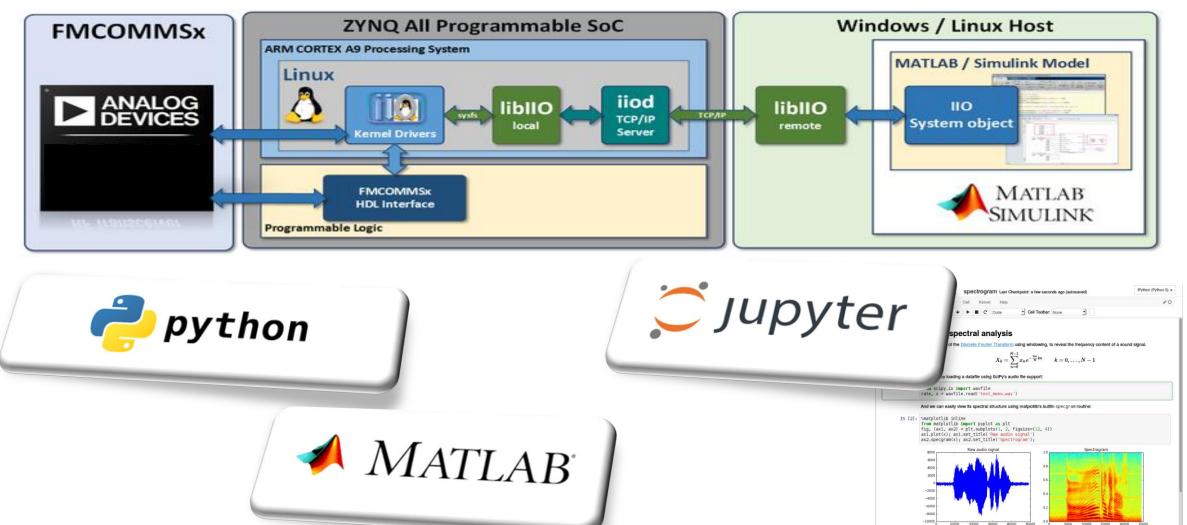
**COMPATIBILITY** 

Industrial I/O python
Jupyter matlab



## MyBiz

### SW compatibility





Highly versatile and configurable realtime processing based on a multisignal data accusation platform

custom services and applications available