Porting GarNet to HLS4ML

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Problem Statement

- Porting GarNet layer to HLS4ML in order to test its performance on FPGA.
- To find out how the GarNet layer can be simplified so that it fits in the limited resource available

Status

- ➔ C++ implementation of Garnet is complete.

Plan for Tomorrow

- Writing code for HLS4ML to read GarNet's architecture and weights file.