

GE1/1 & GE2/1 Power System

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CMS GEM Workshop October 3rd, 2019



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HV System

- Overview
- Status
- Issues Debugging
- LV System
 - Overview
 - Status
 - Issues Debugging



HV System



GE11 HV System Overview





GE21 HV System Overview



GE1/1-GE2/1 HV: CURRENT STATUS

→ Cables through new cable chain (-X4 near and far)

- **J** GE1/1 Negative Endcap DONE
- GE1/1 Positive Endcap: TO DO for installing the same cables on the Positive Endcap we have to wait for the new cable chain installation
- **√**□ <u>GE2/1</u> Negative Endcap DONE

USC-UXC cables (~100 m): TO DO

- 36(GE1/1)+72(GE2/1)=108 thick cables (4x10 conductors) to be routed
- waiting for the installation of new trays along the path for GEM HV cables



HV System Issues&Debugging

Problem: We have observed in laboratory that sometimes HV channels become *unplugged*.

- if the channel was ON it remains ON although it is not possible to control/monitor it anymore
- A simple *clear alarm* does not solve the problem since the faulty condition persists
- The solution that worked most of the times (but not always probably due to some capacity not fully discharged during the cycle) was to power cycle the mainframe
- So far the average cross section is about 1 channel in error every 6-7 months per board (anyway the frequency was not constant with time) → about 1 error every 6 days with 32 boards (GE1/1)

→ Possible explanation by CAEN: the communication is based on the 50Hz frequency of the 220V. If it's not very stable, the motherboard and the channels communication might mismatch. Hence the motherboard is no longer able to correctly interpret the packages coming from the channels → communication error between them (unplugged)

Current solution: Firmware 2.06 for A1515TG was released by CAEN in July, with 2 main changes:

- Automatic procedure to recover unplugged channels without switching off the mainframe
 → complemented from DCS side to lock actions by the user during this procedure
 - The other 6 channels of the involved group are ramped down "normally"
 - The unplugged channel is killed and rebooted by cutting off its power for 10 seconds → this should restore the unplugged channel
- 2. In case of unplugged, the synchronization is reset

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Firmware 2.06 used in all HV board in 904 lab since 15 July → no more "unplugged" errors observed until now



LV System

GE11 & GE21 LV System Overview

1 LV rack

USC

with 1 mainframe

S4F03



2 PATCH PANELS

UXC

- in rack X4N19 (Negative Endcap, Near Side)
- in rack X4N55 (Positive Endcap, Near Side)



Negative endcap

4 LV racks

- X2V33 and X5V32 (Negative Endcap, Near Side)
- X2J33 and X5J32 (Positive Endcap, Near Side)



Positive endcap





GE1/1-GE22/1 LV – UXC rack





48V Service Distr. Unit



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48 V power





GE1/1-GE2/1 LV: CURRENT STATUS





Summary of HCAL case:

In 2018 following a **power interruption** in CMS an HCAL power supply is suspected of having produced a voltage spike that killed the FEASTMP



Could this happen also with A3016 boards?

We discovered **2 ways** to produce transients in laboratory on **3016(HP) boards**:

- 1. ON command to MAO channel already ON (with MAO firmware $\leq 2.4 \rightarrow$ problem fixed in next fw version)
 - Short "glitch" on the input power (220 V) of the MAO, obtained acting on the button (circuit breaker) of the 400/220 V transformer



A3016HP (GE1/1)

LV System Issues&Debugging

SOLUTION

- CAEN will provide an upgraded version of the LV boards with an additional protection circuit
 - It cuts the voltage above a fixed threshold
 - The threshold is determined by resistors in the protection circuit (→ can be modified by caen)
- 1st board (#175) declared with threshold of 11.8 V successfully tested in 904 lab
 - Verified that transients are cut at 11.6-11-8 V in all 6 channels
 - Verified that all FEASTMPs have survived (120 trials in total)

→ ALL BOARDS for P5 will be equipped with this protection circuit





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Backup

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* Same mapping for Positive and Negative Endcap



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CMS

HV Board (Slot)	SC Powered	HV Board (Slot)	SC Powered		SC	Layer	Board channel
0	SC 08 / SC 09	0	SC 10 / SC 11		12	Ly 1 & Ly 2	Channel 0-6
1	SC 06 / SC 07	1	SC 12 / SC 13	ľ			("group 1")
2	SC 04 / SC 05	2	SC 14 / SC 15		13	Ly 1 & Ly 2	Channel 7-13 ("group 2")
3	SC 02 / SC 03	3	SC 16 / SC 17	-	SC	Layer	Board channel
4	SC 36 / SC 01	4	SC 18 / SC 19		36	Lv 1 & Lv 2	Channel 0-6
5	SC 34 / SC 35	5	SC 20 / SC 21			-, - ~ -, -	("group 1")
6	SC 32 / SC 33	6	SC 22 / SC 23		01	Ly 1 & Ly 2	Channel 7-13
7	SC 30 / SC 31	7	SC 24 / SC 25				("group 2")
8	SC 28 / SC 29	8	SC 26 / SC 27		6/7	8/9 10/11	
		,		4/5	1		
in M to N	lainframe Near Side	in Mainfram to Far Side	ne 1/36 34/3	5			14/15 16/17 18/19 20/21
* Same manning for	in 1 H	V rack		32/33	30/31	24/25	22/23
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RACK in USC RACK on cavern balcony Near Side Front Back Front Cavern Front New Wall Cable Chain **CAEN** 9 HV boards mainframe Patch Panel Patch Panel To Near Cable Type 1 GE1/1 GE1/1 Side $\uparrow \uparrow \uparrow$ Patch Patch Panel Patch Panel To detectors Panel GE2/1 GE2/1 SC 1-9, SC 28-36 CAEN Patch Panel Patch Panel 9 HV boards mainframe GE2/1 GE2/1 Cable Type 1 To Far To detectors $\uparrow \uparrow \uparrow \uparrow$ Side SC 10-27 Patch Panel **RACK on cavern balcony Far Side** similar

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GE2/1 HV System for 1 endcap



GE2/1 HV patch panel-1 "Near" side (USC)



GE2/1 HV patch panel-2 "Near" side (USC)



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LV System Mapping - USC

Board (A1676 Slot *)	Target Endcap	Target Rack in UXC	SC Powered
2	Positive	X5J32	SC 01 to SC 18
5	Positive	X2J33	SC 19 to SC 36
8	Negative	X5V32	SC 01 to SC 18
11	Negative	X2V33	SC 19 to SC 36

* Slots are numbered from slot 0



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	LV Sys	tem	Map	oing -	– UXC	X2 Le	evel
Crate	LV Board (Slot)	LV channel	SC Powered	Crate	LV Board (Slot)	LV channel	SC Powered
1	1	0	SC 19 – Ly 1	1	13	0	SC 28 – Ly 1
(EasyCrate2 in DCS)		1	SC 19 – Ly 2	(EasyCrate2 in DCS)		1	SC 28 – Ly 2
11 0 00)		2	SC 20 – Ly 1	11 2 63 /		2	SC 29 – Ly 1
		3	SC 20 – Ly 2			3	SC 29 – Ly 2
		4	SC 21 – Ly 1			4	SC 30 – Ly 1
		5	SC 21 – Ly 2			5	SC 30 – Ly 2
	5	0	SC 22 – Ly 1		17	0	SC 31 – Ly 1
		1	SC 22 – Ly 2			1	SC 31 – Ly 2
		2	SC 23 – Ly 1			2	SC 32 – Ly 1
		3 SC 23 – Ly 2		3	SC 32 – Ly 2		
		4	SC 24 – Ly 1			4	SC 33 – Ly 1
		5	SC 24 – Ly 2			5	SC 33 – Ly 2
	9	0	SC 25 – Ly 1	2	1	0	SC 34 – Ly 1
		1 SC 25 – Ly 2 (EasyCrate3				1	SC 34 – Ly 2
		2	SC 26 – Ly 1	11 003)		2	SC 35 – Ly 1
		3	SC 26 – Ly 2			3	SC 35 – Ly 2
		4	SC 27 – Ly 1			4	SC 36 – Ly 1
		5	SC 27 – Ly 2			5	SC 36 – Lv 2

* Same mapping for Positive and Negative Endcap

in 1 UXC LV rack

′or

	LV Sys	tem	Map	oing -	– UXC	X5 Le	evel
Crate	LV Board (Slot)	LV channel	SC Powered	Crate	LV Board (Slot)	LV channel	SC Powered
1	1	0	SC 01 – Ly 1	1	13	0	SC 10 – Ly 1
(EasyCrate2		1	SC 01 – Ly 2	(EasyCrate2		1	SC 10 – Ly 2
11 003)		2	SC 02 – Ly 1	11 003)		2	SC 11 – Ly 1
		3	SC 02 – Ly 2			3	SC 11 – Ly 2
		4	SC 03 – Ly 1			4	SC 12 – Ly 1
		5	SC 03 – Ly 2			5	SC 12 – Ly 2
	5	0	SC 04 – Ly 1		17	0	SC 13 – Ly 1
		1	SC 04 – Ly 2			1	SC 13 – Ly 2
		2	SC 05 – Ly 1			2	SC 14 – Ly 1
		3	SC 05 – Ly 2			3	SC 14 – Ly 2
		4	SC 06 – Ly 1			4	SC 15 – Ly 1
		5	SC 06 – Ly 2			5	SC 15 – Ly 2
	9	0	SC 07 – Ly 1	2	1	0	SC 16 – Ly 1
		1	SC 07 – Ly 2	(EasyCrate3		1	SC 16 – Ly 2
		2	SC 08 – Ly 1	11 2 6 5 7		2	SC 17 – Ly 1
		3	SC 08 – Ly 2			3	SC 17 – Ly 2
		4	SC 09 – Ly 1			4	SC 18 – Ly 1
		5	SC 09 – Ly 2			5	SC 18 – Ly 2
				in 1 (JXC LV rack		010 m 01

* Same mapping for Positive and Negative Endcap

′or





* All modules in the rack need to be accessed only from the front

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DCS



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Unplugged Procedure

Procedure to be implemented in the new firmware of the HV boards to recover unplugged channels:

Step			Error in DCS	Error in ssh / GECO /
1	Channel X becomes unplugged		Unplugged	Unplugged
2	The other 6 channels are switched off rDwn set, following the offOrder if in G	"normally" (i.e. with the EM mode)	Unplugged	Unplugged
3	 The unplugged channel is killed by cutt channel for 10 seconds During this 10 s the rDown depend line 	ing off the power of the son the impendence of the	PwrFail	PwrFail
4	 After this 10 s power is repristinated as ground the rDown depends on the load The microcontroller of the channel at the hopefully with a working communication of the base 	nd the output is put to his point is rebooted, and on with the microcontroller	Unplugged + PwrFail	Unplugged
5	All 7 channels are now OFF		Unplugged + PwrFail	Unplugged
6	It is necessary to acknowledge the probefore powering the channel group	blem by clearing the alarm		
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- \rightarrow Not able to produce them with Imon=0
- ightarrow More easily produced at higher load

GEB:

- Old version GEB
- «rejected» FEASTMPs used, as they could be damaged in the test
- 8 of 9 FEASTMP with correct output voltage
- only feast FQC "wrong" value (1V instead of 1.2V)

→ Anyway we suppose that the right output voltage of the FEASTMP has no influence on the test (no electronics powered)

No sense wire used, V_{GEB} measured here with voltmeter



Test if FEASTMPs are ALIVE



"Health" of all 9 FEASTMPs verified by measuring with the voltmeter if there is voltage on the following 9 points (w.r.t. GEB ground):

- VFAT pos 15 (or similar powered by FEAST QA)
- VFAT pos 23 (or similar by FEAST QB)
- VFAT pos 3 (or similar by FEAST QC)
- VFAT pos 11 (or similar by FEAST QD)
- OH1
- OH2
- OH3
- OH5
- OH6





Remarks

- The user should not try to switch on channels or clear alarms while they're rebooting
 → locking all 7 channels and the clear alarm command could be implemented on DCS side
- In the DCS the presence of the errors "unplugged + pwrFail + unplugged" in a predeterminate sequence indicates that the micro was rebooted because the unplugged error appeared

Summary of HCAL case (1/2)

On June 30th, following a power interruption in CMS the power supply (#130) A3100HB [One channel 8 ÷ 14 V, 600 W]

- was failing to switch on due to OvHVMax errors
- showed Cal-Error error [= error detected in reading the Eprom that contains all the calibration parameters], that could be cleared allowing to switch the channel on again (appeared a few times also later in the lab tests)
- Despite replacing the module, there was no communication with ngFEC, so the affected HEM sectors were lost.

In next slide is what killed them (notably the FEASTMPs) ...

Summary of HCAL case (2/2)



Both CAEN and HCAL (German) were able to reproduce the issue:

- When the channel was switched on, a transient of 20V was produced before the OverHVMax trip turned off the channel
- The length of the transient over 15V with a load of 2A was around 10 ms
- Reproducing the issue is not systematic, it is necessary to repeat a test many times to see the event (about few times over 100 tests as reported by German)

LV System Issues&Debugging

Two possible solutions to protect against voltage transients:

1. CAEN has developed a protection circuit to be installed on LV boards that kills the output (Vcon=0) whenever the Vcon overcomes a fixed threshold Vmax

2 prototypes of A3100HBP with
CAEN protection circuit produced
→ they are currently under test in
collaboration with German (HCAL)
→ It can be implemented also on
A3016HP boards once "approved"



2. HCAL (German) has developed an external protection circuit

- The choice of the components defines the threshold voltage vMax (e.g. 12 V) and the reset voltage (e.g. 10 V)
- If Vcon>vMax a large current (200 A) flows in the circuit (*not* at the load) and causes the module to trip after the tripTime
- The proper operation is restored when the voltage gets below the reset voltage

GEM community also designed a similar (to HCAL) solution. Due to the fact that A3016HP when operated with CAEN MAO system doesn't suffer of spike problems, implementation is staged

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Deisgn of protection circuit #1

- Panel for rack mounting, based on (86 x 47.75) mm PCB with a matrix of 3x2 protection circuits ? (in figure: merged with copper bar for grounding, to save space)
- 2U panels for GE11 and GE21, 3U panels for MEO (2x number of LV channels)
- Difficult implementation for complexity of realization and space \rightarrow the space for such panels was initially not foreseen in the racks





- Panel for rack mounting
- The circuits are inside the rack, perpendicular to the front panel
- On the front panel there are 4x APP45 connectors for each LV channel



Front view (not to scale)



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Side view







Gas System Mapping

- Each gas rack hosts 13 (12 used) gas channels
- 1 gas channel supplies 6 (single) layers of different SCs (to minimize coverage holes in case of failures)

Negative	e Endcap
SC 4 to 9 – Ly 1	SC 22 to 27 – Ly 1
SC 4 to 9 – Ly 2	SC 22 to 27 – Ly 2
SC 10 to 15 – Ly 1	SC 28 to 33 – Ly 1
SC 10 to 15 – Ly 2	SC 28 to 33 – Ly 2
SC 16 to 21 – Ly 1	SC 34 to 3 – Ly 1
SC 16 to 21 – Ly 2	SC 34 to 3 – Ly 2





FSM – Hardware View





D@\$3;1210al9hammage

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Chamber Status Popup Panel









Chamber Status Monitor Panel

	oard St	atus —													STATU	5		-			
HV B	oard ID	:	GE+1/1	HVBro	101		Drf	t Ovc		UnV		1	tatus		Status 0	Meaning off			Show Tr	rendpa	age
Sho	ow Tre	ndpage	e l	JnderTe	mp Ove	erTemp	G1 G1	Top	ĕ	ŏ	ŏ	16 8	overvolt overcur	tage rent	1 2 4 8	on ramping u ramping o overcurre	up down ent	iMo ZC/ ZCI Op	nRange = F Adjust = F Detect = F Mode = F	ALSE = H ALSE = D ALSE = C ALSE = F	iigh Jis Off Tree
Op l FAI	Node LSE		2 d St	atus ramping	up		G2 G2	Fop				4	ramping standby	down /off	32 64	undervolt	taqe trip	UNI	TS ension or pa	rameter I	Unit
read sett oard l	back ting Dp: CAI	EN/GEM	CAEN H	Opt Opt V 01/bo	Mode = FALS Mode = TRUE	6E = Free E = GEM	G3 G3	Fop O Sot O	Ŏ	ĕ	Ŏ	32 1024	undervol calibration	ltage error	256 512 1024 2048	external t internal t calibratio	disable rip n error d		V C Ramp up T	oltage Jurrent Jolown	V uA V/s
	VO	10	Vmon	Imon	OnOff	OnOff	V1	11	RIIP		TrinTime	onOrder	offOrder	iM Range	ZC	ZC	vMax	iMon Det	iMon Real	Tomp	
it 🛛	0.0	0.00	2145.2	2.71	Off	On	0.0	0.00	0	0	0	0	0	FALSE	FALSE	FALSE	0	0	0	0	Dr
Тор	0.0	0.00	891.2	2.66	Off	Off	0.0	0.00	0	0	0	0	0	FALSE	FALSE	FALSE	0	0	0	0	G1
Bot	0.0	0.00	854.2	2.20	Off	Off	0.0	0.00	0	0	0	0	0	FALSE	FALSE	FALSE	0	0	0	0	G1
Тор	0.0	0.00	412.3	1.76	Off	Off	0.0	0.00	0	0	0	0	0	FALSE	FALSE	FALSE	0	0	0	0	G
Bot	0.0	0.00	256.1	1.24	Off	Off	0.0	0.00	0	0	0	0	0	FALSE	FALSE	FALSE	0	0	0	0	G
Тор	0.0	0.00	338.3	0.84	Off	Off	0.0	0.00	0	0	0	0	0	FALSE	FALSE	FALSE	0	0	0	0	G
Bot	300.0	0.26	200.0	0.30	On	Off	0.0	0.00	0	0	0	0	0	TRUE	FALSE	FALSE	0	0	0	0	G
	readbac	c setting	act	ual							rea	adback set	tting						actual		
LV Bo	oard Sta oard ID:	atus —	GE+1/1 L	VBrd0 ^r	1				_	V 0	10	Vcon	Vmon	Imon	OnOff readBack	OnOff Actual	V1	11	TripTime	vMax SoftValu	e
8d Sta	tus	1	on		Show T	rendpag	je	LV Cha	annel	0.000	0.000 k setting	0.000	5.236	0.500	On	On	0.000	0.000 readbag	0 ck setting	0	

Using this panel, user can monitor,

- LV and HV channel DP values connected to the selected chamber
- LV and HV channel status
- LV and HV channel error status
- Relevant LV and HV Board status
- LV and HV Board trend pages
- Channel trend page

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Chamber Settings Panel

HANGE	SETT	INGS	Char	nber Na	me:	+GEMIN	NI 01 -		Chamb	er ID: C	SE+1/1/01	/1	Status Monite
ettings -	Commo	on Set	ttings -	More									
BOARD	setting	ıs ———											
Operat	tina Mo	de FF	REE T	Δ	nnlv	[Board Dp	: CAEN/	GEM_CAE	EN_HV_0	1/board00		Close
opera	ing no		VEL .		ppiy	d	lick "Apply"	to apply setti	ngs				
Channe	l settin	gs ——											(
	OnOff	V 0	V1	10	11	RUP	RDWN	TripTime	Vmon	Imon	IO readBack	SWITCH ALL ON	(uA)
Drfit	Off 🔻	0.0	0.0	0.000	0.000	0	0	0	2145.2	2.7111	0.000		Set Divider Curre to ALL
G1Top	Off 🔻	0.0	0.0	0.000	0.000	0	0	0	891.2	2.6555	0.000	SWITCH ALL OFF	0.0
G1Bot	Off 🔻	0.0	0.0	0.000	0.000	0	0	0	854.2	2.2000	0.000		(V)
G2Top	Off 🔻	0.0	0.0	0.000	0.000	0	0	0	412.3	1.7555	0.000	Save current	to ALL
G2Bot	Off 🔻	0.0	0.0	0.000	0.000	0	0	0	256.1	1.2444	0.000	settings to	0.255
G3Top	Off 🔻	0.0	0.0	0.000	0.000	0	0	0	338.3	0.8444	0.000	Load settings	0.233
G3Bot	On 🔻	300.0	0.0	0.255	0.000	0	0	0	200.0	0.3000	0.255	from file	Set i0 to ALL
	Sum	300.0	(V)	(uA)	(uA)	(V/s)	(V/s)	(s)	5097.2	11.7110	Sum	Load default	
	l	(V)							(V)	(uA)		varues	vlggA

Using this panel, experts or operators can change,

- HV channel DP values connected to the selected chamber
- Relevant HV Board operating mode
- Can save current settings, load settings and load default settings
- Can set divider current and voltages by clicking a single button

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HV Board Status Popup Panel





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HV Board Status Monitor Panel

🊳 Monitor (dis	st_1 - gen	n1; #1)																		-	
MONITO	RING	Co	nnected	Chan	nbers: +0	EMINI 5	& +GEMI	NI 6		 C	N 📃	OFF/ STA	NDBY	RAMPIN	G 🗾 EF	ROR	EXCLUDE	D		Settings	Close
HV Board II BOARD set	D: GE+ tings— ndpage	Unde	erTemp (Ov Channel 0 Channel 1		UnV Trip	0	Status stan	s dby/off dby/off	Channe Channe				0 0	tatus standby/ standby/	off St	ATUS atus M 0 c 1 c 2 r 4 r	fleaning off on ramping up ramping down	Shov iMonRan ZCAdjus ZCDetec	<i>I</i> Trendpage ge = FALSE = High t = FALSE = Dis t = FALSE = Off
OpMode FALSE readback setting		Bd Sta) st pMode = F/ pMode = TF	atus tandby/off ALSE = Free RUE = GEM		Channel 2 Channel 3 Channel 4 Channel 5 Channel 6			0 0 0 0	stand stand stand stand stand	dby/off dby/off dby/off dby/off	Channe Channe Channe Channe Channe	110 110 111 112 112			0 0 0 0	standby/ standby/ standby/ standby/	off off off off 1	8 0 16 0 32 0 64 e 128 r 256 e 512 ii 024 0	overcurrent overvoltage undervoltage external trip max v external disable internal trip calibration error	UNITS Dimen parat	sion or Unit neter Voltage V Current uA Ip/down V/s
Doard Dp. Cr	V0	IO	Vmon	Imo	OnOff n readBack	OnOff Actual	V1	n	RUP	RDWN	TripTime	e onOrder	offOrder	iM Range	ZC Adjust	ZC Detect	vMax SoftValue	048 u iMo e De	unplugged on iMon et Real	Temp	Triptime s
Channel 0	0.0	0.00	0.0	0.14	4 Off	Off	0.0	0.00	0	0	0	0	0	FALSE	FALSE	FALSE	0	0	0	0	Channel 0
Channel 1	0.0	0.00	0.0	0.19	9 Off	Off	0.0	0.00	0	0	0	0	0	FALSE	FALSE	FALSE	0	0	0	0	Channel 1
Channel 2 Channel 2	0.0	0.00	0.0	0.23	3 Off	Off	0.0	0.00	0	0	0	0	0	FALSE	FALSE	FALSE	0	0	0	0	Channel 2 Channel 3
Channel 3	0.0	0.00	0.0	0.30	s Οπ p Off	Off	0.0	0.00	0	0	0	0	0	FALSE	FALSE	FALSE	0	0	0	0	Channel 4
Channel 5	0.0	0.00	0.0	0.50		Off	0.0	0.00	0	0	0	0	0	FALSE	FALSE	FALSE	0	0	0	0	Channel 5
Channel 6	0.0	0.00	0.0	0.8	L Off	Off	0.0	0.00	0	0	0	0	0	FALSE	FALSE	FALSE	0	0	0	0	Channel 6
Channel 7	0.0	0.00	0.0	0.13	3 Off	Off	0.0	0.00	0	0	0	0	0	FALSE	FALSE	FALSE	0	0	0	0	Channel 7
Channel 8	0.0	0.00	0.0	0.26	5 Off	Off	0.0	0.00	0	0	0	0	0	FALSE	FALSE	FALSE	0	0	0	0	Channel 8
Channel 9	0.0	0.00	0.0	0.39	9 Off	Off	0.0	0.00	0	0	0	0	0	FALSE	FALSE	FALSE	0	0	0	0	Channel 9
Channel 10	0.0	0.00	0.0	0.53	3 Off	Off	0.0	0.00	0	0	0	0	0	FALSE	FALSE	FALSE	0	0	0	0	Channel 10
Channel 11	0.0	0.00	0.0	0.66	5 Off	Off	0.0	0.00	0	0	0	0	0	FALSE	FALSE	FALSE	0	0	0	0	Channel 11
Channel 12	0.0	0.00	0.0	0.7	L Off	Off	0.0	0.00	0	0	0	0	0	FALSE	FALSE	FALSE	0	0	0	0	Channel 12
Channel 13	0.0	0.00	0.0	0.88	B Off	Off	0.0	0.00	0	0	0	0	0	FALSE	FALSE	FALSE	0	0	0	0	Channel 13
Move Cursor Over	readbac	ck setting	act	ual ee the C	orresponding C	hannel Dp					rei	adback se	tting						actual		

Using this panel, user can monitor,

- HV Board status
- HV channel DP values of selected HV board
- HV channel status
- HV channel error status
- Board and Channel trend pages





HV Board Settings Panel

Settings (dist_1 -	gem1;	#1)											
IANGE HV S	SETTI	NGS	Con	nected (Chambe	rs: -GEI	MINI 1 & ·	+GEMINI	2		HV Boa	rd ID: GE	E+1/1 HVBrd1 Status Monitor
ttings - Comn	non	Settings	- More										
BOARD setti	ngs —												
Operating N	Node	GEM 🔻		Apply		Board Click "Ap	d Dp: C ply" to appl	CAEN/GE	M_(CAEN_H	V_01/boa	urd00	Close
Channel sett	ings –	F 1/0	1/4	10	T1	DUD		TrinTim	_	Vmon	Imon	TO and Deals	
Channel 0	On v	300.0	0.0	0.255	0.000	0				200.0	0 3000	0.255	
Channel 1	Off -	0.0	0.0	0.000	0.000	0	0	0		338.3	0.8444	0.000	(uA) (uA) Set Divider Current to ALL
Channel 2	Off -	0.0	0.0	0.000	0.000	0	0	0		256.1	1 2444	0.000	
Channel 3	Off -	0.0	0.0	0.000	0.000	0	0	0		412.3	1 7555	0.000	(V) (V) Set Divider Voltage to ALL
Channel 4	Off -	0.0	0.0	0.000	0.000	0	0	0		854.2	2,2000	0.000	
Channel 5	Off -	0.0	0.0	0.000	0.000	0	0	0		891.2	2.6555	0.000	0 (uA) Set i0 to ALL
Channel 6	Off -	0.0	0.0	0.000	0.000	0	0	0		2145.2	2.7111	0.000	
Sum	(V)	300.0	(V)	(uA)	(uA)	(V/s)	(V/s)	(s)	M	5097.2	11.7110	(uA)	
Channel 7	Off -	0.0	0.0	0.000	0.000	0	0	0		0.0	0.0000	0.000	Save current
Channel 8	Off -	0.0	0.0	0.000	0.000	0	0	0		0.0	0.0000	0.000	SWITCH ALL ON settings to
Channel 9	Off -	0.0	0.0	0.000	0.000	0	0	0		0.0	0.0000	0.000	Load settings
Channel 10	Off -	0.0	0.0	0.000	0.000	0	0	0		0.0	0.0000	0.000	from file
Channel 11	Off -	0.0	0.0	0.000	0.000	0	0	0		0.0	0.0000	0.000	Load default
Channel 12	Off -	0.0	0.0	0.000	0.000	0	0	0		0.0	0.0000	0.000	values
Channel 13	Off -	200.0	0.0	0.000	0.000	0	0	0		0.0	0.0000	0.000	vlagA
Sum	(V	200.0	(V)	(uA)	(uA)	(V/s)	(V/s)	(s)	(v)	0.0	0.0000	(uA)	Click "Apply" to apply settings
o o 10 1					1.0						OFFLOTA	IDDV	

Using this panel, experts can change,

- All HV channel DP values of selected HV board
- HV Board operating mode
- Other features are same as Chamber Setting panel
- It will popup a message when user is going to apply changes, if relevant LV channel is off 53 CERN, Oct. 03, 2019 p. 53

M.Ressegotti



LV Board Status Popup Panel





24th GEM Workshop

LV Board Status Monitor Panel

🏟 Monitor (dist_1 - ge	em1; #1)								_				- 🗆 X
LV MONITOR	ING C	onnected C	hamber	s: +0	GEMINI 1 ,	+GEMIN	II 2 , +GE	MINI 3]			Setting	gs Close
-BOARD settings											ST	ATUS	
DOARD Settings	,					0vc 0	W HEM	Trin	C 4		s	tatus	Meaning
LV Board ID:	GE+1/1 LVI	Brd1							36	atus		0	off
Rd Status 1		Chan	T		Channel	$^{\circ}$ \bigcirc (\mathcal{O}	\bigcirc	1	on		1	on
Dustatus I	on	Snow	renap	age	Channel				4			2	ramping up
Board Dp:					Cildille				1	on		4	ramping down
CAEN/GEM CAEN LV	//branchControlle	er00/easyCrate	0/easyBoa	ard00	Channel	2 🗋 🤇			3			16	overvoltage
UNITS		,						<u> </u>	-			32	undervoltage
Dimension or	Unit	iMonRange = FA	LSE = High		Channel	3 🔴 🜔	$) \bigcirc$	\odot	9			64	external trip
parameter	Of the	ZCAdjust = FA	LSE = Dis					ă E				128	max v
Voltage	V	ZCDetect = FA	LSE = Off		Channel	4∪ (\mathcal{O}	\bigcirc	0	standby/of	ff	256	external disable
Current	uA	Ophidde - 17	LUC - Hee		Channel	\sim			0	المراجعة والمراجعة		1024	calibration error
Ramp up/down	V/s	Show Trend	Ipage		Chaimer	$^{\circ}$		\cup	0	standby/o		2048	unplugged
Triptime	S												
	Chamber Name	e VO	10	Vcon	Vmon	Imon	OnOff readBack	OnOff Actual	V1	11	TripTime	vMax SoftValu	e
Channel 0	GE+1/1/01/1	0.000	0.000	0.000	5.236	0.500	On	On	0.000	0.000	0	0	Channel 0
Channel 1	GE+1/1/01/2	0.000	0.000	0.000	0.000	0.278	Off	Off	0.000	0.000	0	0	Channel 1
Channel 2	GE+1/1/02/1	0.000	0.000	0.000	0.000	0.000	Off	Off	0.000	0.000	0	0	Channel 2
Channel 3	GE+1/1/02/2	0.000	0.000	0.000	0.000	0.000	Off	Off	0.000	0.000	0	0	Channel 3
Channel 4	GE+1/1/03/1	0.000	0.000	0.000	0.000	0.000	Off	Off	0.000	0.000	0	0	Channel 4
Channel 5	GE+1/1/03/2	0.000	0.000	0.000	0.000	0.000	Off	Off	0.000	0.000	0	0	Channel 5
		readbac	k setting		actual					readbac	k setting		
Nove Cursor Over "Channel Ø", "Cha	annel 1", etc. to See the Corr	responding Channel Dp					ON 📃	OFF/ ST	ANDBY	RAMPI	NG	ERROR	EXCLUDED

Using this panel, user can monitor,

- LV Board status
- LV channel DP values of selected LV board
- LV channel status
- LV channel error status
- Board and Channel trend pages 24th GEM Workshop

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🏟 Settings (dist_	1 - gem1; #1)												– 🗆 🗙
CHANGE LV	SETTINGS	Conn	ected (Chamber	s: +GE	MINI 1 ,	+GEMINI	2,+GEMI	NI 3 LV	Board	ID: GE+	1/1 LVBrd1	Status Monitor
BOARD setti	ings												
BdStatu	s <u>1</u>	on		Boa	rd Dp:	CAEN/O	GEM_CAE	N_LV/bran	chControll	er00/eas	syCrate0/e	asyBoard00	Close
Channel sett	ings							vMax]
	Chamber Name	OnOff	V 0	V1	10	11	TripTime	SoftValue	Vmon	Imon	10 readBack		Save current
Channel 0	GE+1/1/01/1	On 🔻	0.000	0.000	0.000	0.000	0	0.000	5.236	0.500	0.000		settings to
Channel 1	GE+1/1/01/2	Off 🔻	0.000	0.000	0.000	0.000	0	0.000	0.000	0.278	0.000	SWITCH ALL ON	Load settings
Channel 2	GE+1/1/02/1	Off 🔻	0.000	0.000	0.000	0.000	0	0.000	0.000	0.000	0.000		Irom me
Channel 3	GE+1/1/02/2	Off 🔻	0.000	0.000	0.000	0.000	0	0.000	0.000	0.000	0.000	SWITCH ALL OFF	Save current settings as default
Channel 4	GE+1/1/03/1	Off 🔻	0.000	0.000	0.000	0.000	0	0.000	0.000	0.000	0.000		Load default
Channel 5	GE+1/1/03/2	Off 🔻	0.000	0.000	0.000	0.000	0	0.000	0.000	0.000	0.000		values
			(V)	(V)	(A)	(A)	(s)	(V)	(V)	(A)	(A)		
		(V)	(/)	Set This V	oltage		0	(A)	Set i0 to	ALL		App Click "Apple" to a	aly apply settings
		(-7		to ALL	VU							Citck Apply to a	appry seconds
Move Cursor Over "D)rift", "G1Top", etc. to Se	e the Corre	sponding	Channel Dp				ON	OFF/ STAN	IDBY	RAMPING	ERROR EXC	LUDED

Using this panel, experts can change,

- All LV channel DP values of selected LV board
- Other features are same as Chamber Setting panel

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Main Frame, MAO and Crate Status







Chamber Include/ Exclude Panel

× ChamberSettings (dist_1 - gem1; #1) ENDCAP Plus ENDCAP Minus Supper Chamber Enable/ Disable Layer Status **Apply Changes** Clear Selection Select All GEMINI 1 Layer 1 Included Exclude 14131211109 Layer 2 Included Exclude 8 GEMINI 2 Layer 1 Excluded Include Layer 2 Exclude Included 2 6 **GEMINI 3** Layer 1 Excluded Include 11 L1 2 L2 Layer 2 Exclude Included 8 L1 L1 ENDCAP L2 L2 36 L2 **GEMINI 4** Layer 1 6 Included Exclude L2 Plus L1 L1 2 ò Layer 2 レ1 Included Exclude 2 GEMINI 5 Layer 1 Included Exclude Layer 2 Included Exclude **GEMINI 6** Layer 1 Exclude Included Included Layer 2 Exclude Included Excluded GEMINI 7 Layer 1 Included Exclude Close Laver 2 Included Exclude





FINE (dist_1 - gen)	1; #1)					_
ENDCAP Plus ENDC	CAP Minus					
Supper Chamber	Layer	Voltage	Current	Enable/ Disable	A Power Cycle LV	Clear Selection
GEMINI 1	Layer 1	5.236	0.500	Click here to select		
	Layer 2	0.000	0.278	Click here to select		
GEMINI 2					· · · · · · · · · · · · · · · · · · ·	
	Layer 2	0.000	0.000	Click here to select	23 23 C C C 10 10	11/1/2/2000
GEMINI 3		0.000	0.000	Excluded	1 9 4	12 2
	Layer 2	0.000	0.000	Click here to select		
GEMINI 4	Layer 1	0.000	0.000	Click here to select	L1 10 L2 Pl	
	Layer 2	0.000	0.300	Power Cycle	1 2 12	
GEMINI 5	Layer 1	0.000	0.178	Power Cycle	1 2 3/3/2/	TICO
	Layer 2	0.000	0.000	Click here to select		5 5 203134 4,
GEMINI 6	Layer 1	0.000	0.000	Click here to select	- <u>- 26 27</u>	28 29 5
	Layer 2	0.000	0.000	Click here to select		
GEMINI 7	Layer 1	0.000	0.000	Click here to select		
	Laver 2	0.000	0.000	Click here	✓ Cito	30

Features :-

- User can select multiple chambers at once
- Selections will be highlighted in red color
- Displays the current voltage, current and state on the table and graphical area
- Two separate tabs for ENDCAP Plus and Minus
- When "Power Cycle LV" is clicked, selected chambers will be power cycled one by one

Power cycling status for each chamber will be shown in "Messages" area



Features :-

- User can select multiple chambers at once
- Selections will be highlighted in red color in both table and graphical area
- User should set start and stop voltages, step size and hold time
- When "Start Scan" button is clicked, selected chambers will be scanned one by one
- User can abort the scan by clicking "Stop Scan" button





Features :-

- User can select multiple chambers at once
- Selections will be highlighted in red color in both table and graphical area
- User should set start and stop voltages, step size and hold time
- When "Start Scan" button is clicked, selected chambers will be scanned one by one
- User can abort the scan by clicking "Stop Scan" button

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