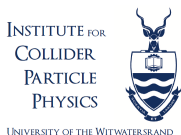


Tile GbE Switch Connection for Off-Detector Electronics with the Phase 2 Electronics System Upgrade

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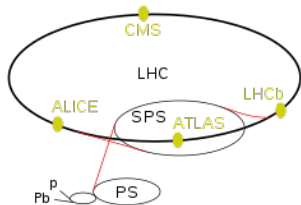
January 31st, 2020



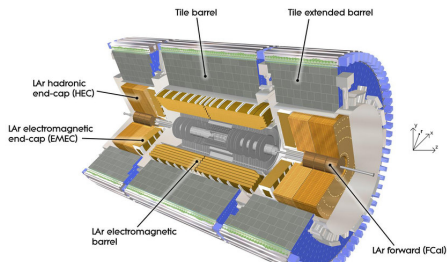
Overview

- 1 Project Context
- 2 ATLAS Tile Calorimeter
- 3 Electronics Read-out Chain
- 4 Tile Switch
- 5 Tile Switch Integration
- 6 Tile Switch Tasks
- 7 Current Production Progress
- 8 Project Timeline

Large Hadron Collider

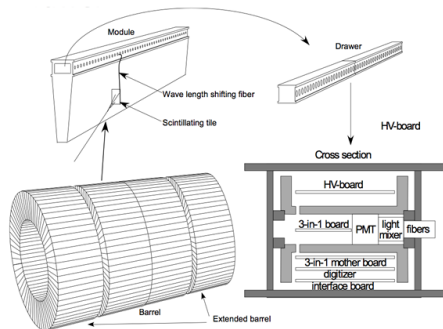


- Large Hadron Collider undergoing major upgrade
- Increase instantaneous luminosity \implies increased proton-proton interactions per second
- Increase data \implies electronics upgrade



ATLAS Detector

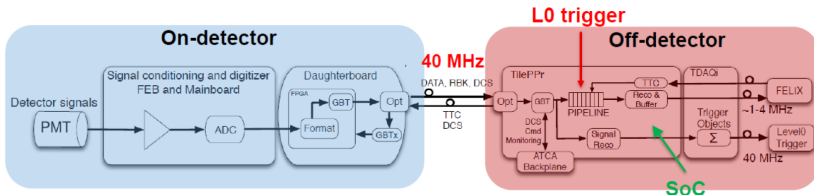
- Inner Detector
- Electromagnetic and hadronic calorimeter
- Muon Spectrometer



TileCal Detector

- 4 barrels \implies 2 central, 2 extended
- On-detector electronics
- Off-detector electronics

Electronics Read-out Chain



- On-detector electronics
 - Photo-Multiplier \implies scintillating tiles
 - Mainboard \implies FEB control, ADC
 - Daughterboard \implies high-speed communication
- Off-detector electronics
 - PreProcessor
 - Advance Telecommunication Architecture

Electronics Read-out Chain Upgrade

Upgrades to Tile Calorimeter

- Luminosity [1]
 - Current: $1 \times 10^{-34} \text{ cm}^{-2} \text{ s}^{-1}$
 - Upgrade: $7.5 \times 10^{-34} \text{ cm}^{-2} \text{ s}^{-1}$

Upgrades to Off-Detector Electronics

	Current	Phase 2 Upgrade
Total BW	165 Gbps	40 Tbps
Nb. Fibers	256	4096
BW/module	800 Mbps	150 Gbps
Nb. Boards	32	32
Nb. Crates	4 (VME)	4 (ATCA)
BW/board	6.4 Gbps	1.28 Tbps

Tile Switch

- Gigabit Ethernet Switch (TileGbE) mezzanine board
- 16 Ports \implies TilePPr
- Connect Phase 2 upgrade with CERN

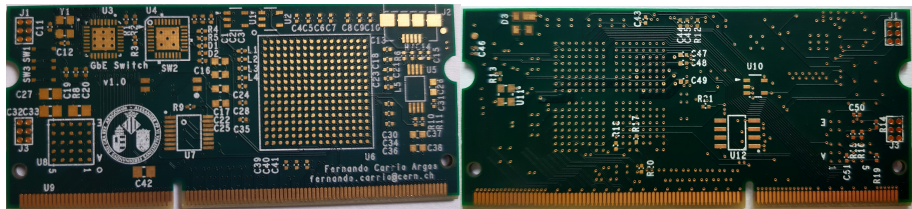


Figure: TileSwitch PCB

Tile Switch - 16 GbE Port Switch

- Network Switch

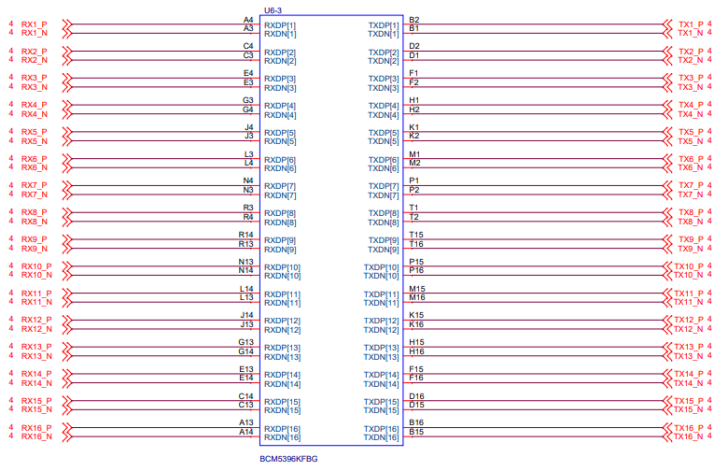
- Connects devices on a network
- Packet Switching
- Connection points for hosts

- Gigabit Ethernet

- IEEE 802.3
- Local Area Connection \implies CERN applicable
- Gigabit speeds

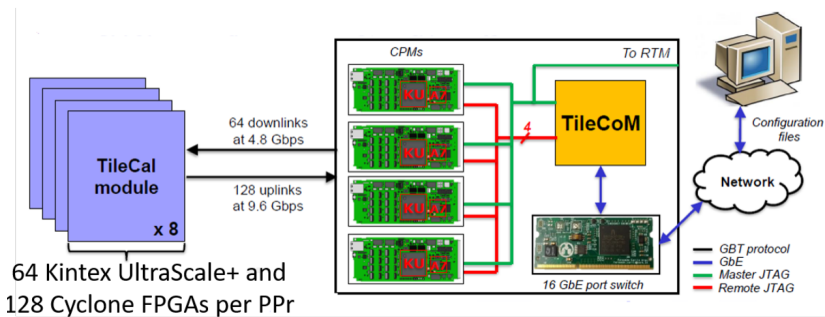
Tile Switch - Schematic

Tile Switch - 16 GbE Port Switch



Tile Switch Integration

- Off-Detector Electronics → CERN Network
- GBT Protocol
 - Decentralized network
 - Peer-to-peer optimized



Tile Switch Testing

- Test-bench preparation with motherboard
- Ethernet communication \implies 16 ports
- Power consumption and temperature measurements
- Firmware and software upgrades

Current Production Progress

- Manufactured in South Africa
- All documentation approved (iThemba labs)
- 6 PCBs in production (Trax Interconnet)
- Board population (Jemstech)
- Testing → CERN 2020

Project Timeline

- Feb 2020 - Receive the populated TileSwitch boards
- Feb 2020 - Test-bench with motherboard
- Mar 2020 - Ethernet communication → 16 ports
- Apr 2020 - Power consumption and Temperature measurements
- May 2020 - Firmware and software upgrades

Thank you!

Questions?



F Carrio and F Valero. “The PreProcessor Module for the ATLAS Tile Calorimeter at the HL-LHC”. In: *ATLAS Tile Calorimeter Collaboration* (Mar. 2019).