

The HPC Collaboration: CERN, SKAO, GÉANT PRACE



As you learned in the talk of Joseph Flix on Tuesday, the needs for computing in data intensive science are increasing dramatically

- The HL-LHC and SKAO face unprecedented computing challenges over the next 5-10 years

The needs for new resources are driving an extensive R&D effort

- Heterogenous hardware and High-Performance Computing (HPC)
 - There are big national investments in HPC
 - Improvements in heterogenous hardware are driving increases in capacity
- New methods and opportunities in AI/ML

New Computing Challenges

Upgraded Accelerator

- Higher Luminosity



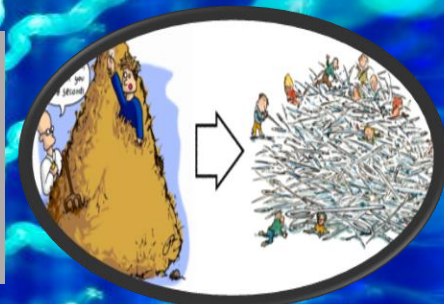
Upgraded Detectors

- Higher Granularity
- Higher Occupancy



Changing Filtering Paradigms

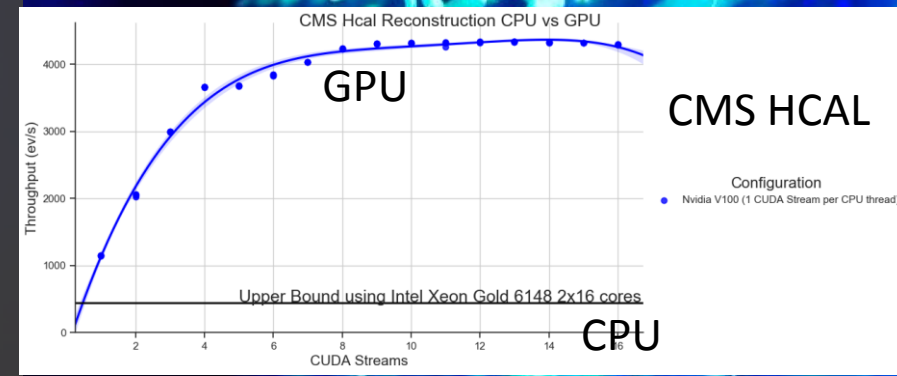
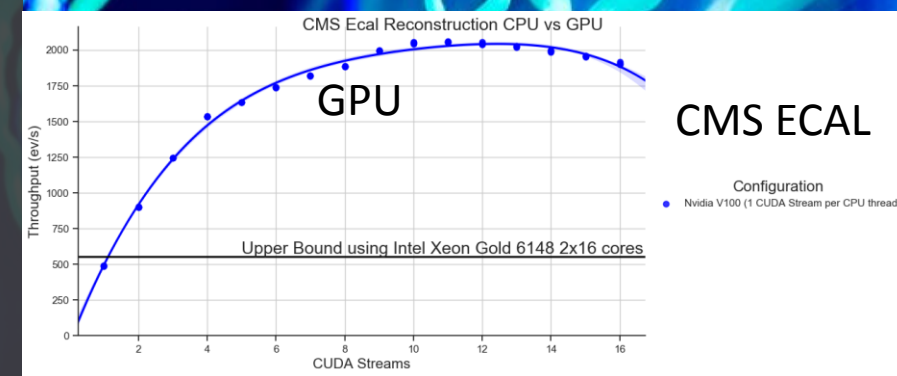
- Higher Sensitivity
- Higher Data Rates



New Computing Challenges



- Explorations in heterogeneous hardware are one of the drivers of innovation in computing
 - Large improvements recently in processing performance have come from offloading work to GPUs
 - E.g. CMS HLT (**ECAL, HCAL**) with GPUs gives ~factor 10 in improvement in throughput (on NVIDIA V100)
- Reengineering the software is necessary
 - Results in performance gains, easier adoption of heterogeneous hardware and better maintainability
- Experiments are encouraged to make use of supercomputers (growing to exascale)
 - Common across US, EU, and Asia
 - HPC sites are early adopters of new technologies and sources of expertise



Motivation for an HPC Collaboration

General purpose CPU performance increases have slowed

Optimized heterogenous architectures have evolved faster, **HEP Is investing heavily in development to use new hardware resources**

- **GPUs** are the most common
- **FPGAs** currently used mostly in low latency applications
- **TPUs** and specialized ASICs are available

General Purpose X86 processing resources

CPU

Code ported to Power

Low power highly parallelized

Accelerated Reconstruction And AI/ML

Accelerator

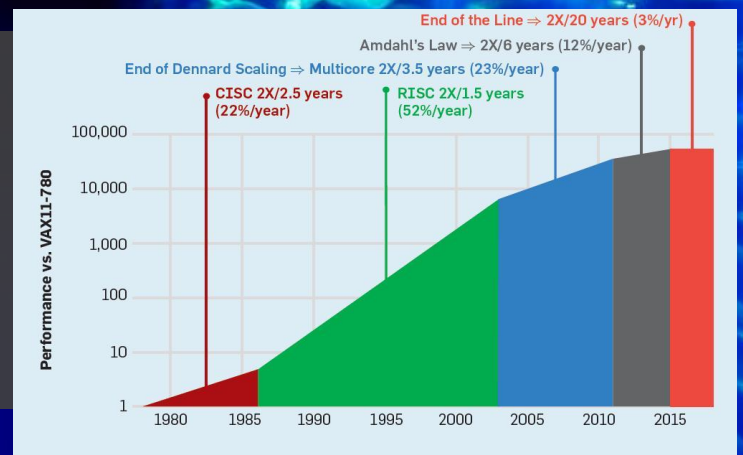
Low latency Online applications

		Intel	NVidia	AMD	FPGA	Other
CPU	Intel	Aurora	Cori Piz Daint Tsukuba Mare Nostrum		Tsukuba	
	AMD		Perlmutter JUWELS Booster	Frontier El Capitan LUMI		
	IBM		Summit Sierra Mare Nostrum			
	ARM		Wombat			Astra Fugaku

Amazon
Graviton2
Google Cloud TPU
Microsoft Azure
Intel DevCloud

Changing Computing Landscape

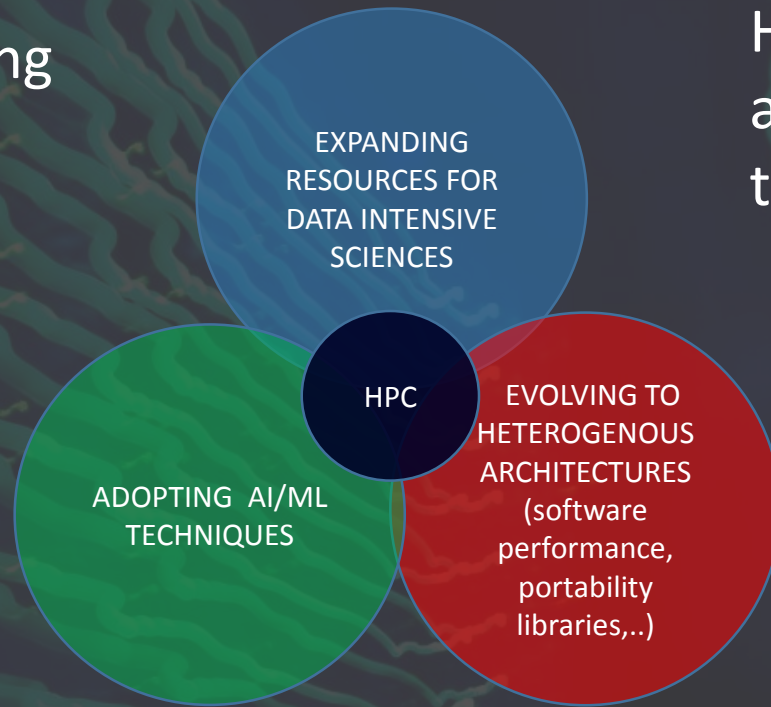
<https://eaom.aom.org/magazines/2019/12/234352-a-new-golden-age-for-computer-architecture/fulltext>



The current landscape is pushing HEP and other sciences to integrate HPC resources

HPC falls at the intersection of several important R&D areas

Engagement with the HPC Community can be a catalyst for progress



HPC Supercomputers will grow by a factor of 10 on the time scale of the HL-LHC

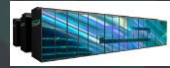
A thorough R&D program has been established

Unified programming models facilitate HPC adoption

High Performance Computing

Challenges

Software and Architectures



Supercomputers are early adopters of heterogenous architectures

Benchmarking and Accounting



Performance on diverse architectures needs to be understood

Data Processing and Access



Enormous data volumes to stage, process, and export

Authorization and Authentication



Strict cyber security

Runtime Environments and Containers



Resources are shared, environment needs to be brought with the workload

Provisioning



Resources allocated for periods of time through allocations

Wide and Local Area Networking



Processing and storage resources are separate

Challenges in HPC Integration

The common challenges for HPC integration into LHC Computer were described in an engagement document
<https://zenodo.org/record/3647548#.YBnA1y2cbVs>

As we adapt

- Our consortium is ideally composed
- HL-LHC and SKA have a burning physics need and in depth knowledge of the algorithms employed
- PRACE provide considerable experience in the system adaptation of software environments
- GEANT provides the infrastructure to take the computing to the many nodes that are needed to tackle the demand



Signature Ceremony

[From the HPC Collaboration Kick-off-Workshop](#)

CERN, SKAO, GÉANT, PRACE Consortium

PRACE | Tier-0 Systems in 2020



MareNostrum: IBM
BSC, Barcelona, Spain
#38 Top 500



Piz Daint: Cray XC50
CSCS, Lugano, Switzerland
#10 Top 500



NEW ENTRY 2018/2019
SuperMUC NG: Lenovo
cluster GAUSS @ LRZ,
Garching, Germany #13
Top 500

NEW ENTRY 2018
JUWELS (Module 1):
Atos/Bull Sequana
GAUSS @ FZJ, Jülich,
Germany #39 Top 500



NEW ENTRY 2018
JOLIOT CURIE: Atos/Bull Sequana
X1000; GENCI @ CEA, Bruyères-le-
Châtel, France #34 Top 500



MARCONI-100: IBM
CINECA, Bologna, Italy
#9 Top 500

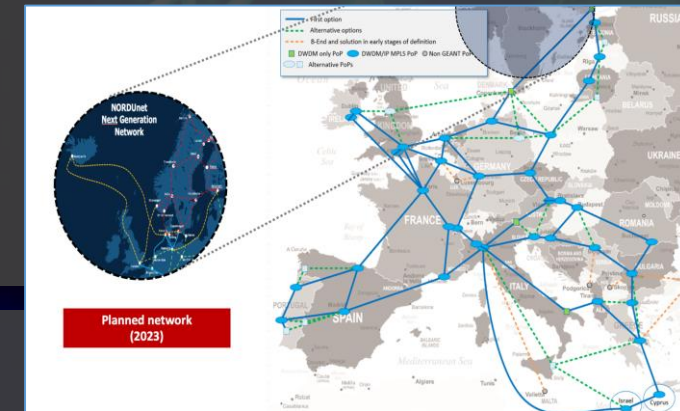
NEW ENTRY 2020
HAWK: HPE Apollo
GAUSS @ HLRS,
Stuttgart, Germany



Close to 110 Petaflops
total peak performance

5

The Partnership for Advanced Computing in Europe | PRACE



- Four areas of work have been identified as foundational. Progress will be evaluated by a series of common challenges and demonstrators
 - **Benchmarking**
 - **Data Access**
 - **Authentication and Authorization**
 - **Building a Common Center of Expertise**
- The next crucial step is to address the challenges through a **common program of work**
 - The **roadmap** is outlined on the next slides

The Four Pillars of the Collaboration

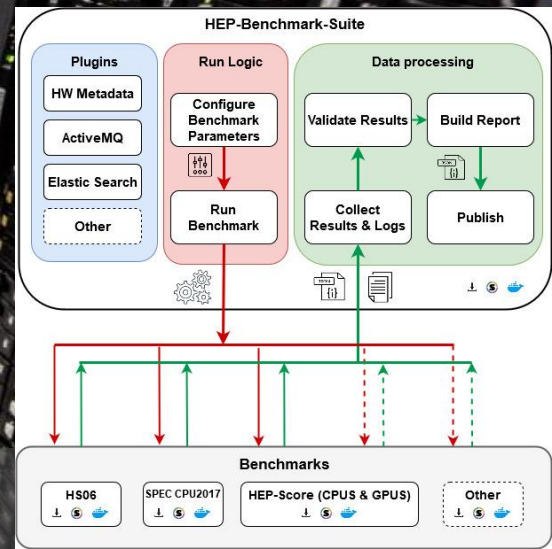
Benchmarking Activities

Open Question: How do we show we can effectively use HPC systems and account for contributions?

- PRACE-CERN-GÉANT-SKAO collaboration brings opportunity to expand capabilities using tools already developed for HPC sites by each community:
 - Unified European Applications Benchmark Suite (UEABS)- 13 workloads for HPC
- CERN is evolving the approach to benchmarking in HEP to embrace HPC:
 - Builds on experience from WLCG computing environment tools
 - Developed with secure, self-contained workload images (Singularity)
 - Assumes no privileges, no docker, limited/restricted node connectivity

PRACE Unified European Applications Benchmarking Suite

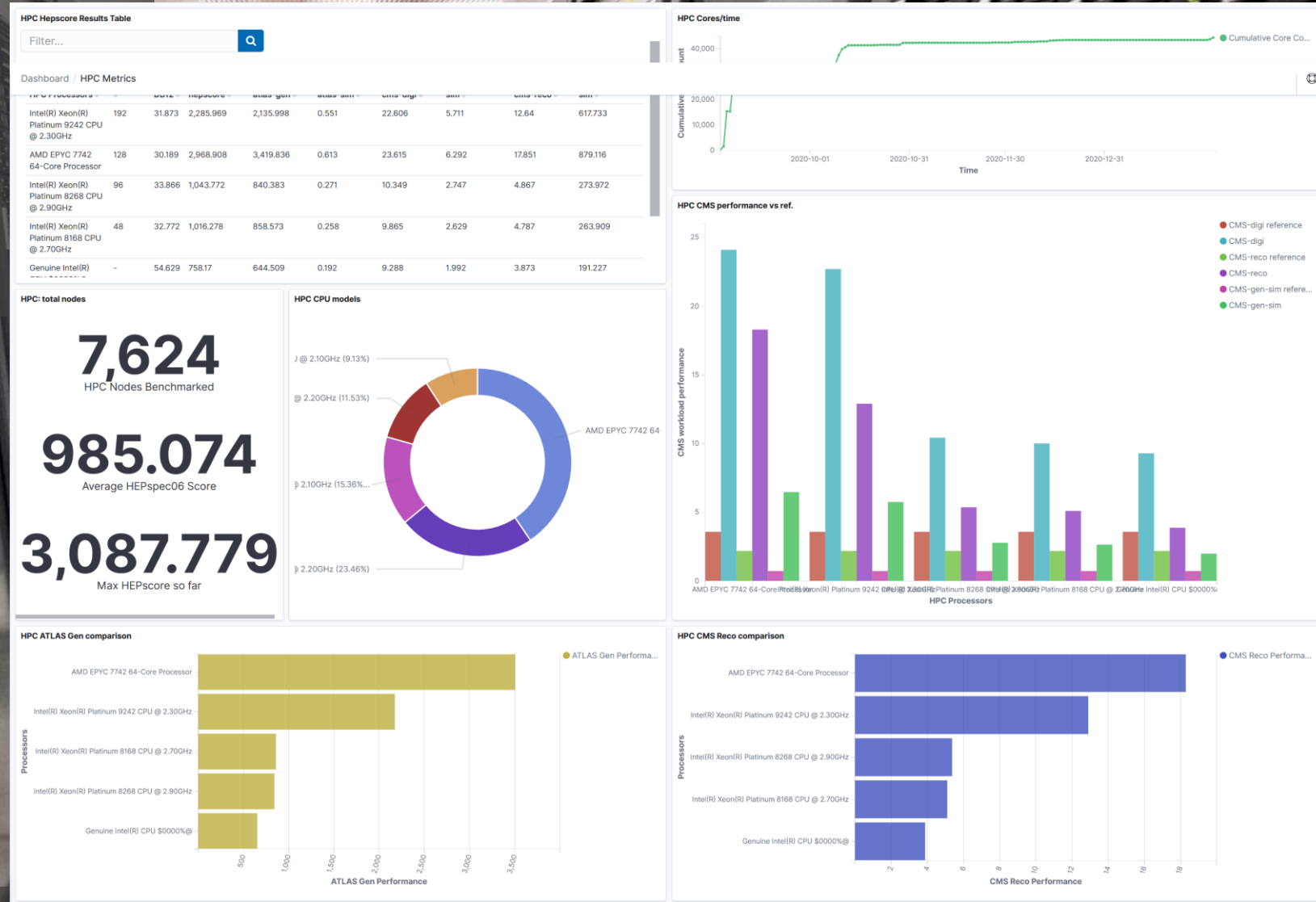
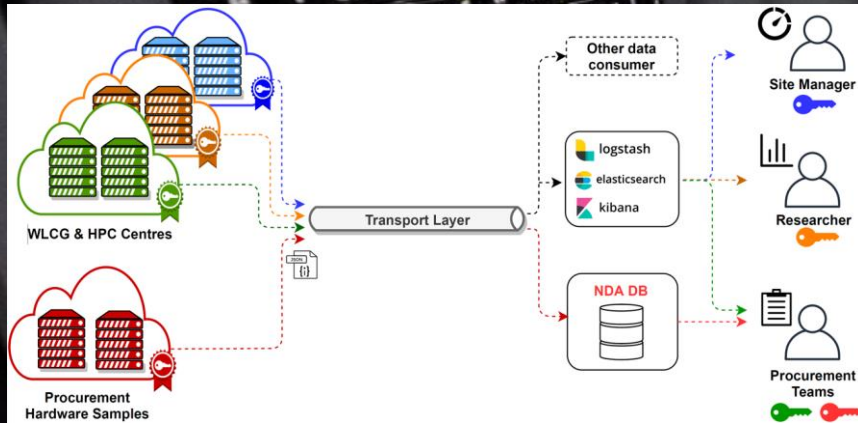
Application	Test Case	Per Dist-HSW	EWELS	MacBombrant	Shiu	Itazero Item	Iron-SKL	Per Dist-P100	Marconi-SKL	DAVID-PP	Marconi-BDW	DAVIDE	Itasca	Proof	Marconi-SNL	SDV	Iron-SNL
Alya	P	1.00	0.93				0.74						0.74		0.93	0.93	
	B	1.00	0.85				0.79						0.87		0.85	0.85	
Code_Saturne	A	0.96	1.00	0.68		0.96	0.49	0.14				0.14	0.14	0.19	0.19	0.19	0.19
	B	1.00	0.53	0.39		0.89	0.49	0.13					0.14	0.14	0.14	0.14	0.14
	C	1.00	0.57	0.39		0.89	0.49	0.13					0.14	0.14	0.14	0.14	0.14
CP2K	A	1.00	0.95						0.95			0.96	0.51	0.96	0.51	0.51	0.51
	B	1.00	0.95						0.95			0.96	0.51	0.96	0.51	0.51	0.51
	C	1.00	0.95						0.95			0.96	0.51	0.96	0.51	0.51	0.51
GADGET	A	1.00	0.84										0.84		0.84	0.84	
	B	1.00	0.95										0.95		0.95	0.95	
	S	1.00	0.89	0.74									0.89		0.89	0.89	
	M	0.44	0.24	0.32				0.14		0.14		0.14	0.14	0.14	0.14	0.14	0.14
GROMACS	A	1.00	0.77	0.89									0.77		0.77	0.77	
	B	1.00	0.88	0.73	0.63	0.34							0.88	0.22	0.22	0.22	0.22
NAMD	B	1.00	0.47	0.68	0.42	0.41							0.47	0.47	0.47	0.47	0.47
	G	0.89	1.00	0.82									0.89	0.89	0.89	0.89	0.89
PFARM	1	1.00	0.44	0.34	0.42	0.43							0.44	0.44	0.44	0.44	0.44
	2	1.00	0.43	0.46	0.79	0.43							0.43	0.43	0.43	0.43	0.43
NEMO	1	0.99	1.00	0.91	0.91	0.91							0.99	0.99	0.99	0.99	0.99
	2	1.00	0.99	0.90	0.73	0.90							1.00	1.00	1.00	1.00	1.00
QCD	A	1.00	0.99	0.90	1.00	1.00							1.00	1.00	1.00	1.00	1.00
	S	0.96	0.99	1.00	1.00	1.00							0.96	0.96	0.96	0.96	0.96
Quantum Espresso	A	1.00	0.99	0.90	1.00	1.00							1.00	1.00	1.00	1.00	1.00
	S	0.96	0.99	1.00	1.00	1.00							0.96	0.96	0.96	0.96	0.96
SPECFEM3D	A	0.82	0.37	0.23	0.28	1.00	1.00						0.82	0.82	0.82	0.82	0.82
	B	0.37	0.45	0.42	1.00	1.00	1.00						0.37	0.37	0.37	0.37	0.37



Benchmarking Demonstrator

Benchmarking Heterogenous architectures

- Multi-architecture as workflows become available (ARM, IBM Power)
 - GPU accelerators (NVIDIA, AMD)
- Automated collection and aggregation



<https://gitlab.cern.ch/hep-benchmarks>

HEP Workflows on HPC

Courtesy of D. Southwick

Open Questions: How do we bring data to process on supercomputers?. Can we use HPC sites to produce simulation and reconstruction datasets at exascale?

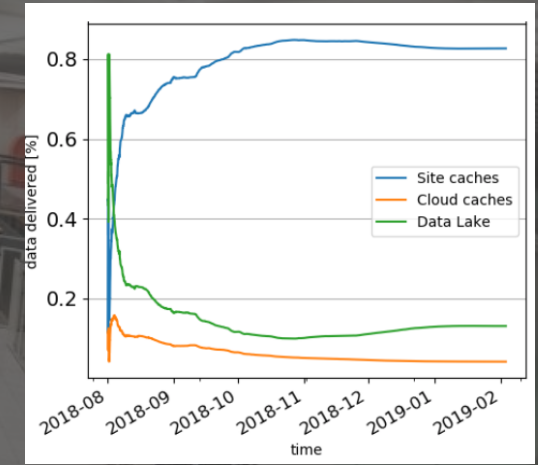
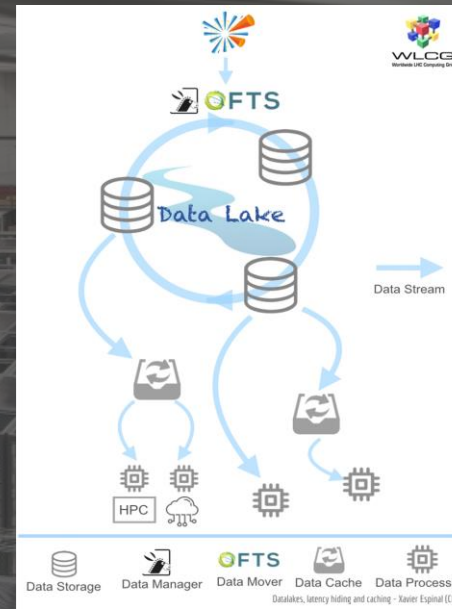
WLCG *Data Lake* model separates storage and processing functionality. HPC will be a part of the *Data Lake* model

- Relies on caching and networking
- EuroHPC will have significant WAN connectivity and disk space

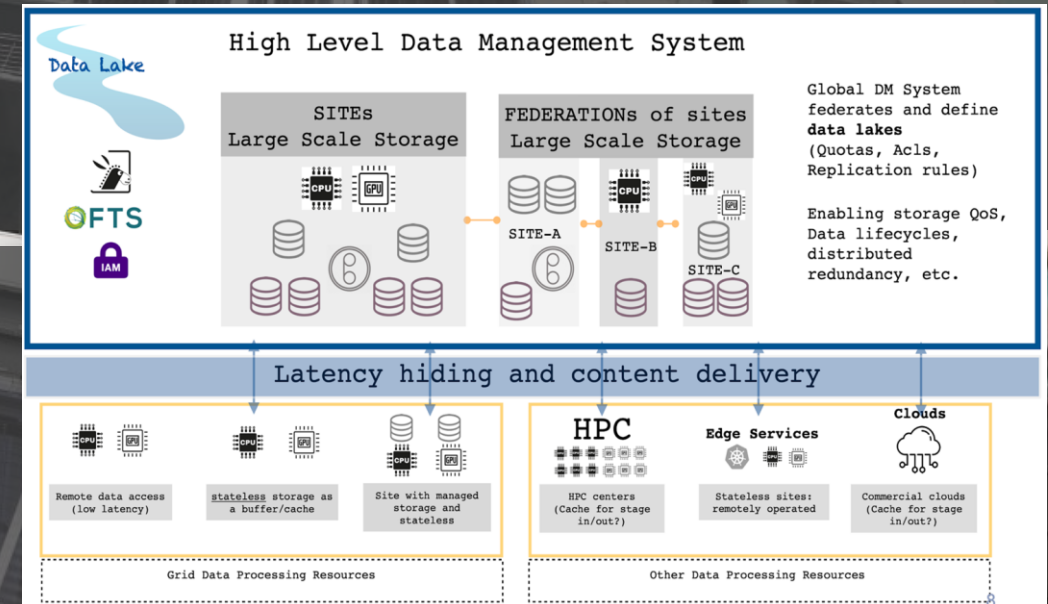
Technical Activities

Execute a series of data challenges to demonstrate the feasibility of the *Data Lake* model on a path to Exascale

Data Access – *Data Lakes*



Emulation of cache delivery vs. time including regional caches



- With the help of Summer of HPC Students we started an IO Benchmarking project on HPC
 - Evaluate and rank system performance.
 - Find limitations (more computing power isn't necessary more efficiency).
- The goal of this project:
 - Scale out I/O mock-ups using HPC benchmarks .
 - Evaluate and visualize performance metrics (e.g., bandwidth utilization).
 - Report performance under heavy dataflow load.



Courtesy of V. Khristenko

Studying Data Access



□ CSCS Grand Tavé¹

Specifications	
Model	Cray XC40 KNL
Compute Nodes	64 cores Intel(R) Xeon Phi @ 1.30GHz
Memory Capacity per Compute Node	96 GB, 16 GB HBM
Login Nodes	8 cores Intel(R) Xeon(R) @ 2.60GHz
Memory Capacity per Login Node	256 GB
Theoretical Peak Performance	436.63 TFlops
Max number nodes	164
Scratch capacity	/scratch/snx2000 904 TB

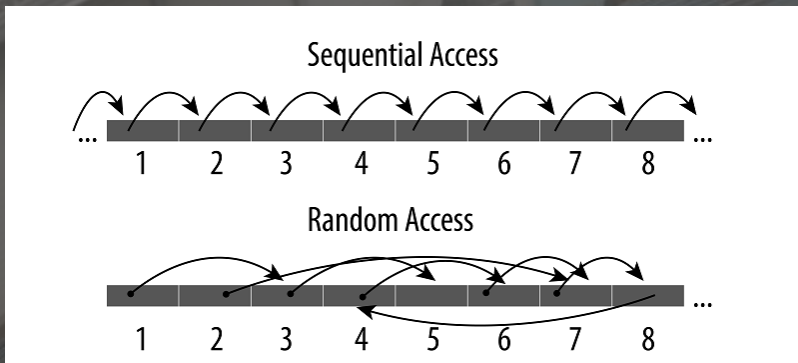
The \$SCRATCH space (/scratch/snx2000/\$USER) is connected via Infiniband interconnect to the system.



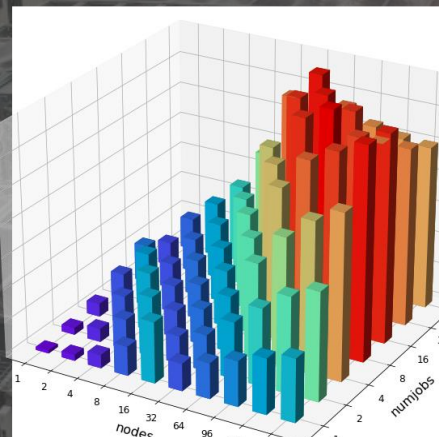
Courtesy of V. Khristenko

HPC Data Access Testbed

FIO (Flexible I/O tester) is an open-source synthetic benchmark tool
Generates various I/O type workloads (sequential/random reads and writes, etc.)

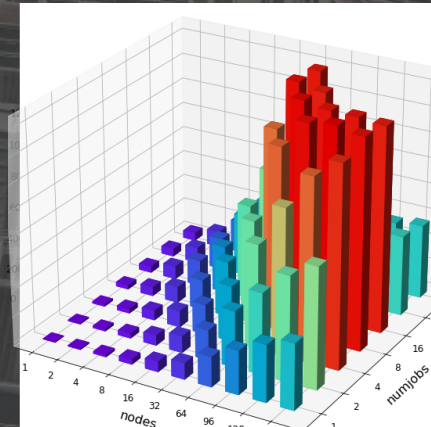


BS=16K
Peak: 1.37 GB/s



RANDOM

BS = 16K
Peak: 8.3 GB/s



SEQUENTIAL

Courtesy of V. Khristenko

Total Bandwidth vs Nodes

In addition to studying the file systems and storage performance, HEP is investigating how to improve the IO performance of the stored data files

HEP data is primarily stored as files, optimized for highly parallel HTC

- **ROOT** is the HEP analysis framework
- **ROOT** defines columnar data layout tailored for HEP: extreme throughput compared to alternatives
- <https://root.cern>

ROOT Challenges

- Maximize throughput I/O and optimize for HPC
- Optimize **persistent** data layout to facilitate conversion for CPU, GPU, SIMD ([LLAMA](#)), read patterns, and storage backend
- Ongoing R&D, bringing >4GB/s from off-the-shelf desktop to HPC
- **Scaling:** multi-threaded (>200 cores), distributed backends (dask / spark /...)



Courtesy of A. Naumann

HEP Data in HPC



ROOT

Data Analysis Framework

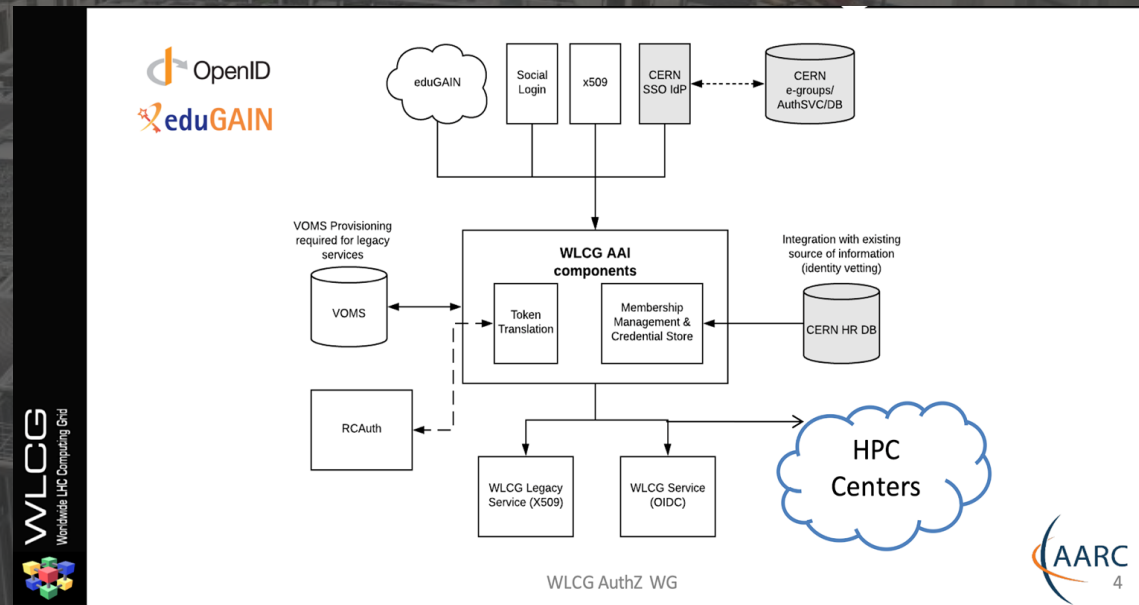
Open Questions

- Can we accommodate the strict cybersecurity requirement of HPC while enabling access by large international scientific collaborations?
- Can we capitalize on the efforts to modernize AAI infrastructure on the grid infrastructure?

Technical Program

- Take advantage of growing experience in federated identity management (e.g. from AARC and GN4-3 EU Projects)
- Test OAuth2 token-based finer grained authorization on HPC, trusting token issuers that belong to scientific collaborations

Token issuer for integration tests available



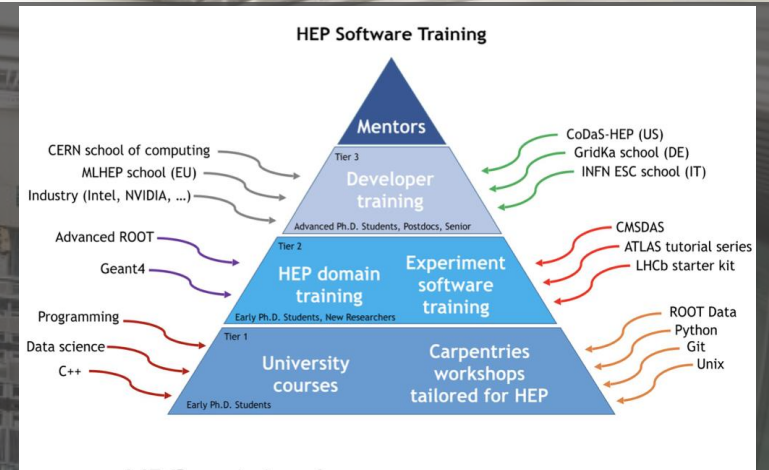
Authentication and Authorization Infrastructure

Open questions

- How to build a common Center of Expertise
- How to make training relevant, scalable, and sustainable in HEP
 - Community investment in software is large, turnover is high, huge user and developer base

Training Program

- Dedicated events throughout 2021 on accelerator programming and performance tuning
 - Large expertise in PRACE
- Joint summer internship programs between PRACE Summer of HPC/CERN openlab in Q2 2021, focused on demonstrators

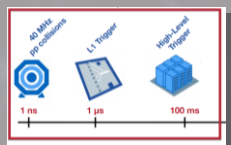


Building a Center of Expertise

Proven CERN capability ✓

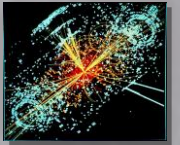
Use case specific

Fast ML



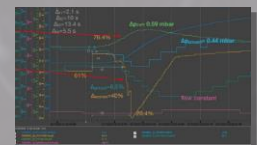
Ultra-fast on-edge inference under strict latency constraints

Anomaly detection



Object identification, classification, anomaly detection in big and noisy data sets

Industrial controls



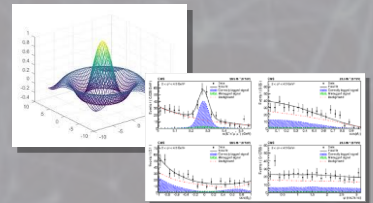
Machine efficiency and predictive maintenance with industrial control systems

Distributed computing



Optimization of distributed computing, storage, and networks; fast I/O for large files

Large scale, science grade data analytics and visualization



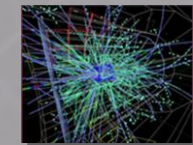
Cross use case

- Optimization and evaluation for science-grade precision of large data sets using advanced data analytics
- Data visualization, interactive plotting (e.g., statistical visualizations, uncertainties, distributions), model visualization
- Large-scale, quality-controlled CERN data as testbed/benchmark (e.g., single data set with 100m examples, >1TB)

In development, opportunity for joint R&D

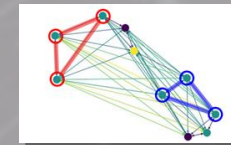


Simulation

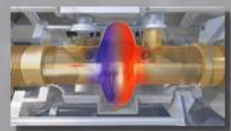


Simulation and reconstruction with generative DL for efficient computation

Graphs



Exploring Graph NNs for high-multiplicity problems with non-linear distances



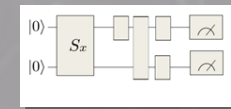
Determining optimal machine design and component configuration

ML in Robotics



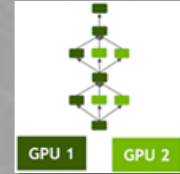
Remote maintenance and safety with autonomous robots and computer vision

Quantum ML



Research quantum algorithms to solve pattern recognition, classification and generation problems

Computing parallelization

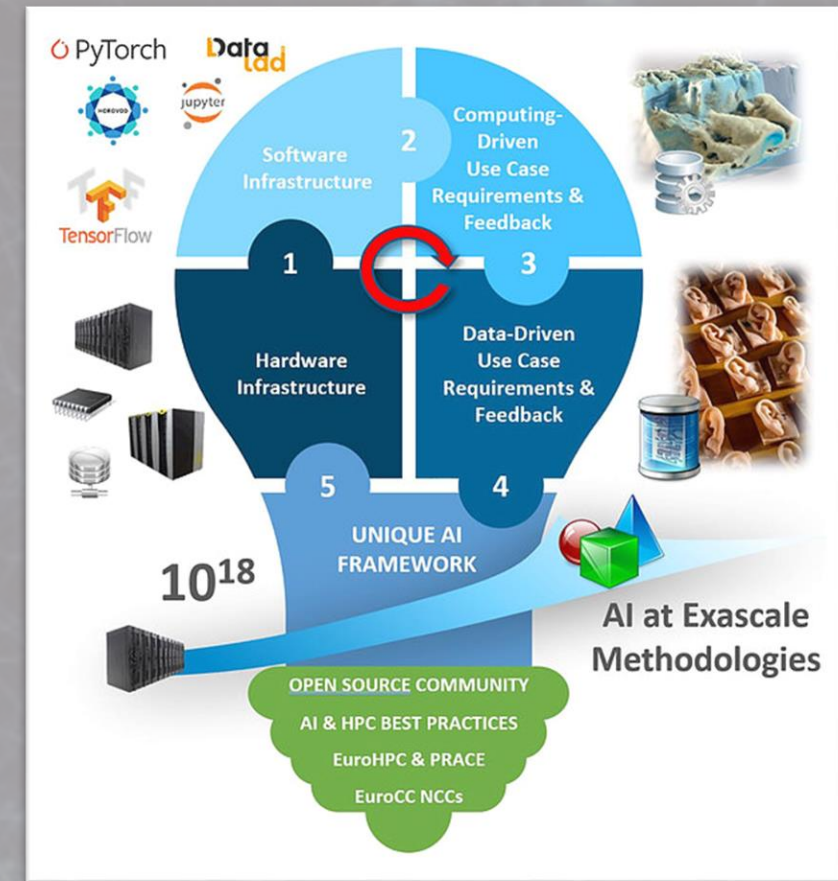
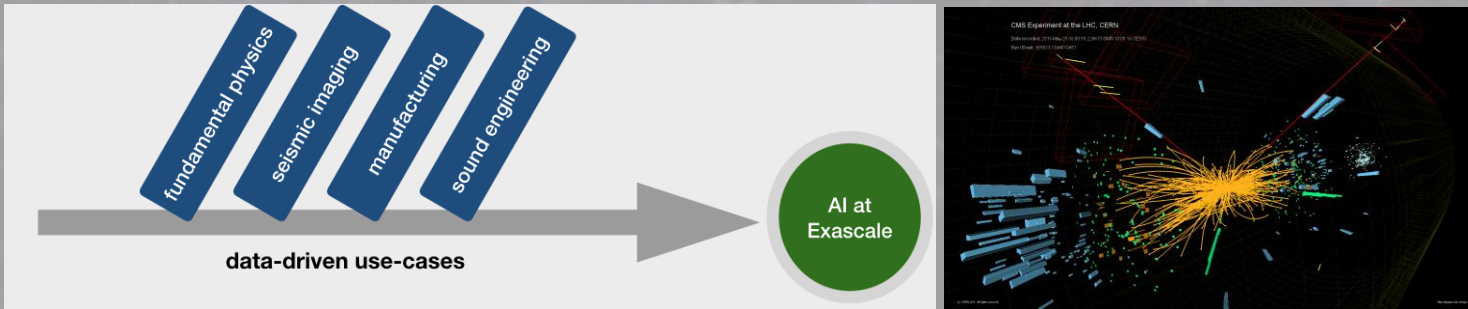


Training and optimization of complex NNs on parallelized GPU infrastructure

Progress on AI/ML Capabilities

Launched in January the RAISE Center of Excellence enabled researchers from science and industry to develop novel, scalable Artificial Intelligence technologies towards Exascale along representative use-cases from Engineering and Natural Sciences

CERN is leading the leading the data driven use-cases



AI/ML Projects



Testbeds are critical resources for development progress

- Representative hardware architectures, network structures and scale
- Some of the PRACE Tier-0 testbeds will be accessible in Q1 2021 through the collaboration for executing the working groups demonstrators
 - Benchmarking
 - Data access
 - AAI

PRACE | Tier-0 Systems in 2020



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Testbeds

Collaborations

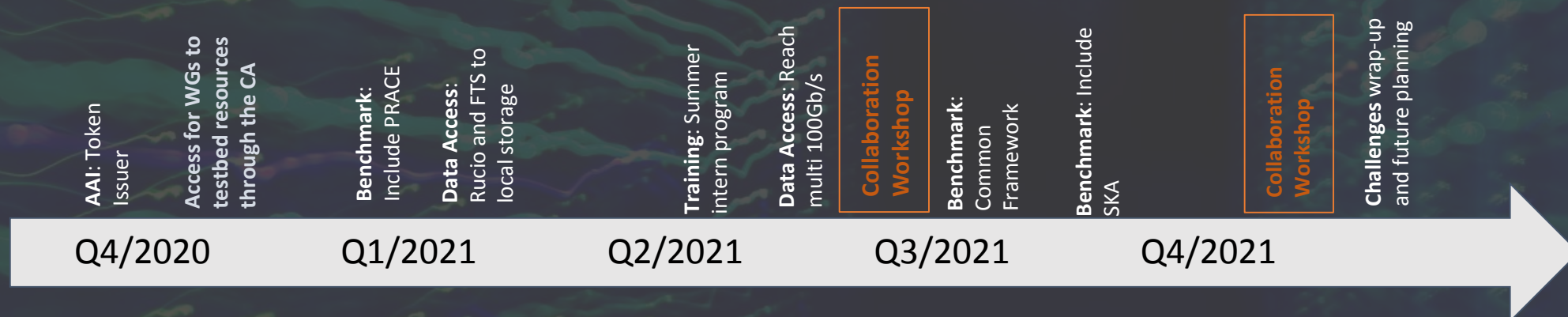
HPC Collaboration



Maria Gironi, CERN openlab Technical Workshop 2021

Participation on the path to exascale

- All stakeholders have engaged and are expected to contribute to the fundamental tasks of **effectively** HPC systems, securely accessing them and intensively accessing data
 - Additional contributions are very welcome
- The next months are crucial to demonstrate collaborative use of HPC systems
 - Access to testbeds proving technical benefits
- Leveraging experience from PRACE and GÉANT as centers of expertise is valuable to our community
 - We will provide application knowledge for a joint program on software optimisation on HPC systems



Towards a Successful Collaboration